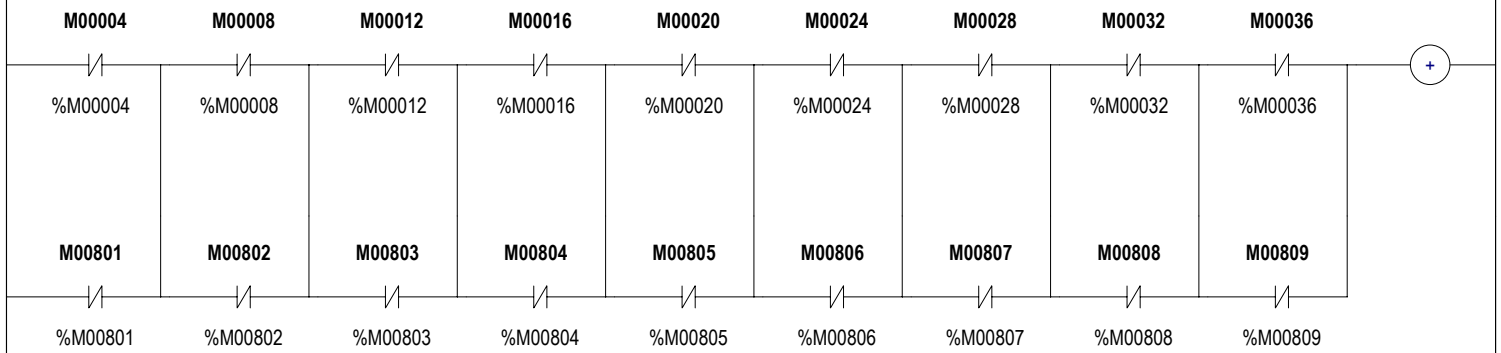
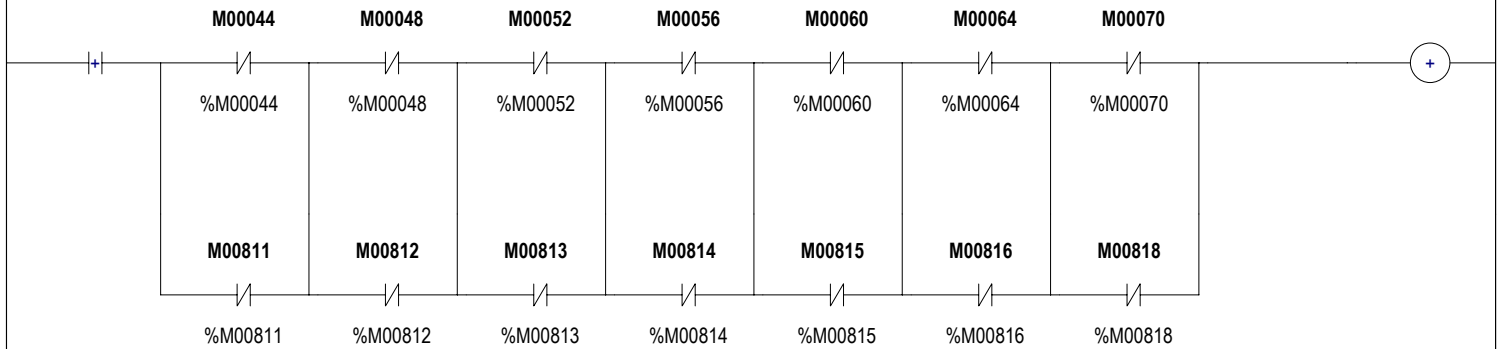


2



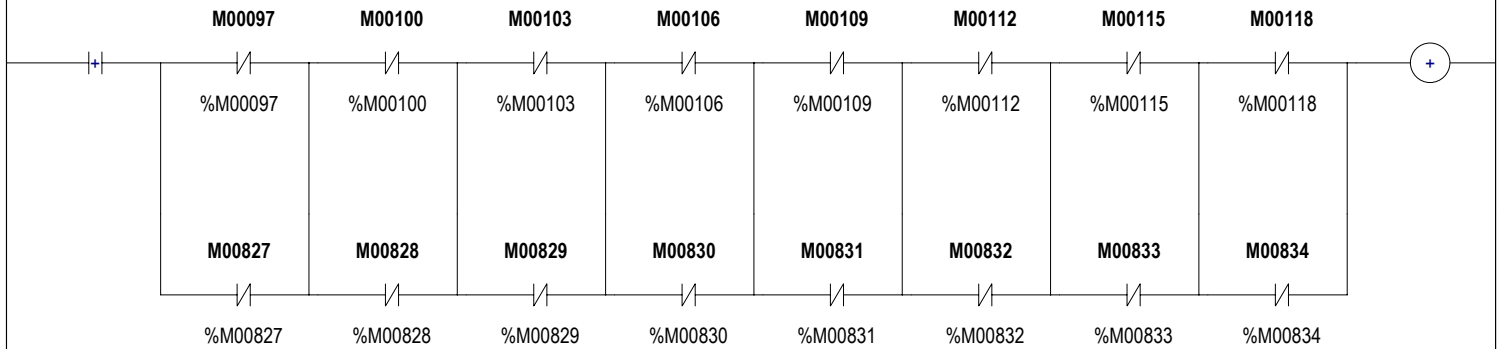
3

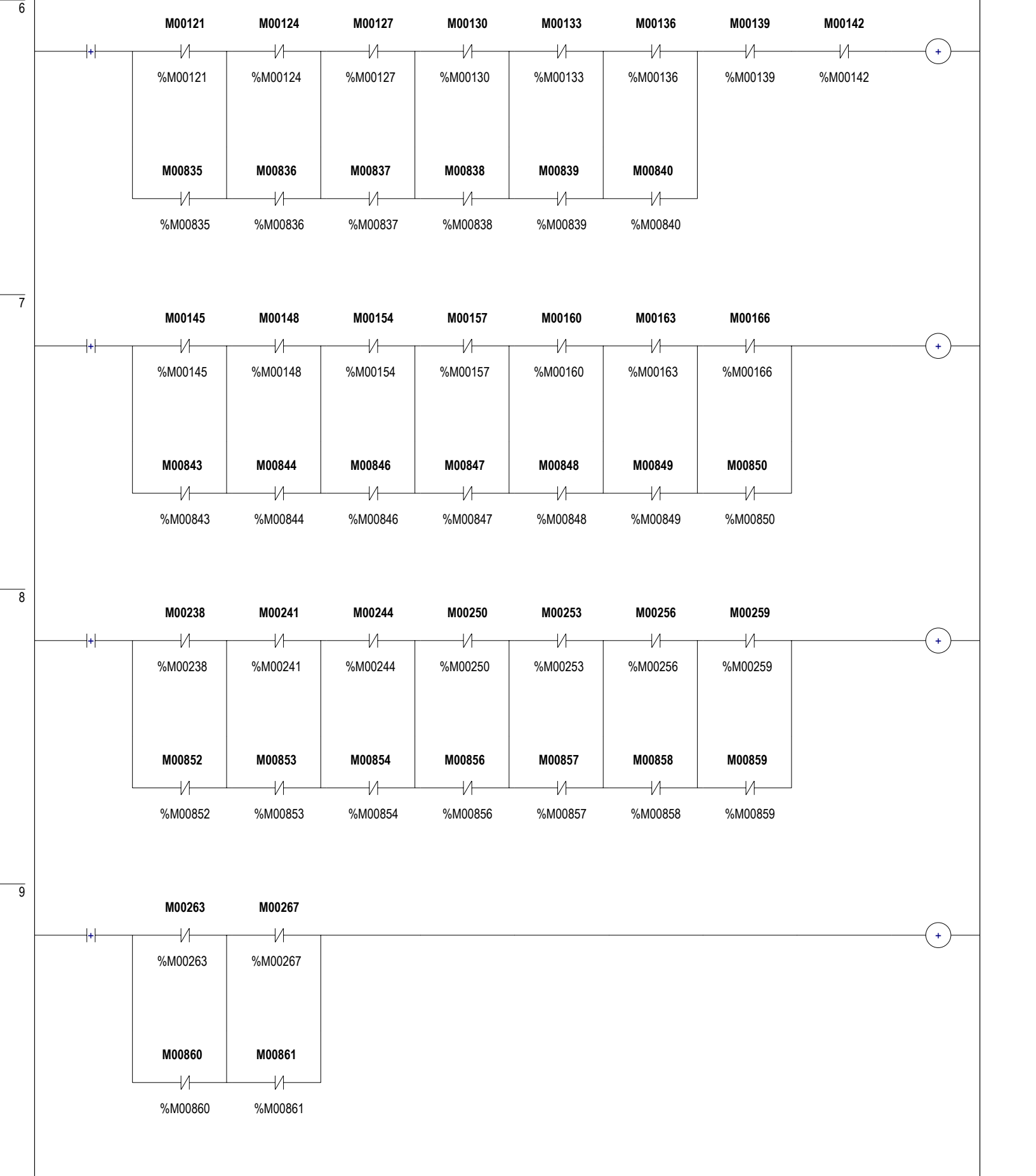


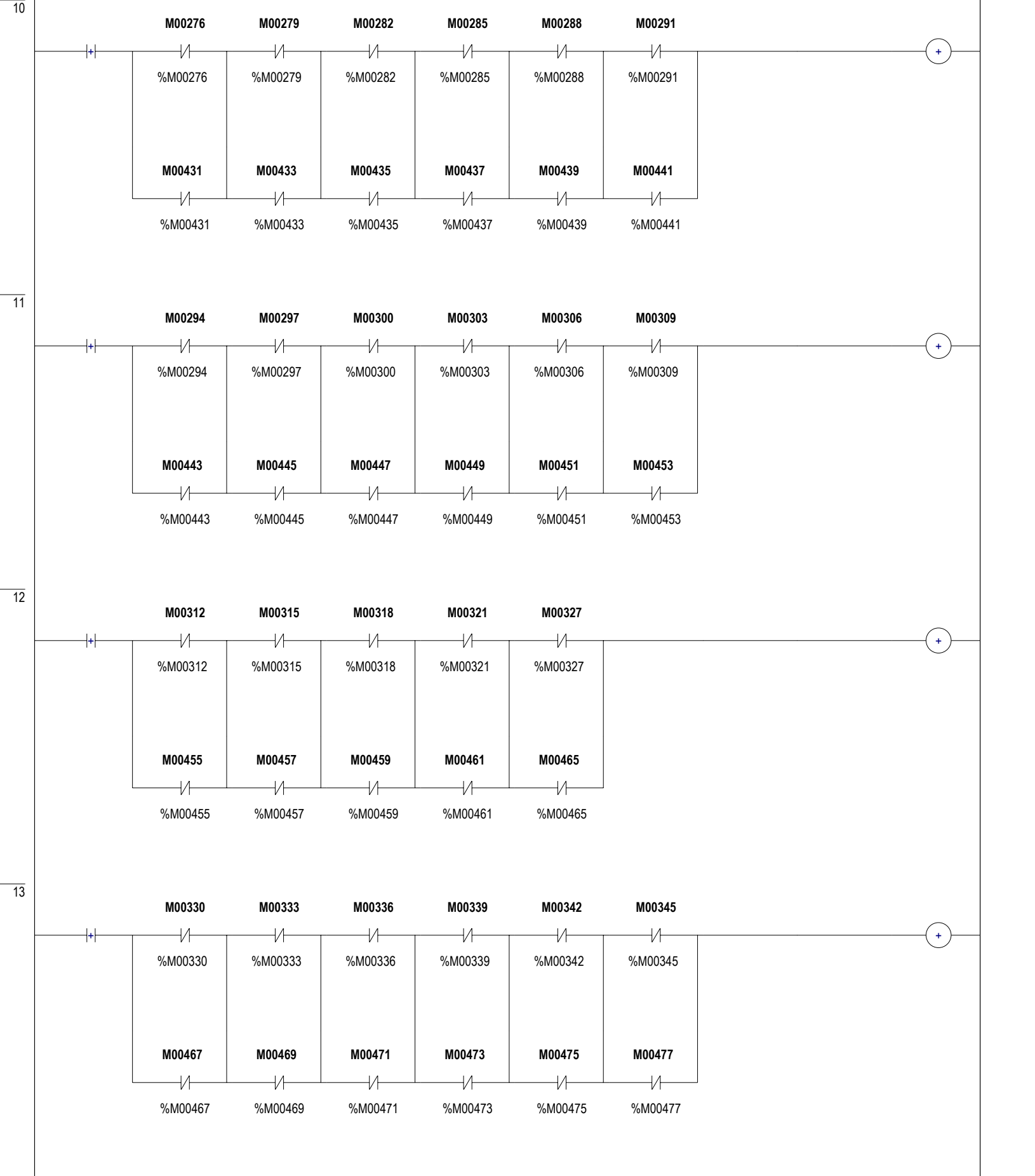
4

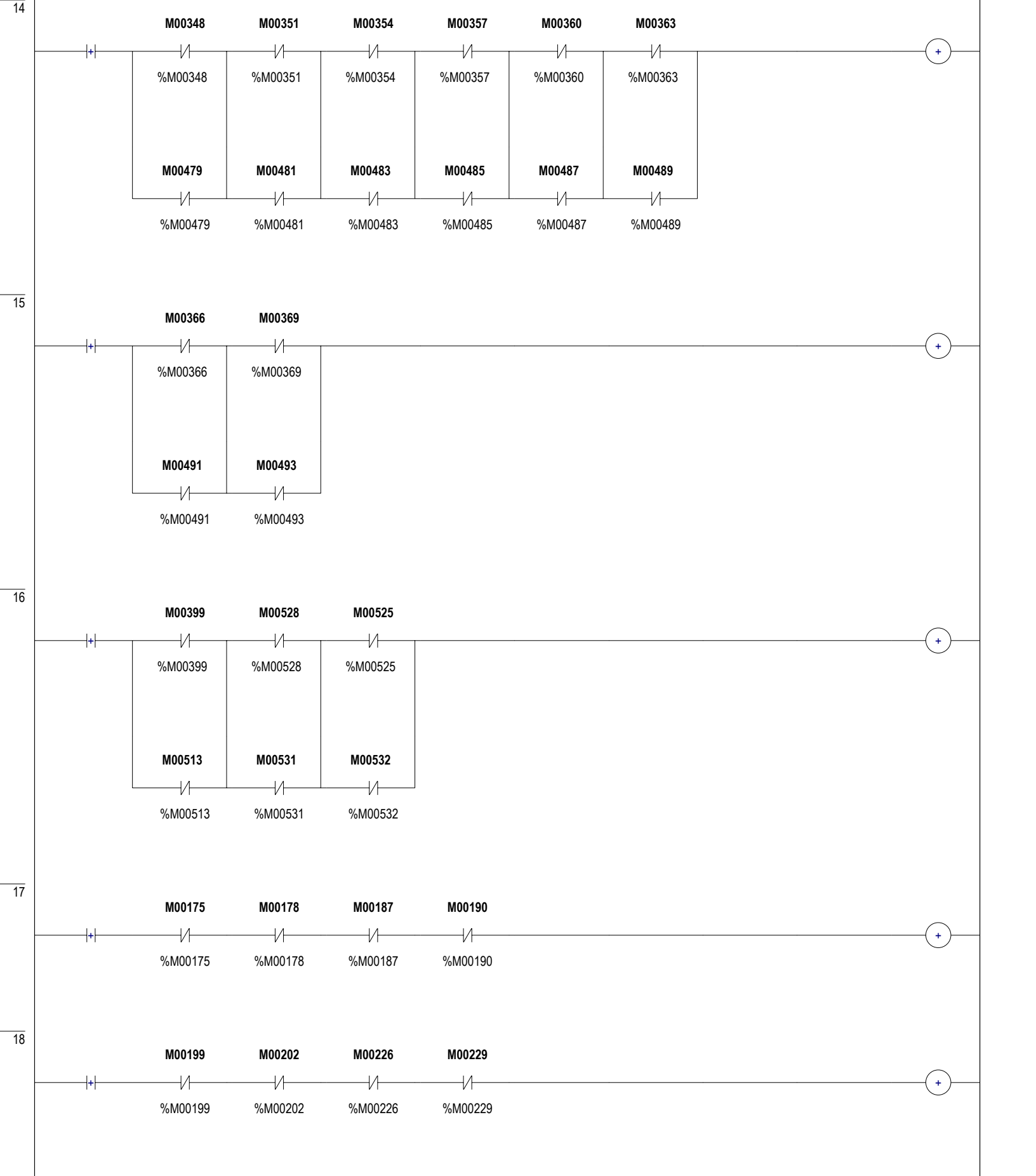


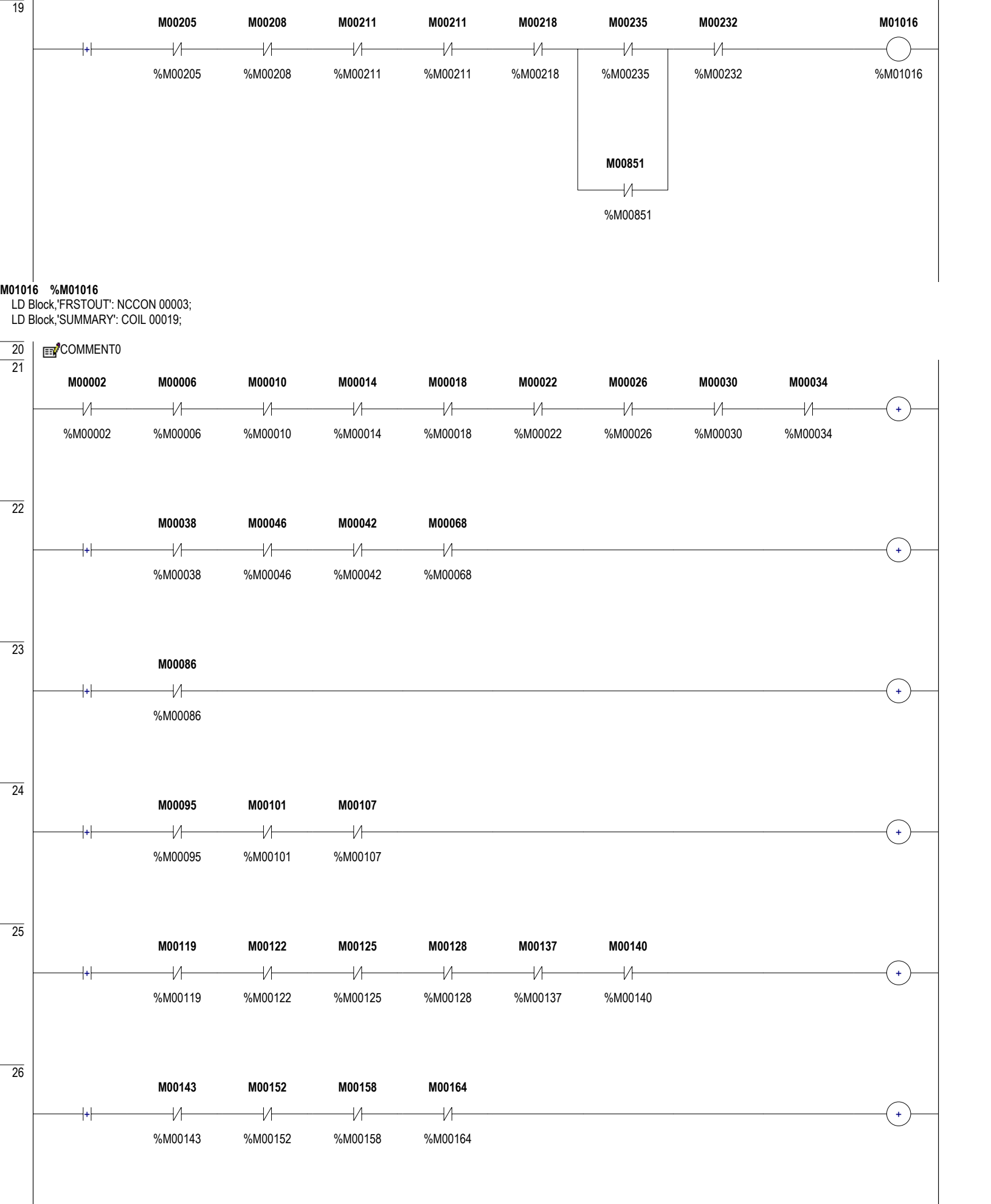
5



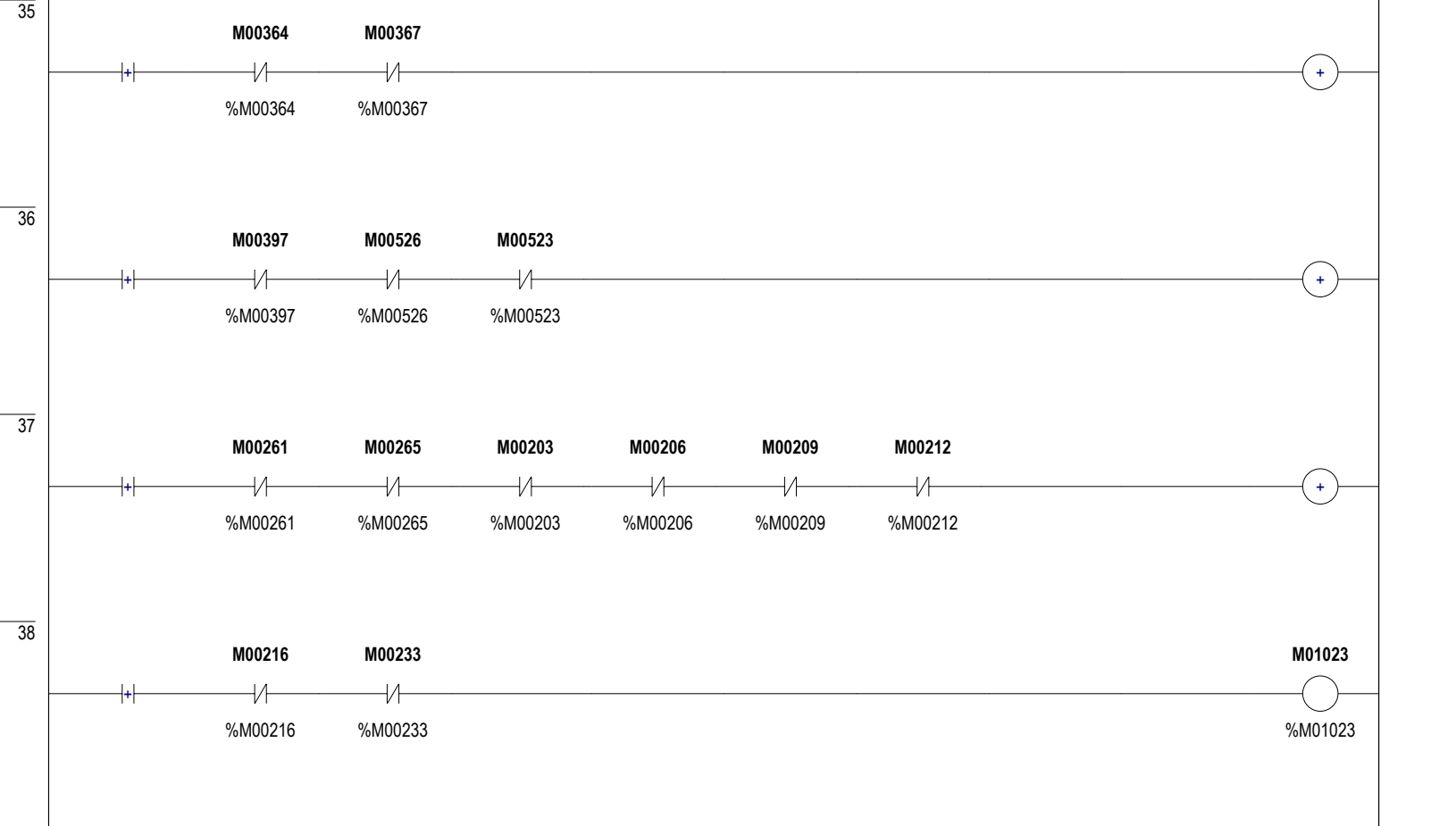










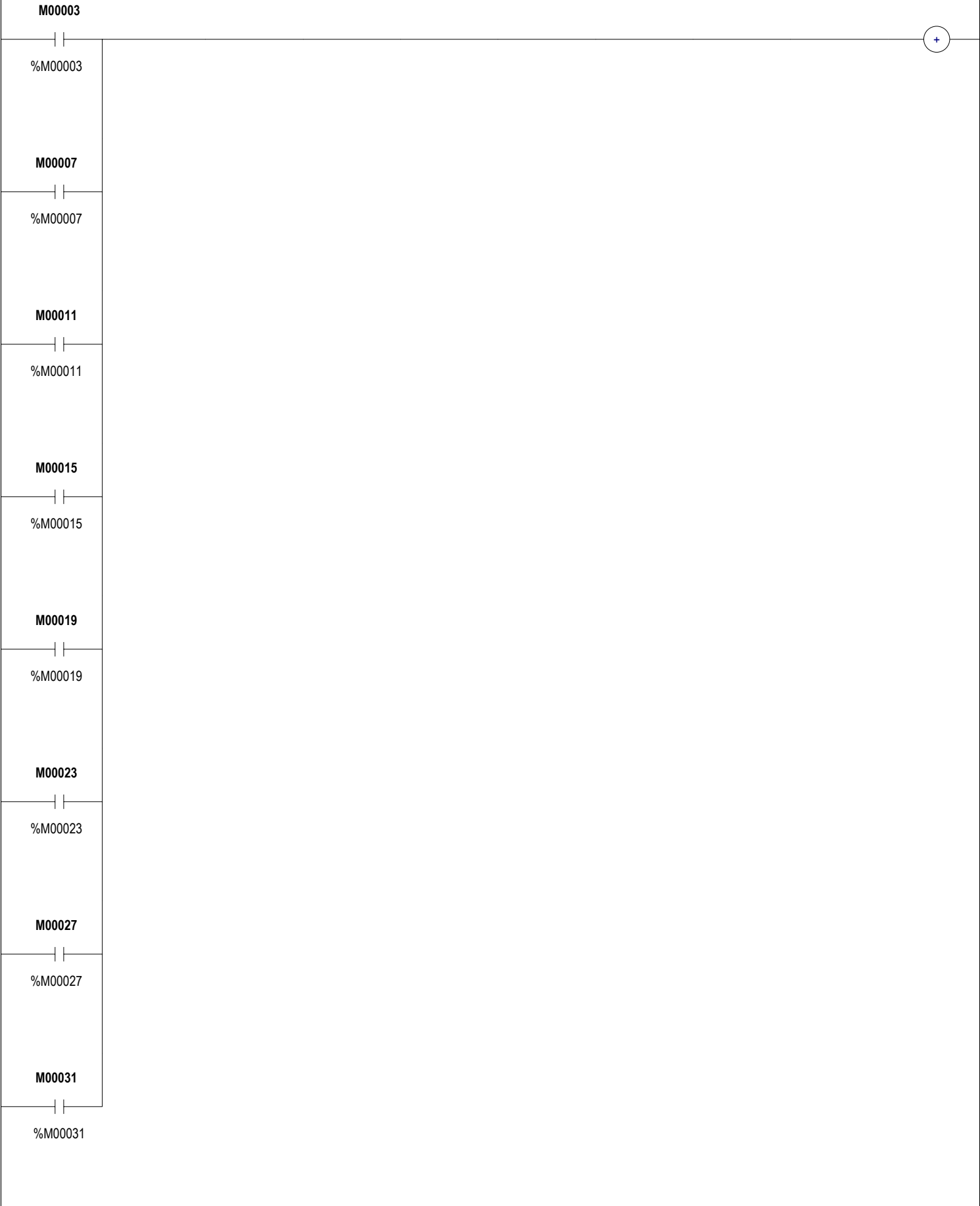


**M01023 %M01023**

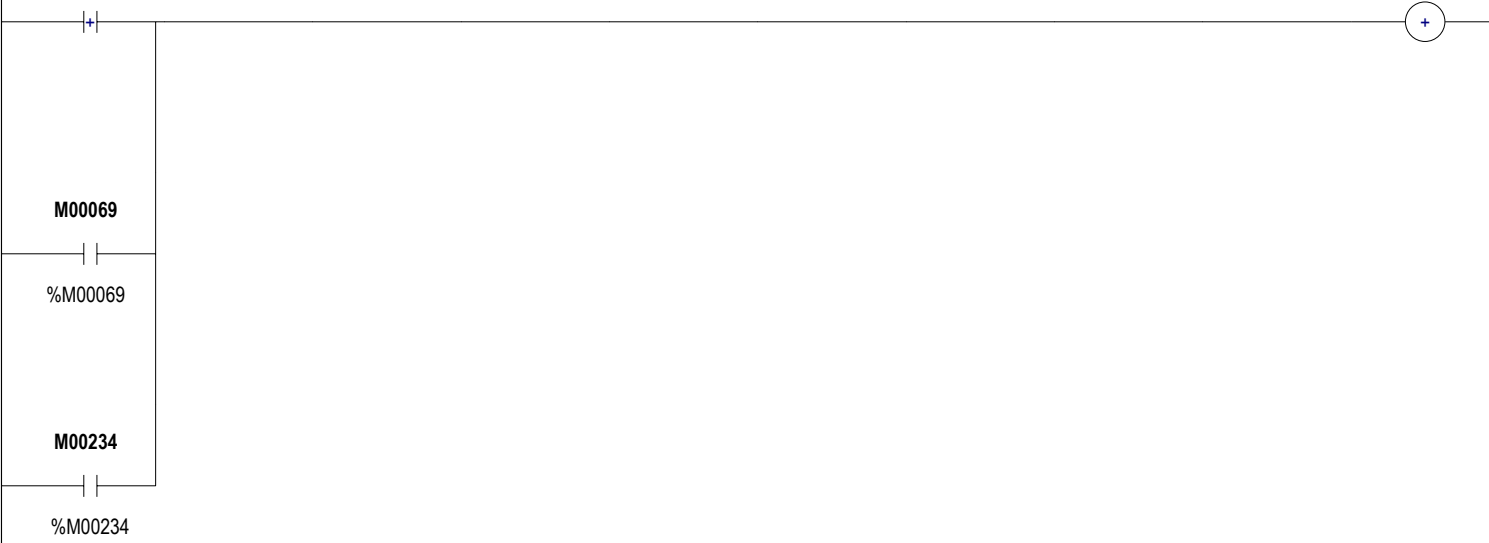
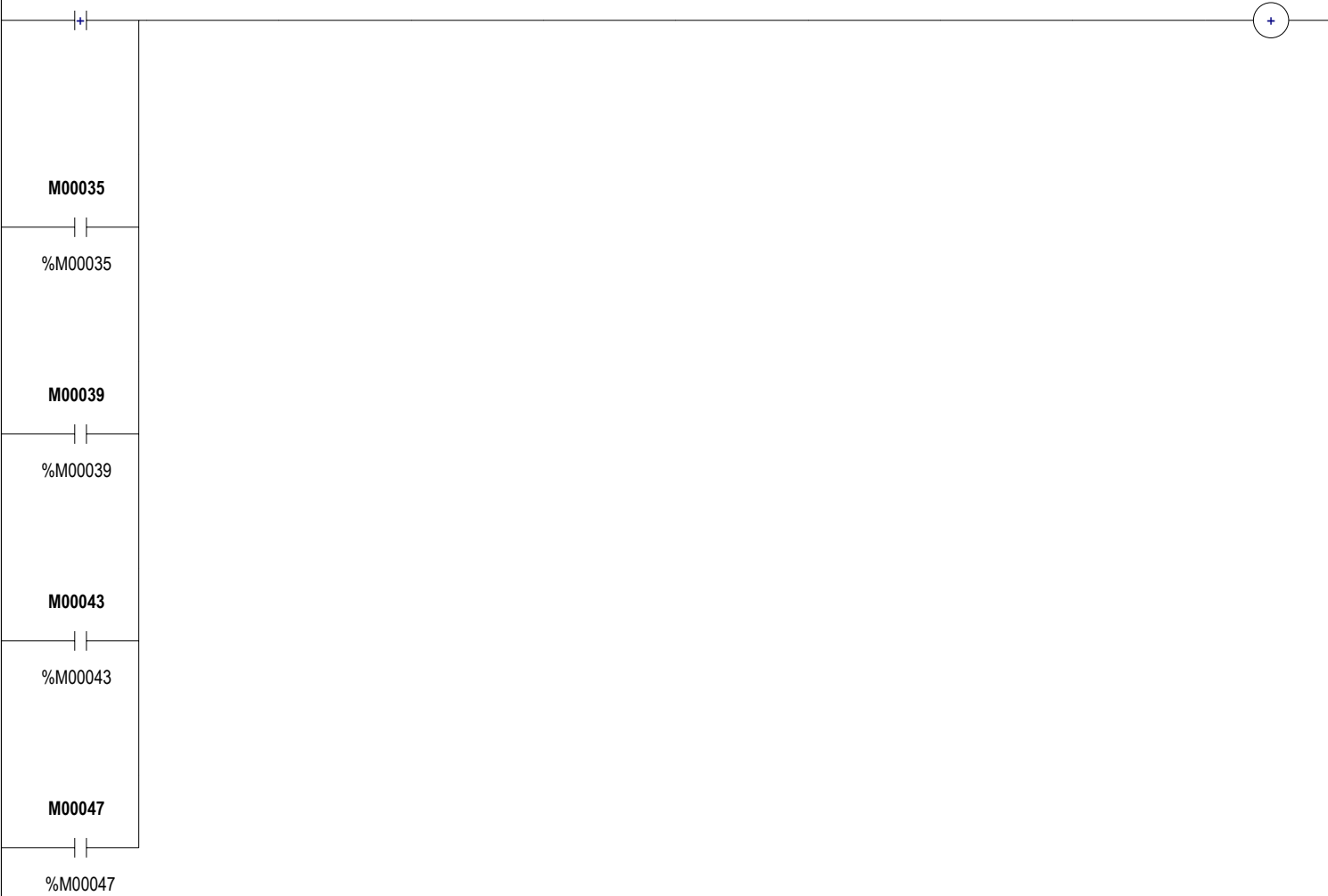
LD Block,'FRSTOUT': NOCON 00019, 00025, 00030, 00036, 00041, 00047, 00052, 00058, 00063, 00069, 00074, 00080, 00085, 00091, 00096, 00102, 00107, 00112, 00116, 00120, 00124, 00128, 00132, 00136, 00140, 00144, 00148, 00152, 00156, 00160, 00164, 00168, 00172, 00176, 00180, 00184, 00188, 00192, 00196, 00200, 00204, 00208, 00212, 00216, 00220, 00224, 00228, 00232, 00236, 00241, 00245, 00249, 00253, 00259, 00264, 00269, 00274, 00279, 00284, 00289, 00294, 00299, 00304, 00309, 00314, 00319, 00324, 00329, 00334, 00339, 00344, 00349, 00354, 00359, 00364, 00369, 00377, 00385, 00393, 00401, 00409, 00417, 00425, 00433, 00441, 00447, 00452, 00457;

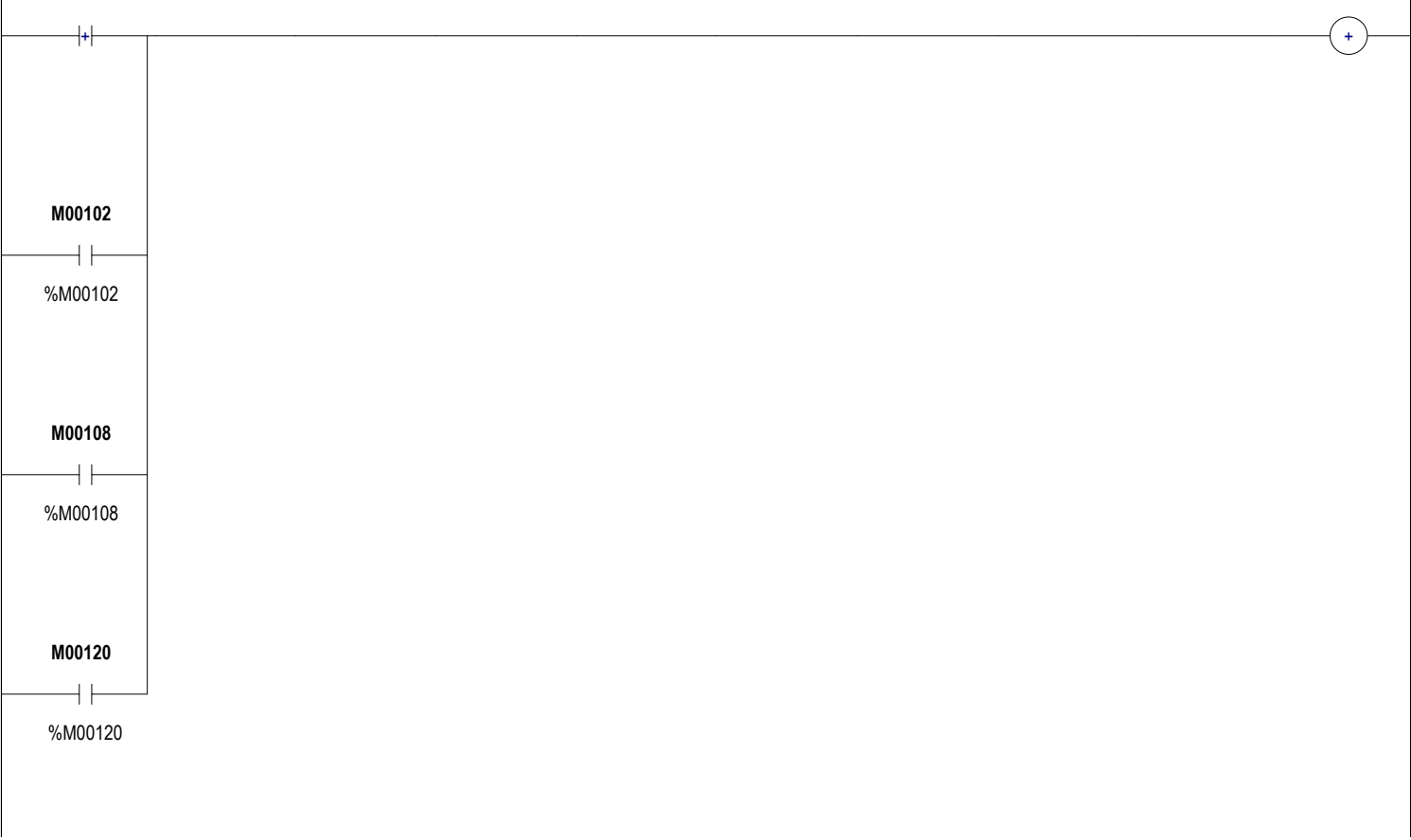
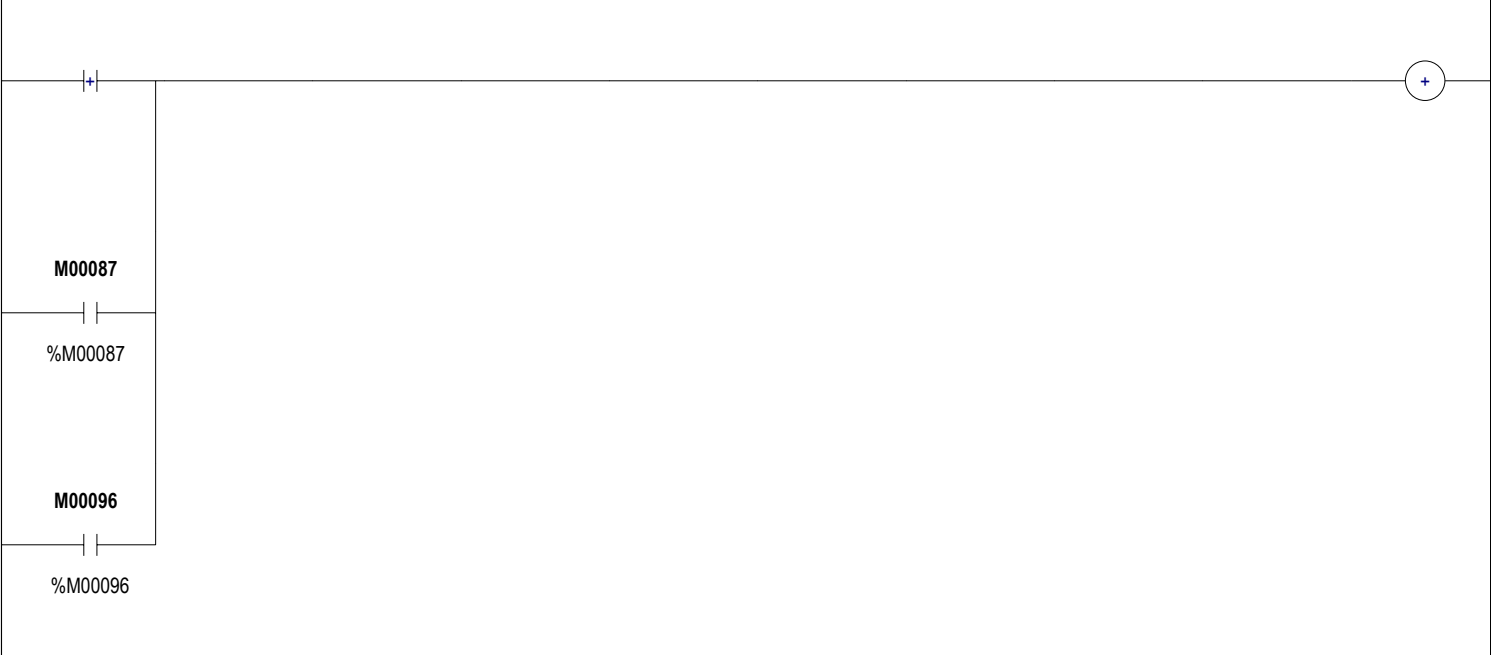
LD Block,'VALMALF': NOCON 00004, 00009, 00014, 00019, 00024, 00029, 00034, 00039, 00044, 00049, 00054, 00059, 00064, 00069, 00074, 00079, 00084, 00089;

LD Block,'SUMMARY': COIL 00038;

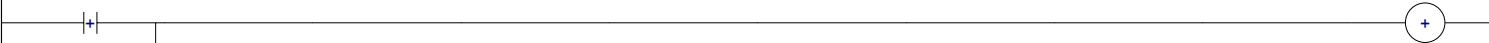












M00144

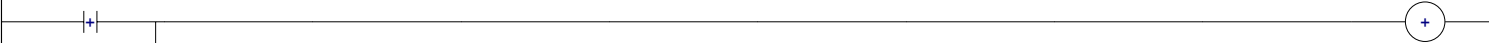
%M00144

M00153

%M00153

M00159

%M00159



M00165

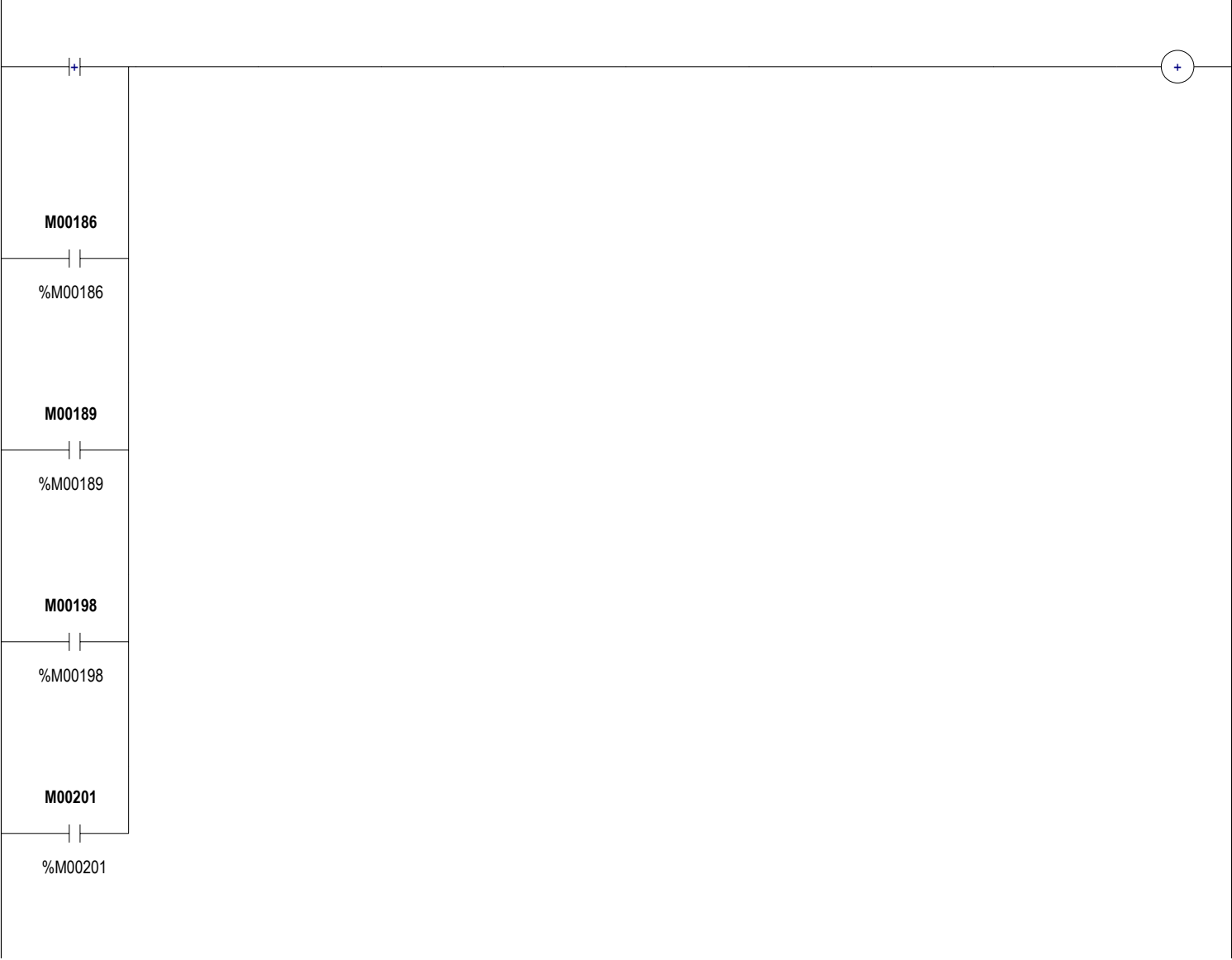
%M00165

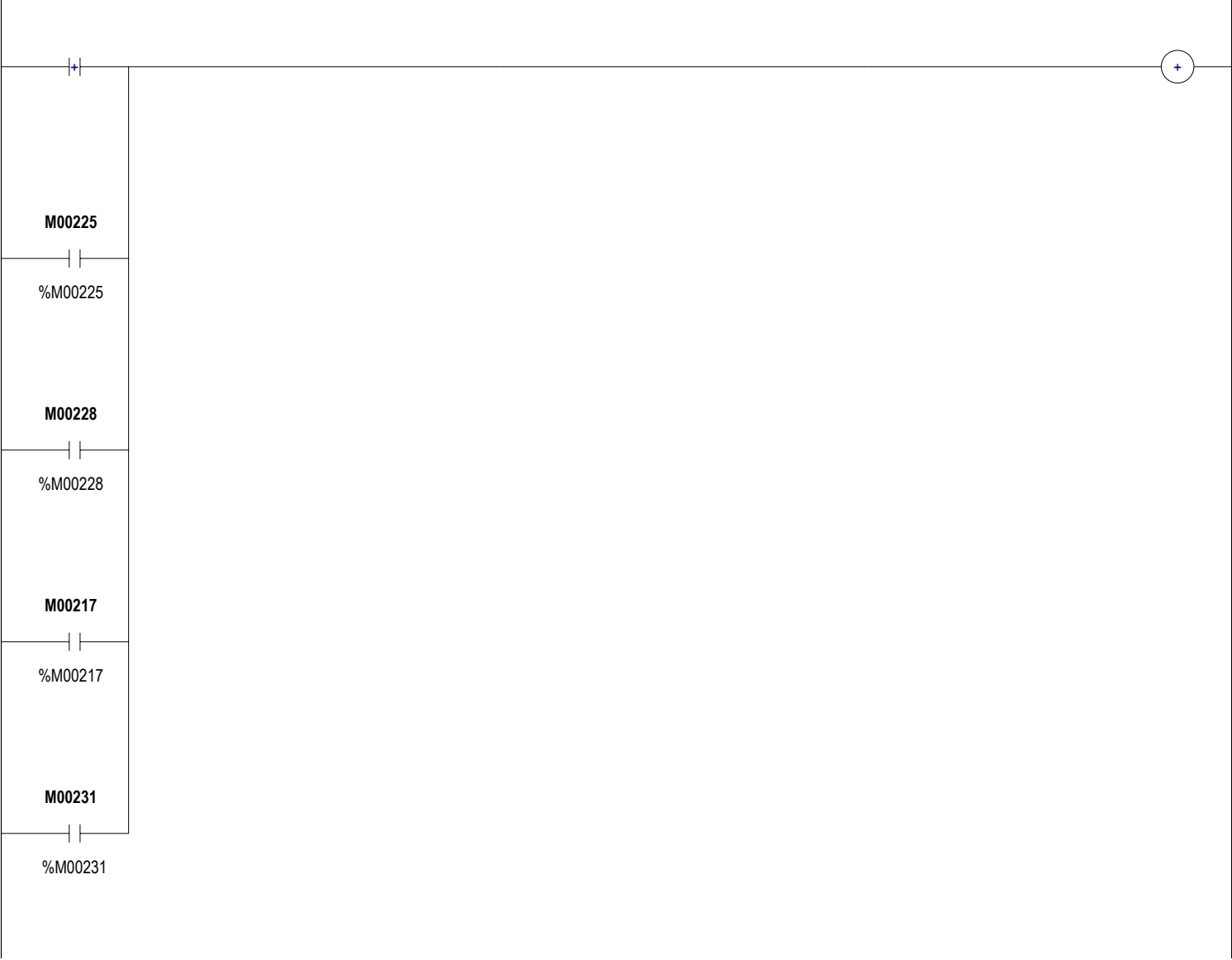
M00174

%M00174

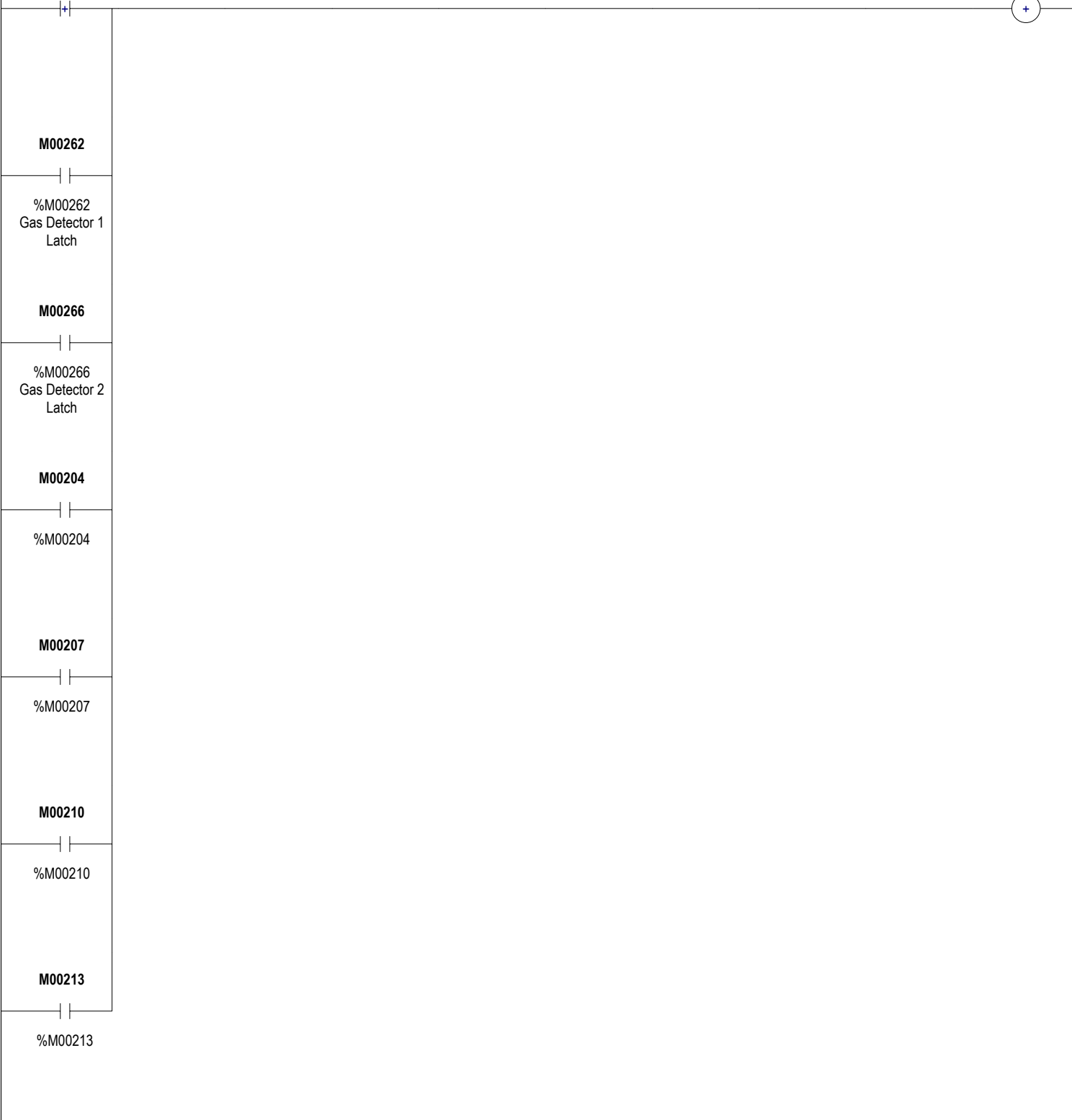
M00177

%M00177







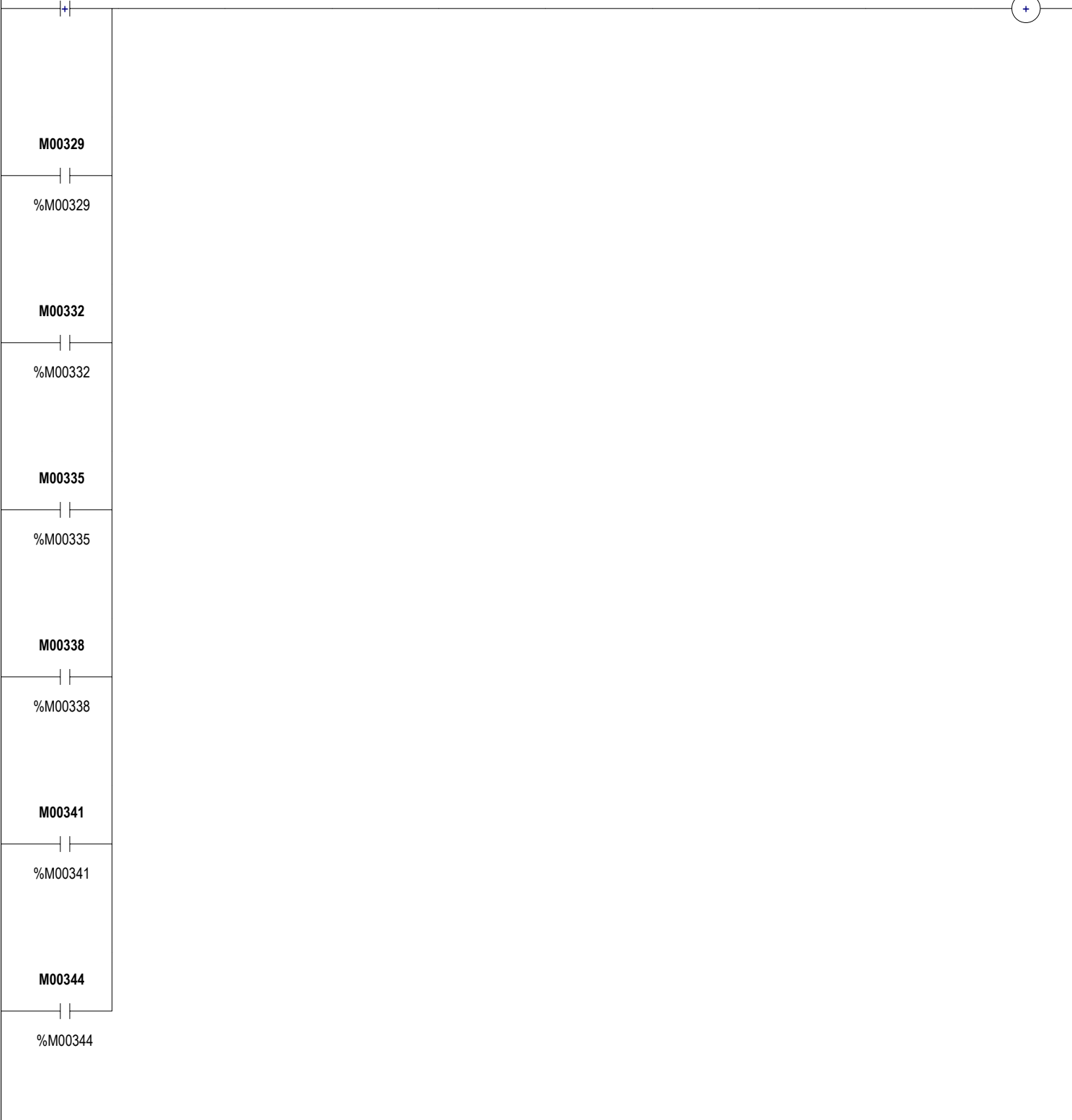




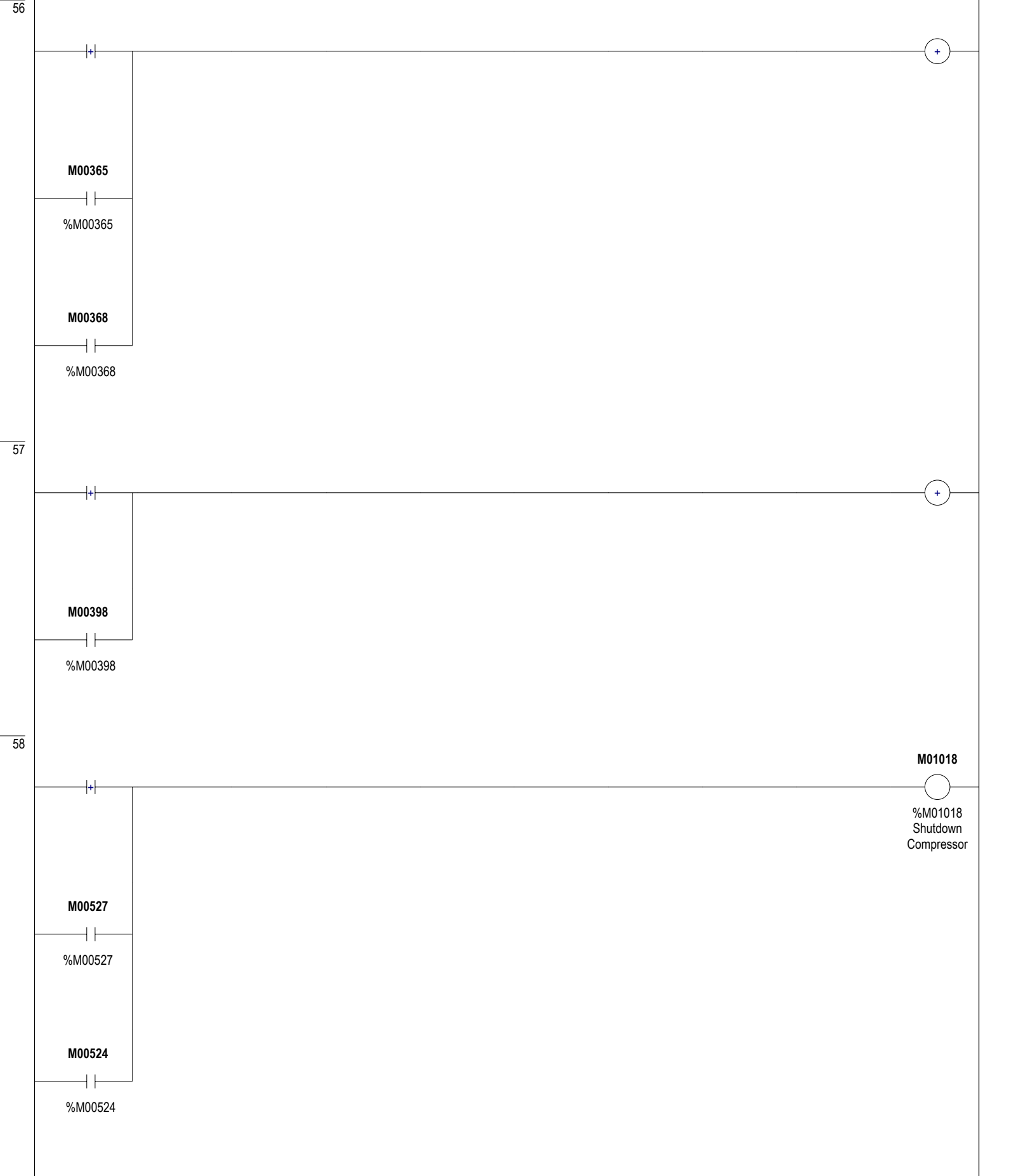












**M01018 %M01018**  
LD Block,'FRSTOUT': NCCON 00024, 00035, 00046, 00057, 00068, 00079, 00090, 00101;  
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LD Block,'SEQUENC': NCCON 00006, 00011; NOCON 00012;  
LD Block,'ROLL': NCCON 00020;  
LD Block,'STATS': NCCON 00010; NOCON 00013;