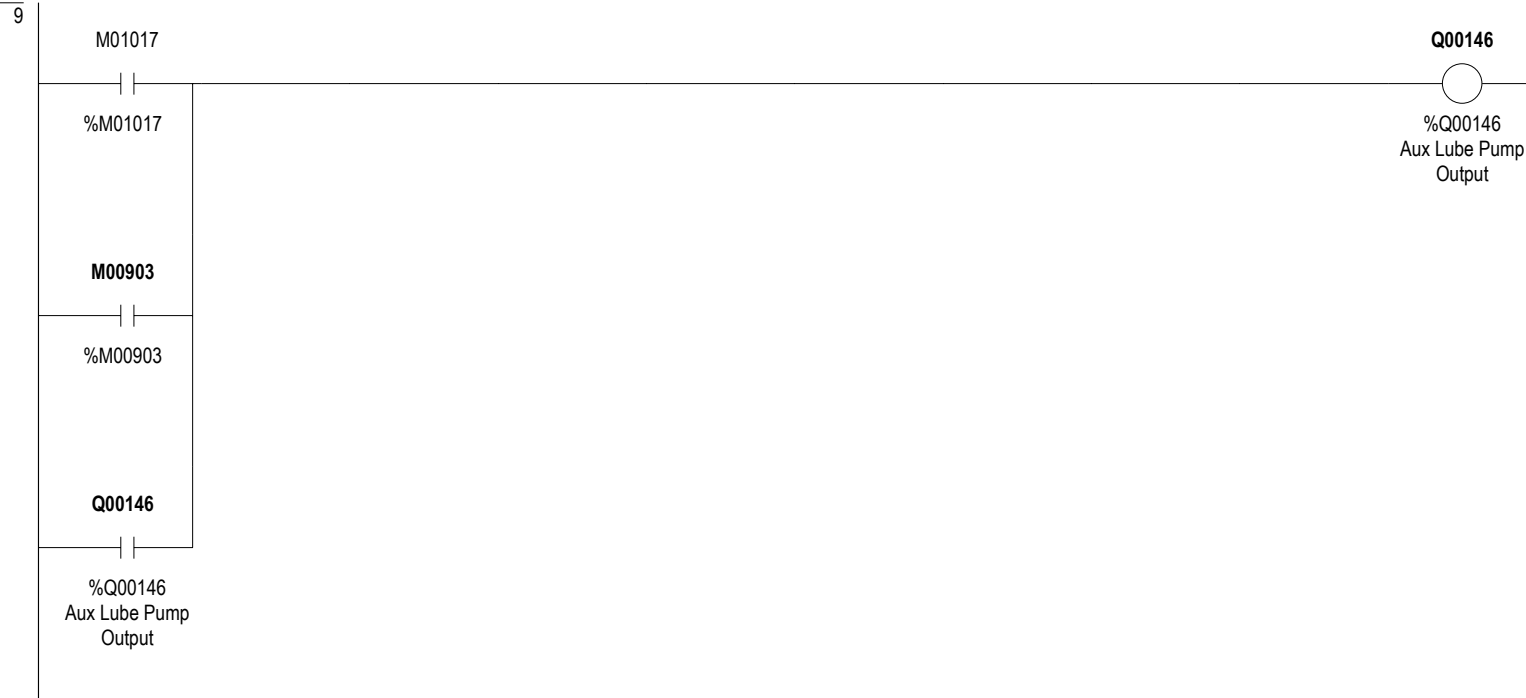


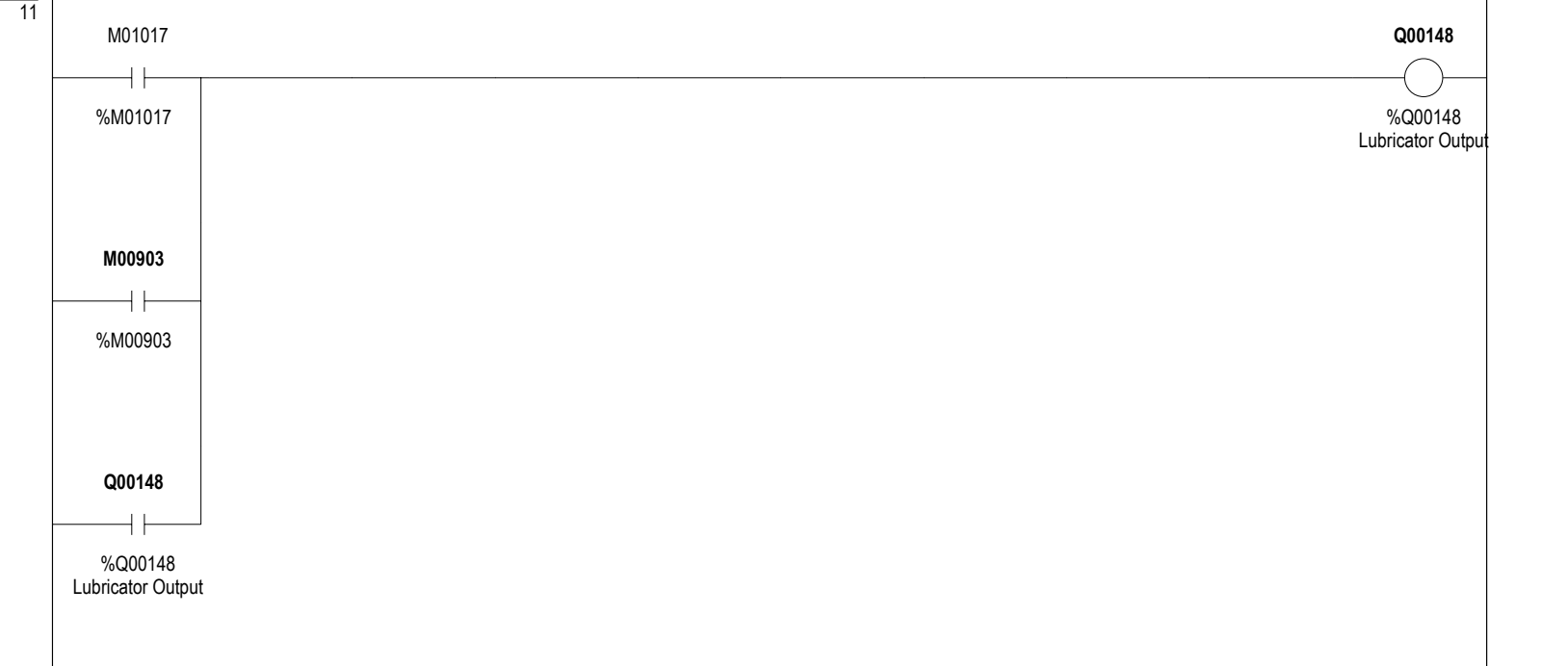
LD Block,'PREPURG': COIL 00008;  
LD Block,'ROLL': COIL 00008;  
LD Block,'PHASE0': COIL 00014;  
LD Block,'SHUTDWN': COIL 00011;  
LD Block,'COMPESD': COIL 00009;  
LD Block,'POSTLB': COIL 00014;  
LD Block,'LOAD2': COIL 00020;  
LD Block,'UNLOAD2': COIL 00015;  
LD Block,'FASTSD': COIL 00014;



**Q00146 %Q00146**  
LD Block,'WARMUP2': COIL 00004;  
LD Block,'VENT': COIL 00008;  
LD Block,'PREPURG': NOCON 00009, 00010, 00024, 00026; COIL 00009;  
LD Block,'ROLL': COIL 00004;  
LD Block,'PHASE0': COIL 00007;  
LD Block,'SHUTDWN': COIL 00006;  
LD Block,'COMPESD': COIL 00004;  
LD Block,'POSTLB': COIL 00009;  
LD Block,'LOAD2': COIL 00006;  
LD Block,'UNLOAD2': COIL 00006;  
LD Block,'FASTSD': COIL 00006;



**Q00146 %Q00146 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00009;



**Q00148 %Q00148**  
LD Block,'WARMUP2': COIL 00005;  
LD Block,'VENT': COIL 00002;  
LD Block,'PREPURG': NOCON 00011, 00012, 00015; COIL 00011;  
LD Block,'ROLL': COIL 00005;  
LD Block,'PHASE0': COIL 00001;  
LD Block,'SHUTDWN': COIL 00027;  
LD Block,'COMPESD': COIL 00017;  
LD Block,'POSTLB': COIL 00008;  
LD Block,'LOAD2': COIL 00007;  
LD Block,'UNLOAD2': COIL 00007;  
LD Block,'FASTSD': COIL 00019;



**Q00148 %Q00148 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00011;



**M00970 %M00970**



**M00970 %M00970 (Controlling Rung Reference)**

LD Block,'PREPURG': COIL 00013;

**M00971 %M00971**

LD Block,'VENT': COIL 00022;

LD Block,'PREPURG': NCCON 00013, 00015; NOCON 00014; COIL 00014;

LD Block,'PHASE0': COIL 00018;



**M00971 %M00971 (Controlling Rung Reference)**

LD Block,'PREPURG': COIL 00014;

**Q00148 %Q00148 (Controlling Rung Reference)**

LD Block,'PREPURG': COIL 00011;

**Q00037 %Q00037**

LD Block,'PREPURG': COIL 00015;

LD Block,'ROLL': COIL 00024;



**Q00150 %Q00150**

LD Block,'WARMUP2': NOCON 00009; COIL 00009;

LD Block,'VENT': COIL 00003;

LD Block,'PREPURG': NOCON 00016, 00017, 00018, 00020; COIL 00016;

LD Block,'ROLL': NOCON 00001; COIL 00001;

LD Block,'PHASE0': COIL 00002;

LD Block,'SHUTDWN': NOCON 00022; COIL 00022;

LD Block,'COMPESD': COIL 00012;

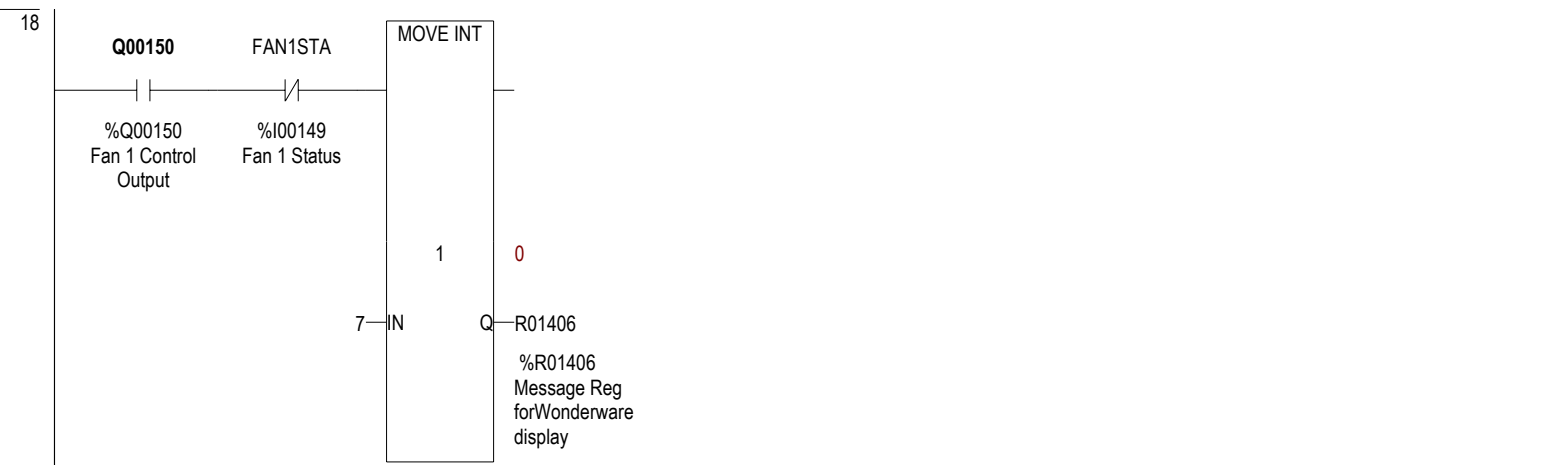
LD Block,'POSTLB': NOCON 00019; COIL 00019;

LD Block,'LOAD2': NOCON 00003; COIL 00003;

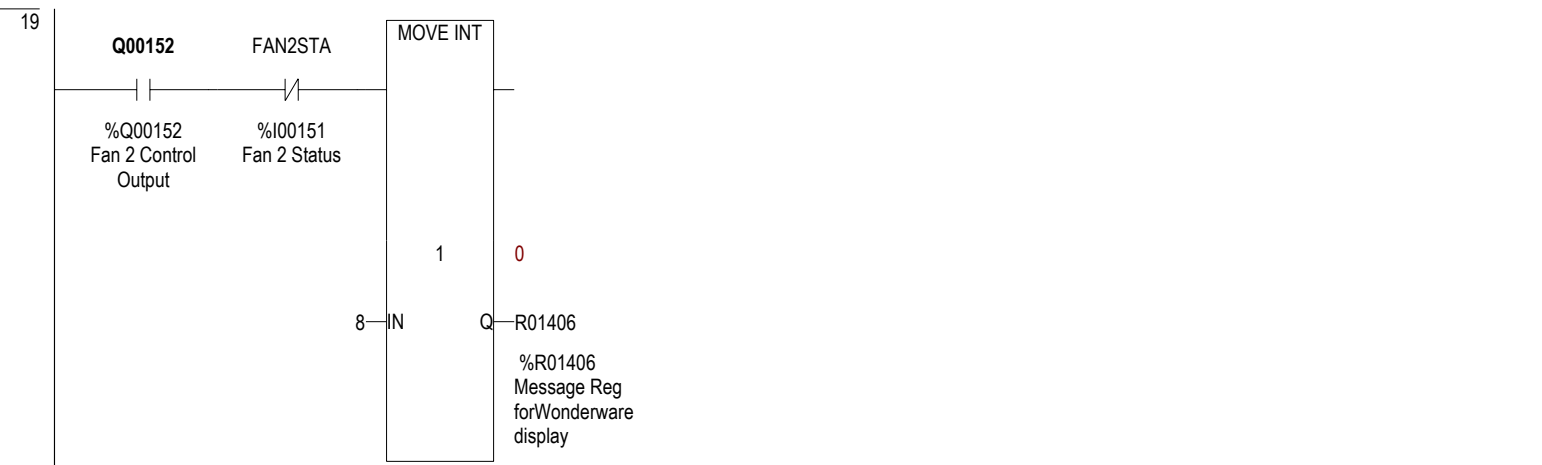
LD Block,'UNLOAD2': NOCON 00003; COIL 00003;



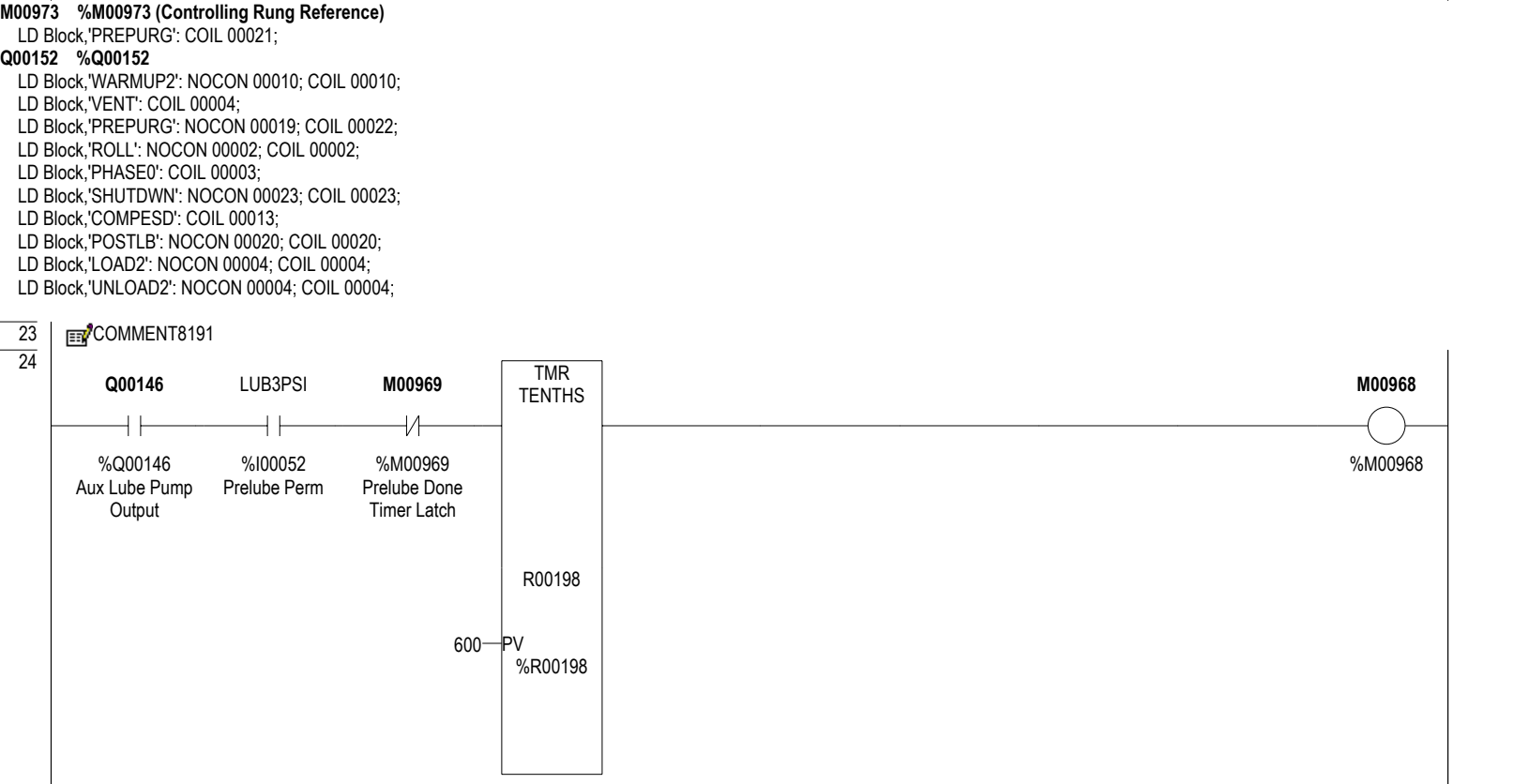
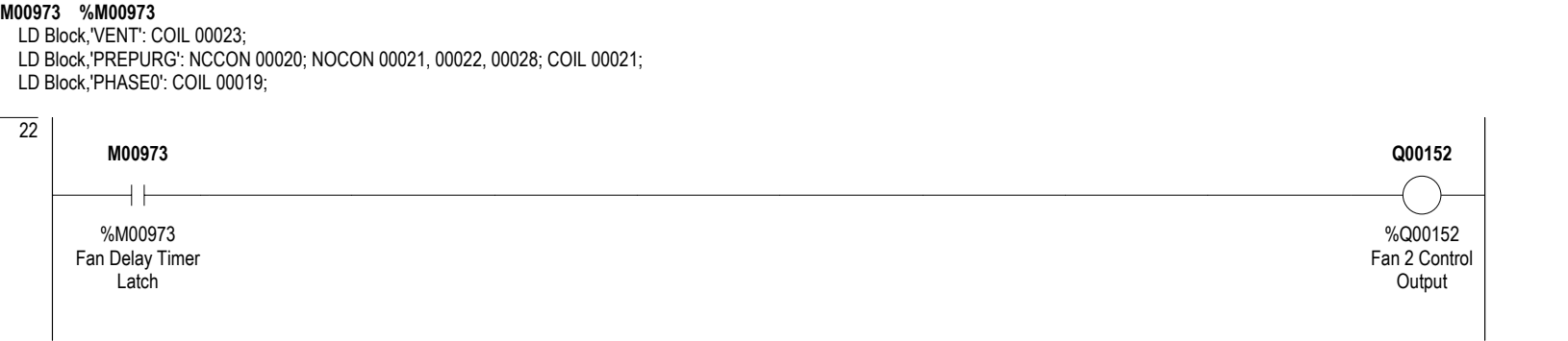
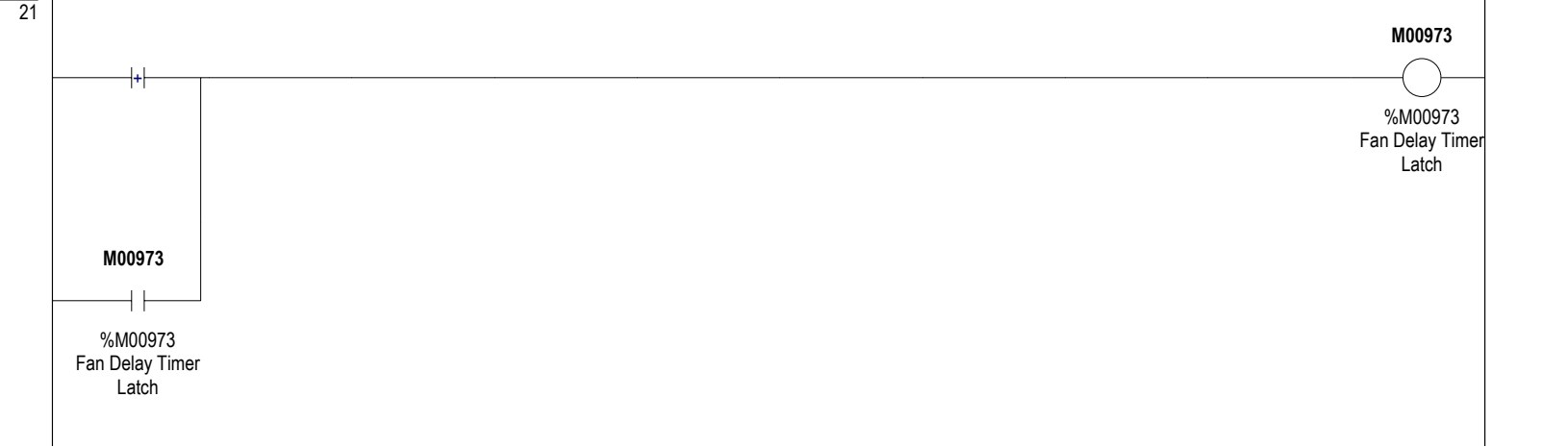
**Q00150 %Q00150 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00016;

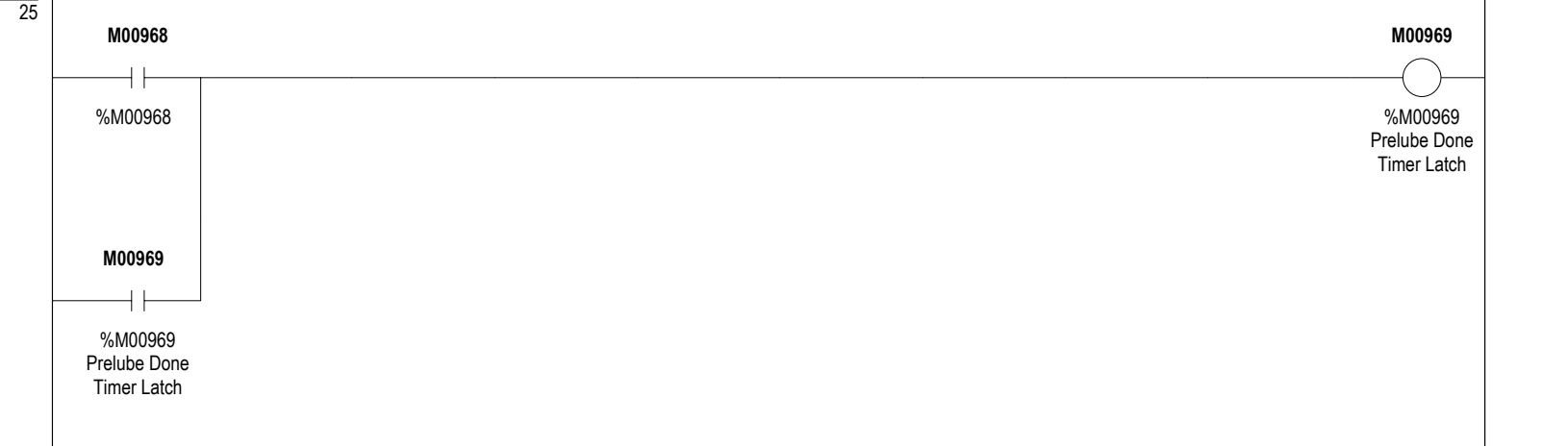


**Q00150 %Q00150 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00016;



**Q00150 %Q00150 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00016;





**M00968 %M00968 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00024;  
**M00969 %M00969**  
LD Block,'VENT': COIL 00024;  
LD Block,'PREPURG': NCCON 00024; NOCON 00025, 00043, 00045; COIL 00025;  
LD Block,'PHASE0': COIL 00020;

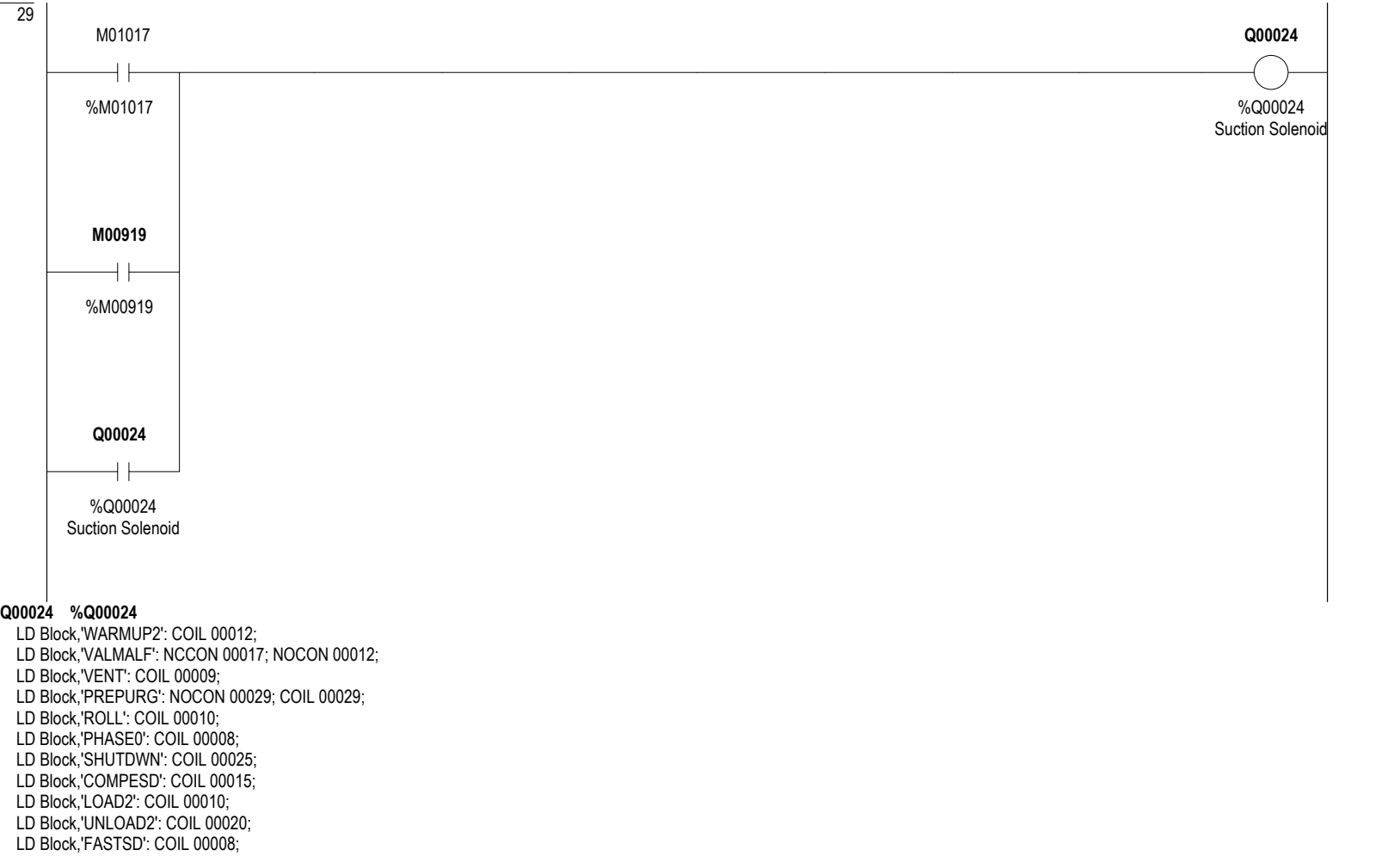


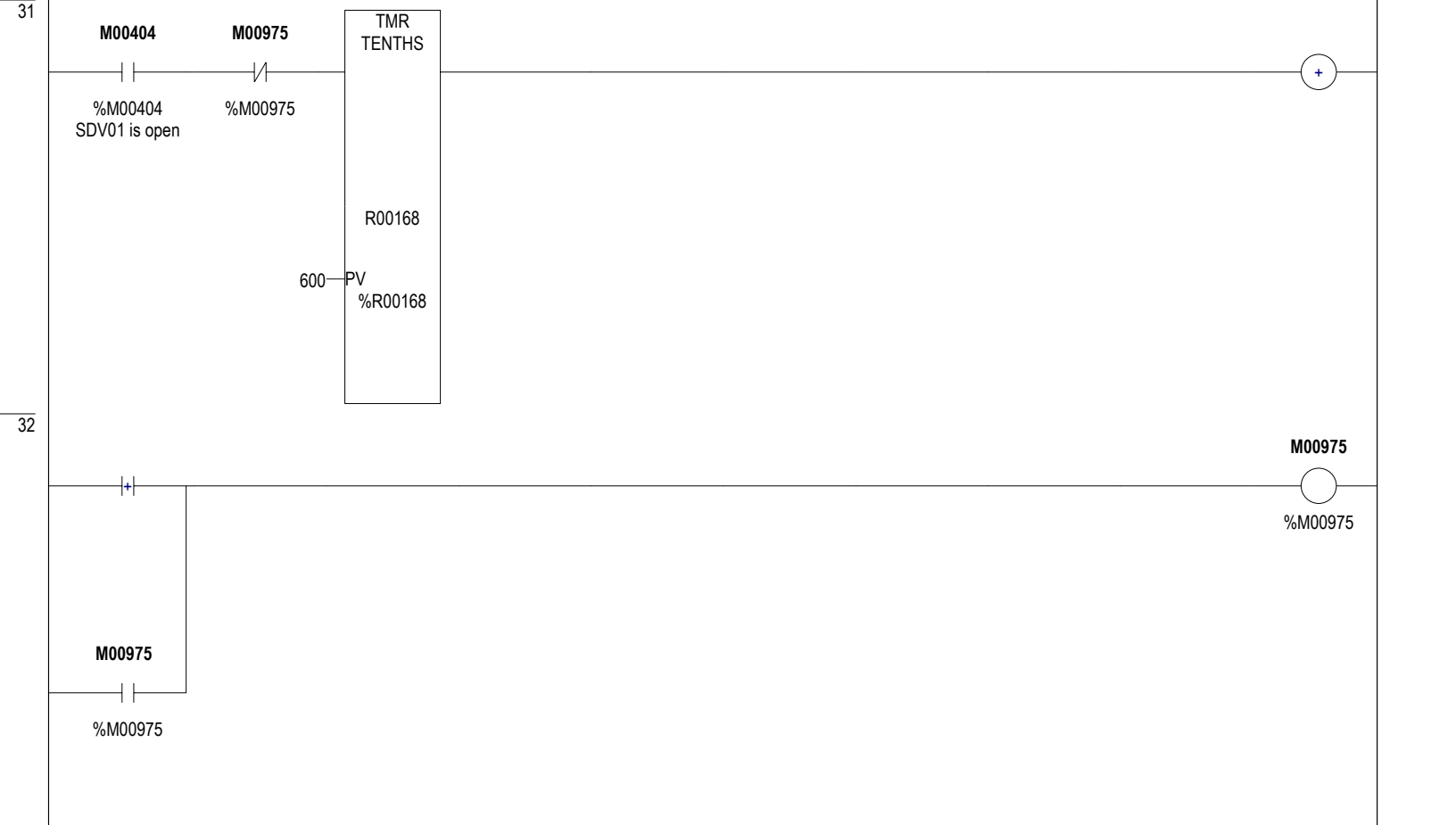
**Q00146 %Q00146 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00009;  
**M00215 %M00215**  
LD Block,'FRSTOUT': NEGCOIL 00240; NOCON 00241, 00242;  
LD Block,'PREPURG': NOCON 00027; COIL 00026;



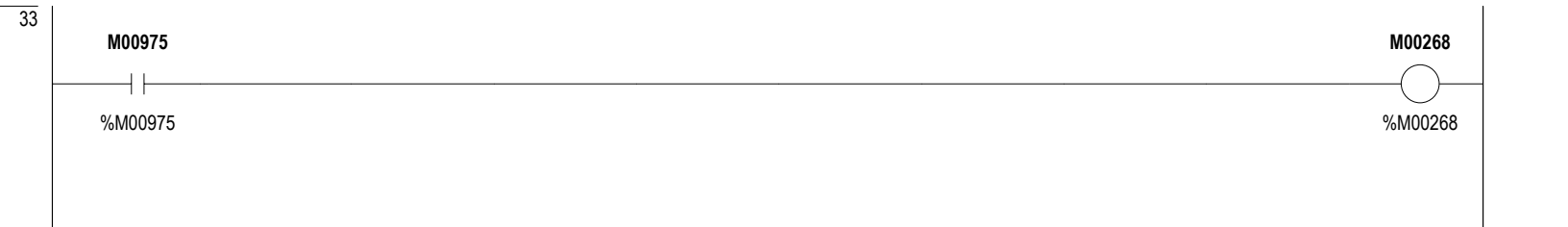
**M00215 %M00215 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00026;



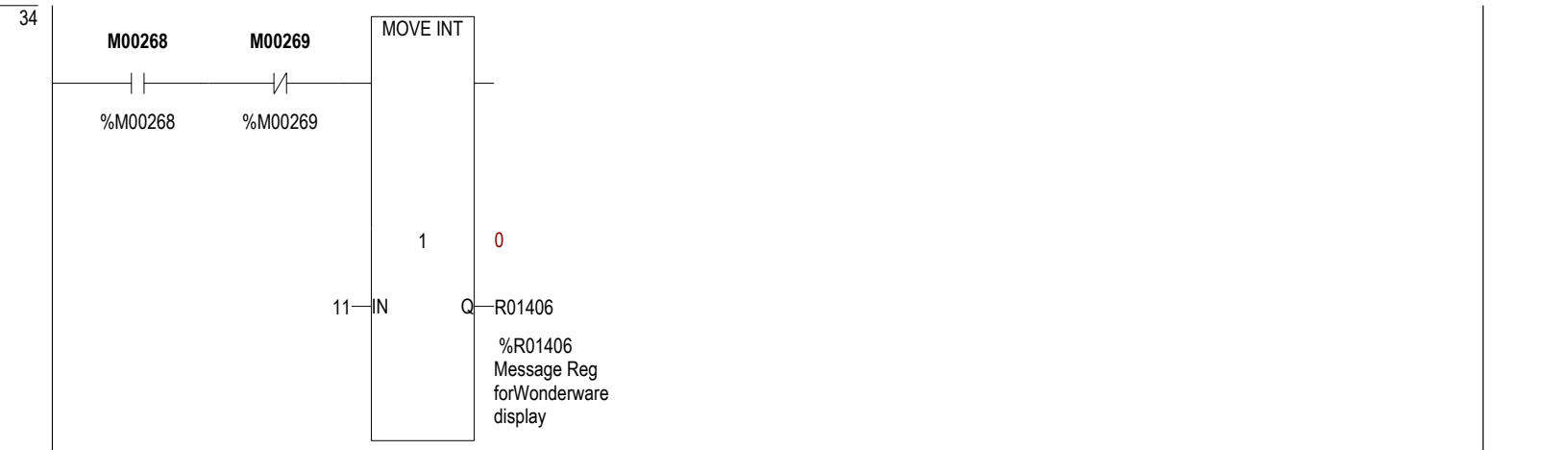




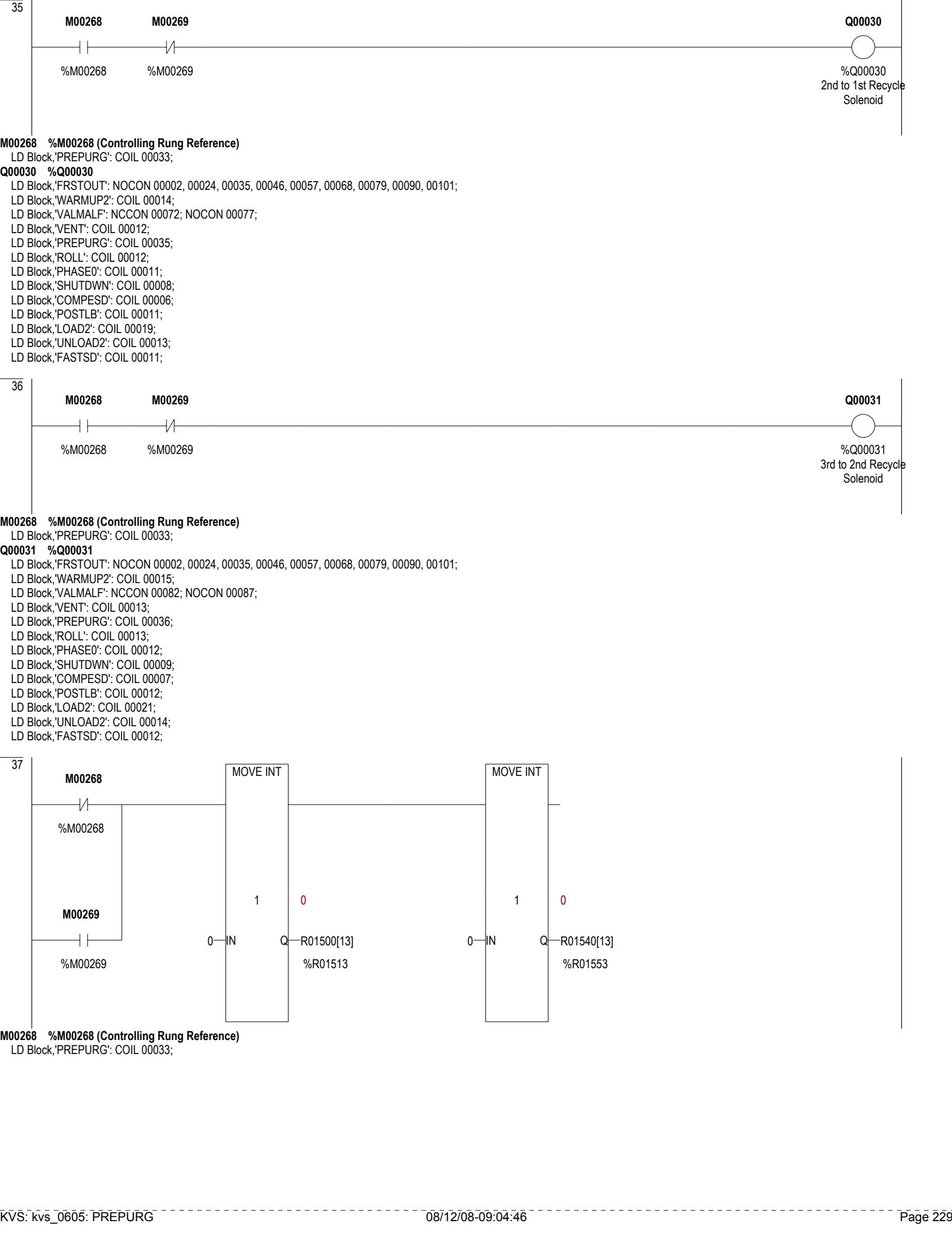
**M00975 %M00975**  
LD Block,'VENT': COIL 00025;  
LD Block,'PREPURG': NCCON 00031; NOCON 00032, 00033; COIL 00032;  
LD Block,'PHASE0': COIL 00021;

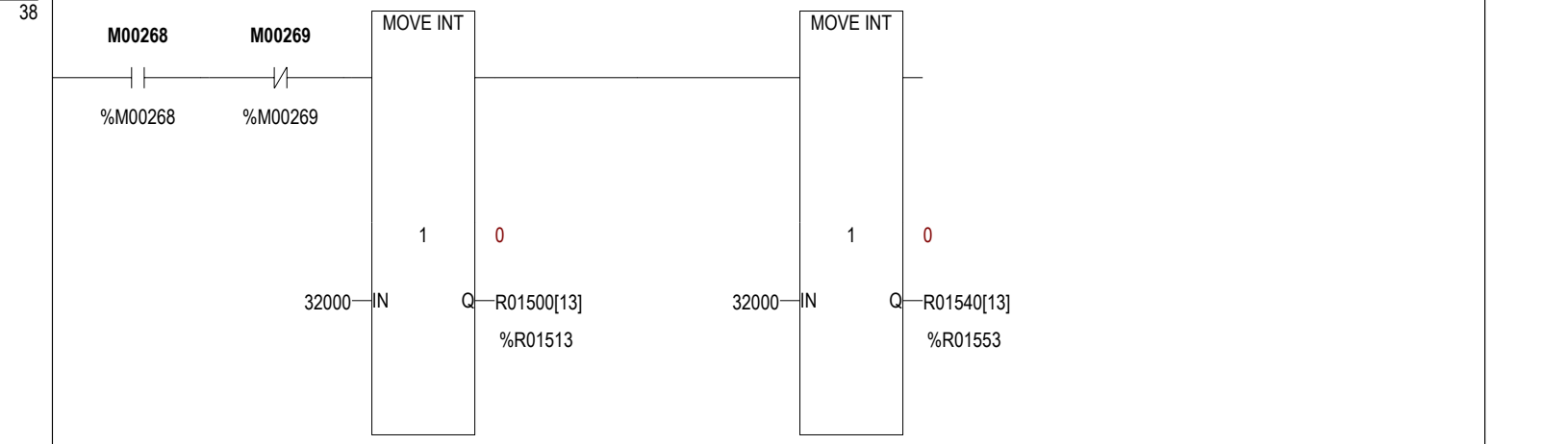


**M00975 %M00975 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00032;  
**M00268 %M00268**  
LD Block,'PREPURG': NCCON 00030, 00037; NOCON 00034, 00035, 00036, 00038; COIL 00033;

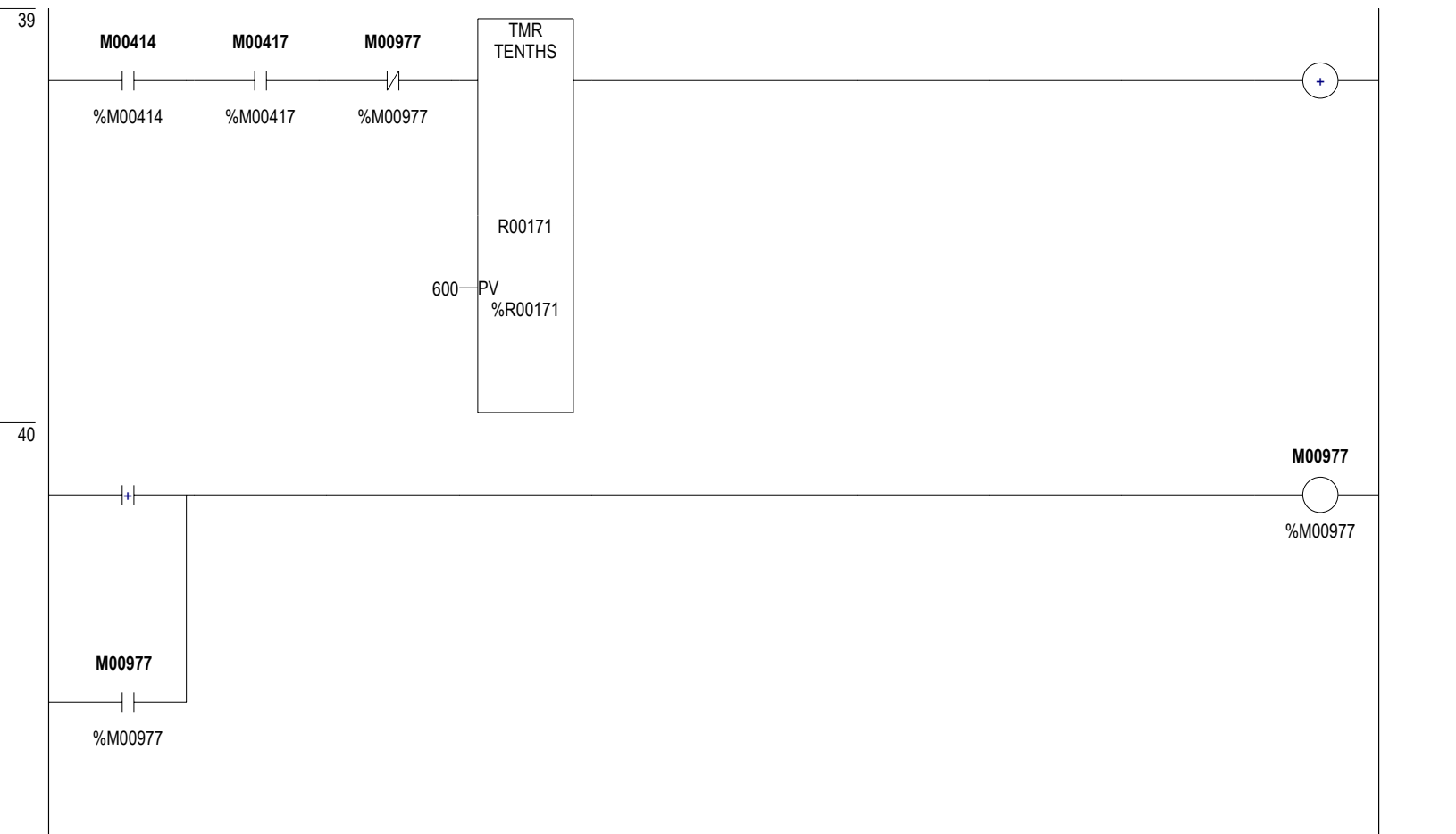


**M00268 %M00268 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00033;





**M00268** %M00268 (Controlling Rung Reference)  
LD Block,'PREPURG': COIL 00033;



**M00977** %M00977  
LD Block,'VENT': COIL 00026;  
LD Block,'PREPURG': NCCON 00039; NOCON 00040, 00041; COIL 00040;  
LD Block,'PHASE0': COIL 00022;



**M00977** %M00977 (Controlling Rung Reference)  
LD Block,'PREPURG': COIL 00040;

**M00269** %M00269  
LD Block,'PREPURG': NCCON 00034, 00035, 00036, 00038; NOCON 00037, 00042, 00043, 00044, 00045; COIL 00041;



**M00269 %M00269 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00041;



**M00969 %M00969 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00025;

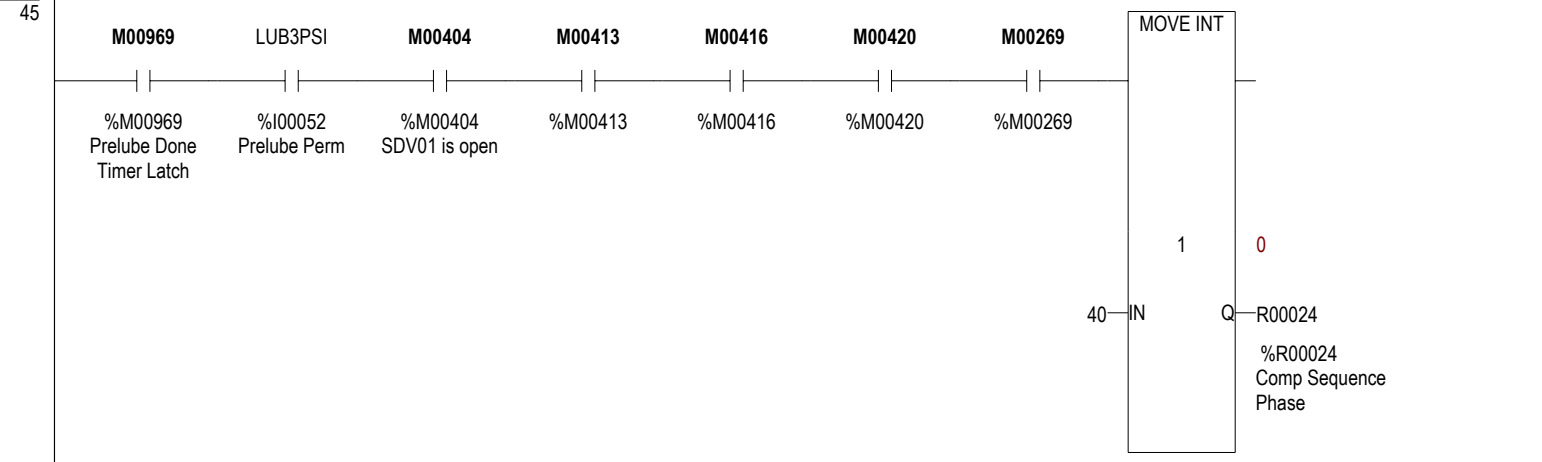
**M00269 %M00269 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00041;

**Q00028 %Q00028**  
LD Block,'WARMUP2': COIL 00016;  
LD Block,'VALMALF': NCCON 00057; NOCON 00052;  
LD Block,'VENT': COIL 00011;  
LD Block,'PREPURG': COIL 00043;  
LD Block,'ROLL': COIL 00014;  
LD Block,'PHASE0': COIL 00010;  
LD Block,'SHUTDWN': COIL 00026;  
LD Block,'COMPESD': COIL 00016;  
LD Block,'POSTLB': COIL 00006;  
LD Block,'LOAD2': COIL 00011;  
LD Block,'UNLOAD2': COIL 00021;  
LD Block,'FASTSD': COIL 00009;



**M00269 %M00269 (Controlling Rung Reference)**  
LD Block,'PREPURG': COIL 00041;

**Q00029 %Q00029**  
LD Block,'WARMUP2': COIL 00017;  
LD Block,'VALMALF': NCCON 00062; NOCON 00067;  
LD Block,'VENT': COIL 00018;  
LD Block,'PREPURG': COIL 00044;  
LD Block,'ROLL': COIL 00016;  
LD Block,'PHASE0': COIL 00016;  
LD Block,'SHUTDWN': COIL 00024;  
LD Block,'COMPESD': COIL 00014;  
LD Block,'POSTLB': COIL 00007;  
LD Block,'LOAD2': COIL 00012;  
LD Block,'UNLOAD2': COIL 00019;  
LD Block,'FASTSD': COIL 00007;

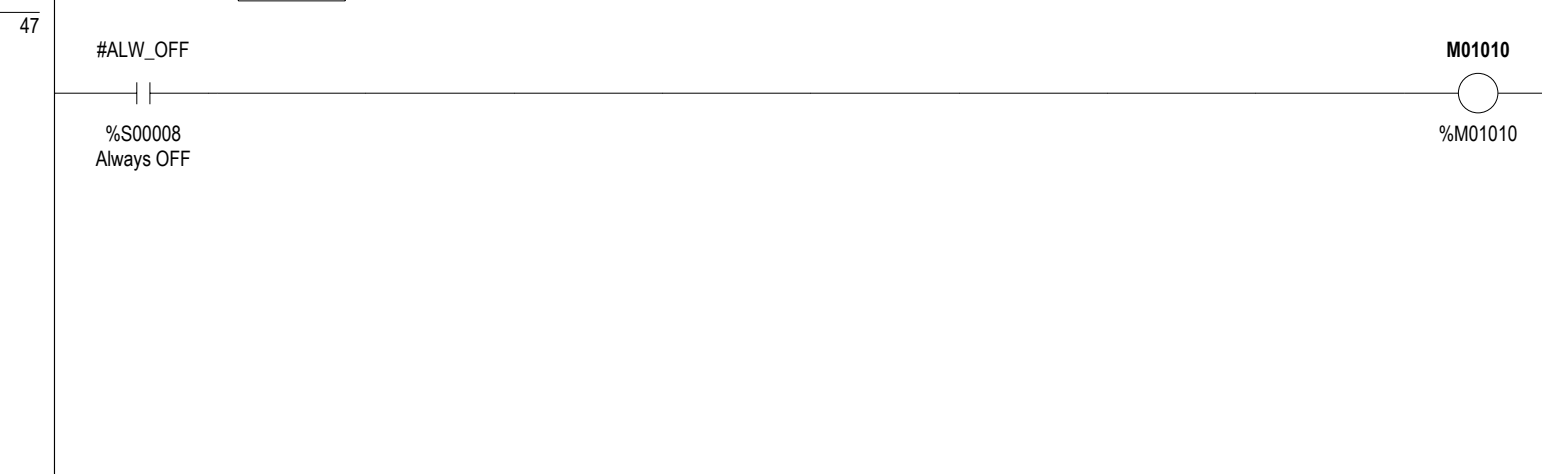
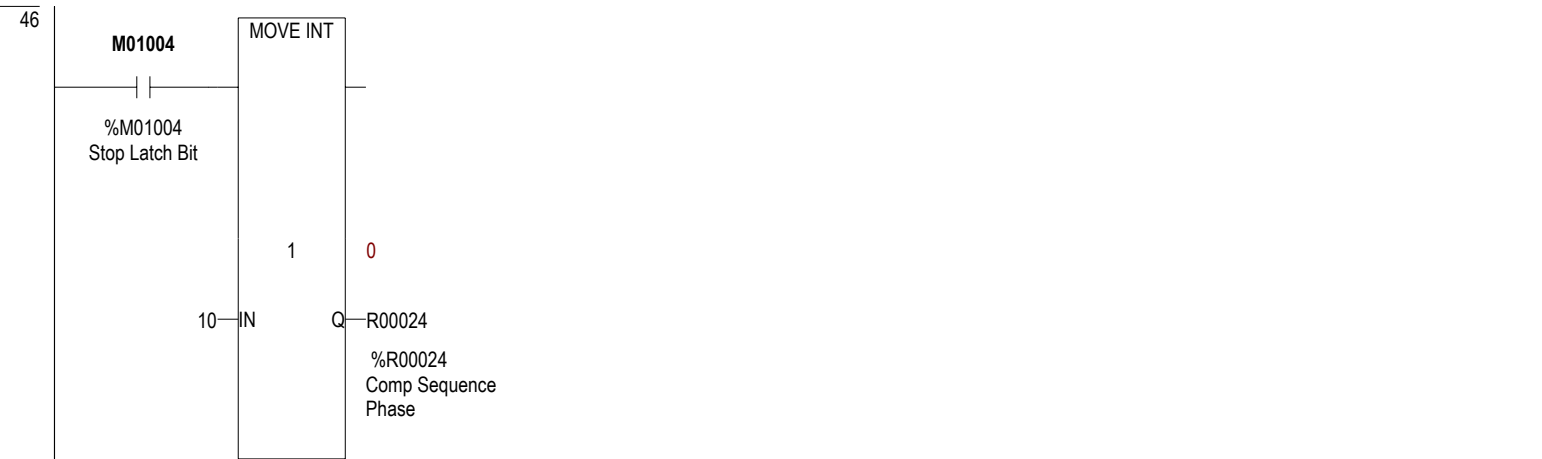


**M00969 %M00969 (Controlling Rung Reference)**

LD Block,'PREPURG': COIL 00025;

**M00269 %M00269 (Controlling Rung Reference)**

LD Block,'PREPURG': COIL 00041;



**M01010 %M01010**

LD Block,'WARMUP2': COIL 00033;

LD Block,'VENT': COIL 00028;

LD Block,'PREPURG': COIL 00047;

LD Block,'ROLL': COIL 00042;

LD Block,'PHASE0': COIL 00024;

LD Block,'LOAD2': NOCON 00014, 00015, 00016, 00017, 00018, 00019, 00020, 00021, 00022; COIL 00014;

LD Block,'UNLOAD2': COIL 00001;