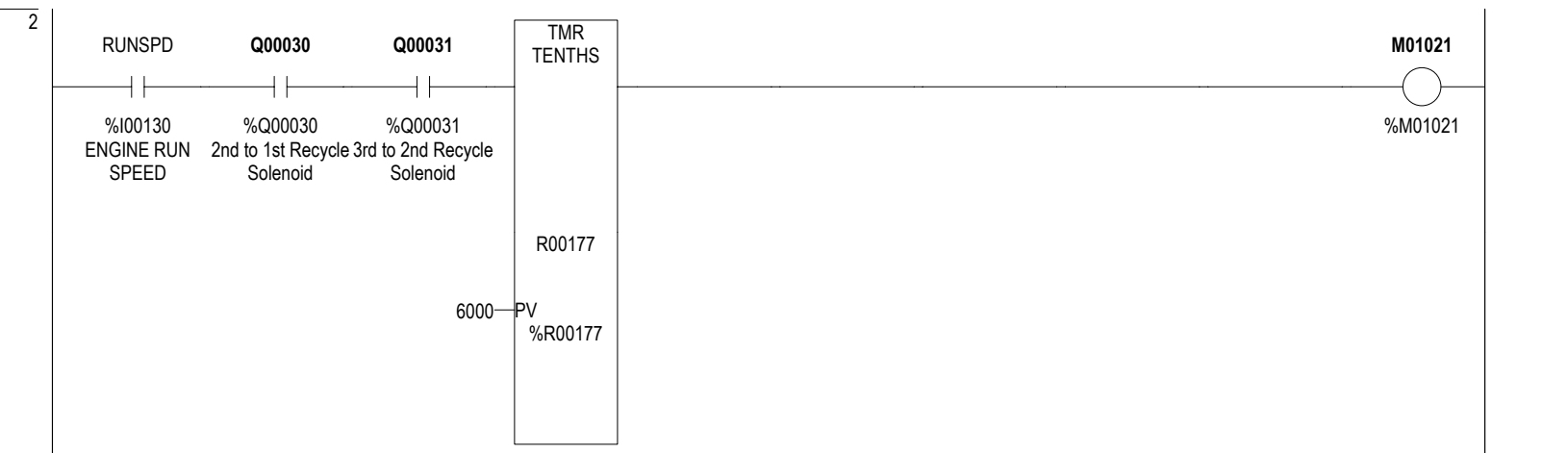




M01020 %M01020
LD Block,'FRSTOUT': NOCON 00212, 00213, 00220, 00221, 00224, 00225, 00232, 00233; COIL 00001;

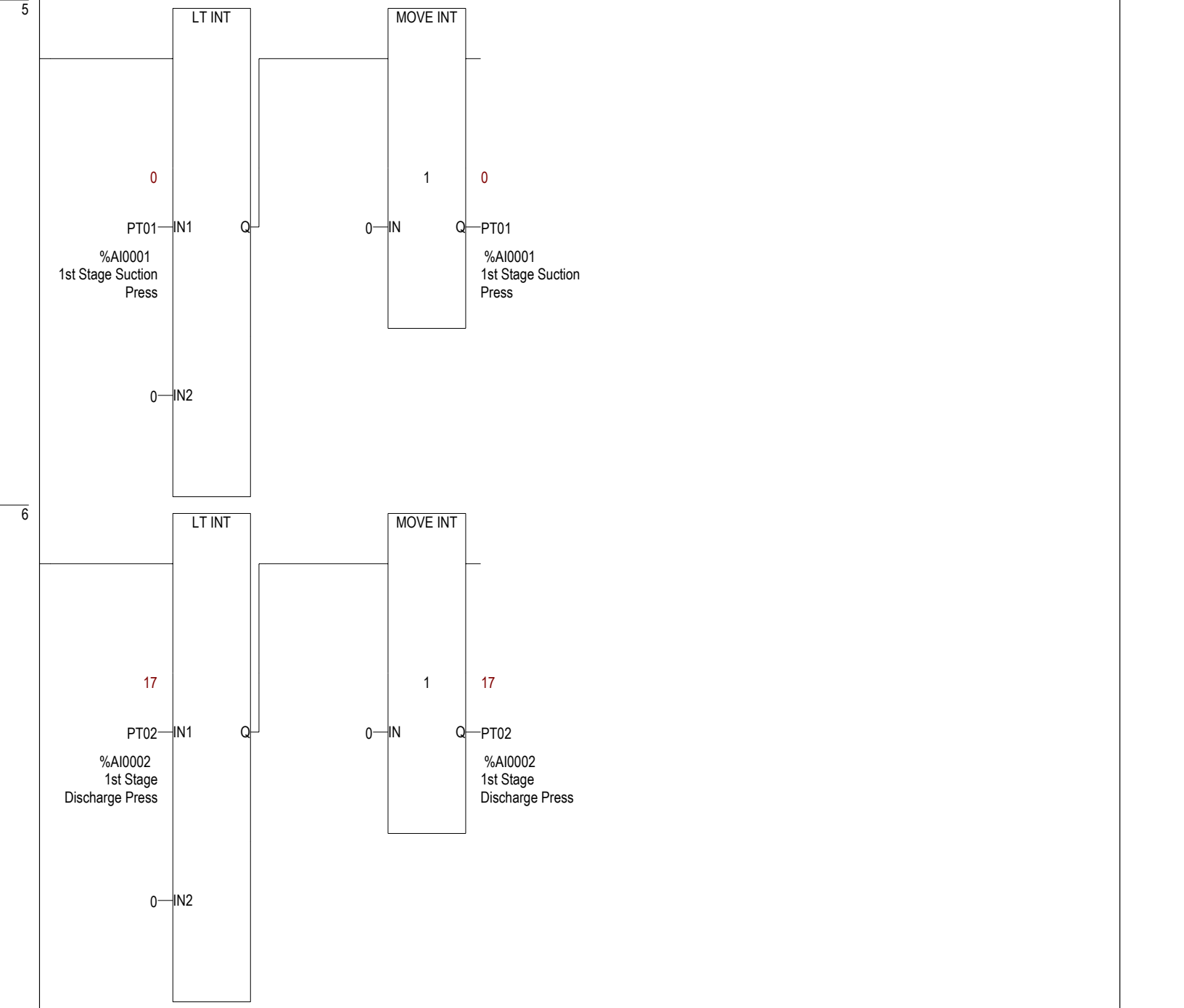


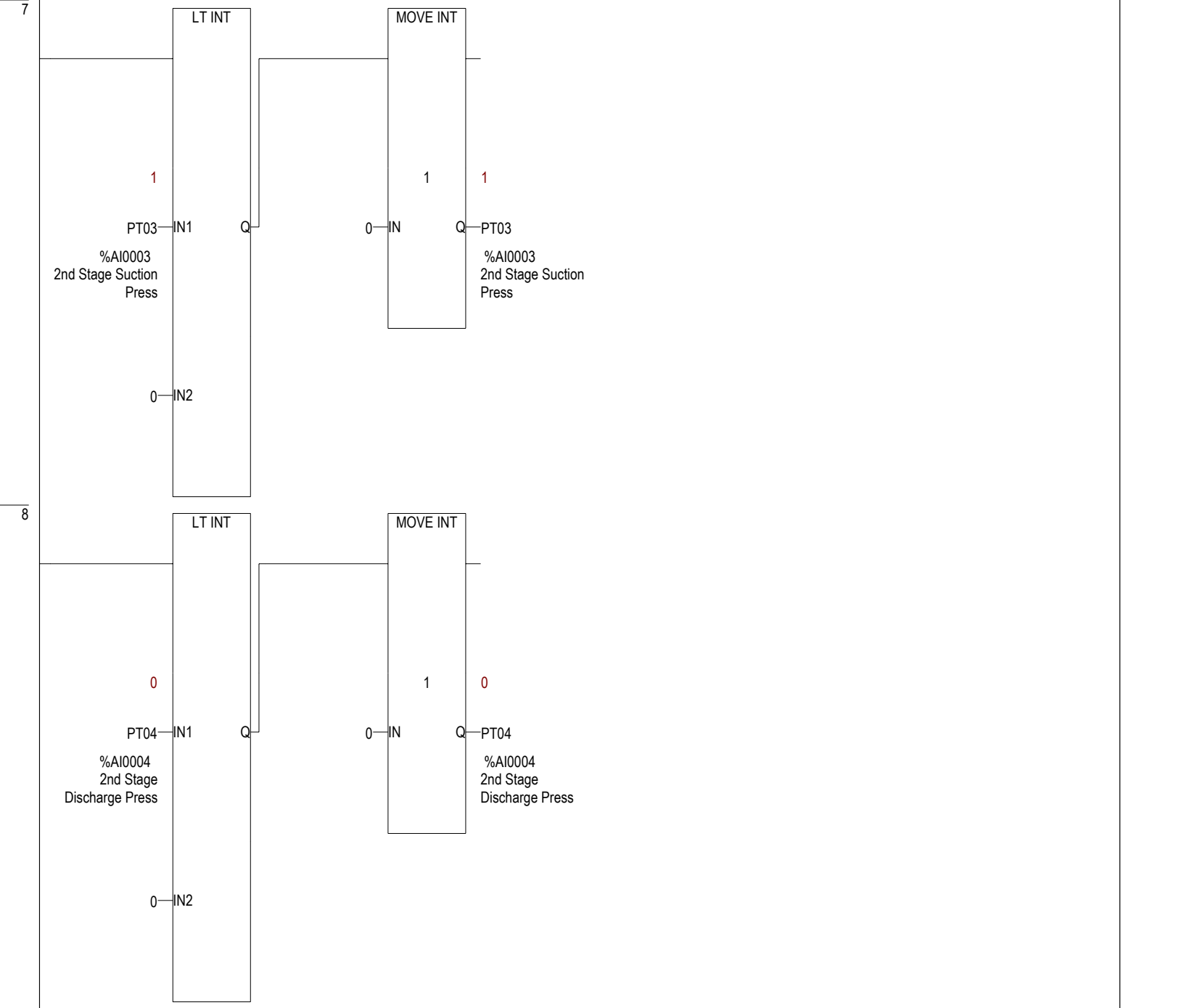
M01021 %M01021
LD Block,'FRSTOUT': NOCON 00024, 00035, 00046, 00057, 00068, 00079, 00090, 00101; COIL 00002;

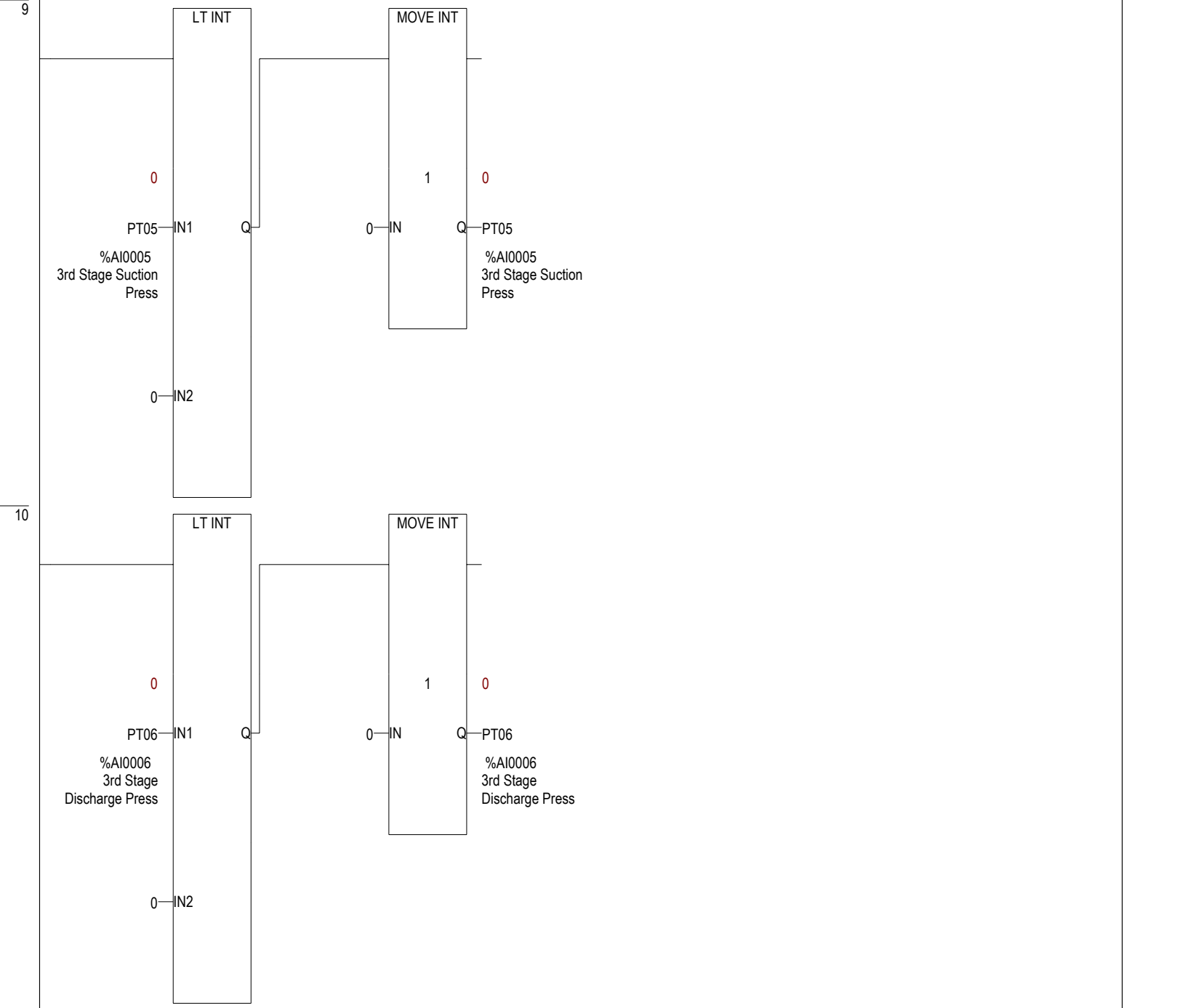


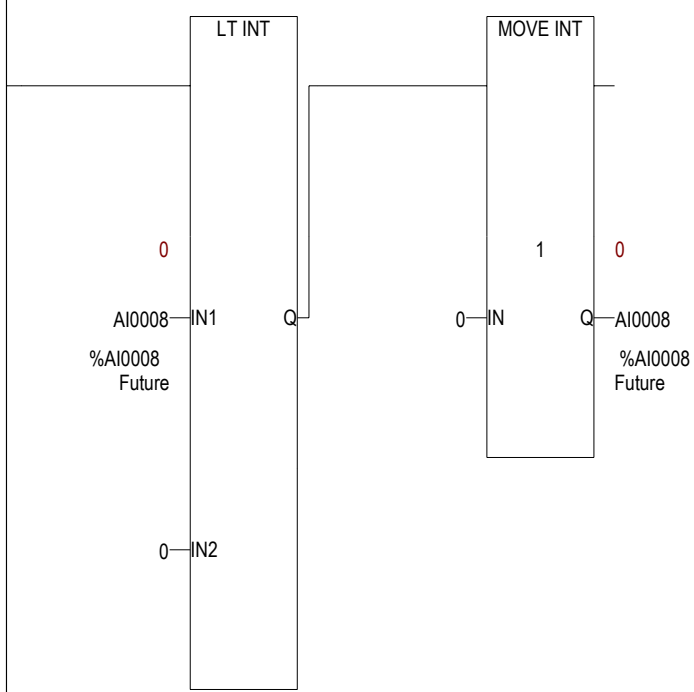
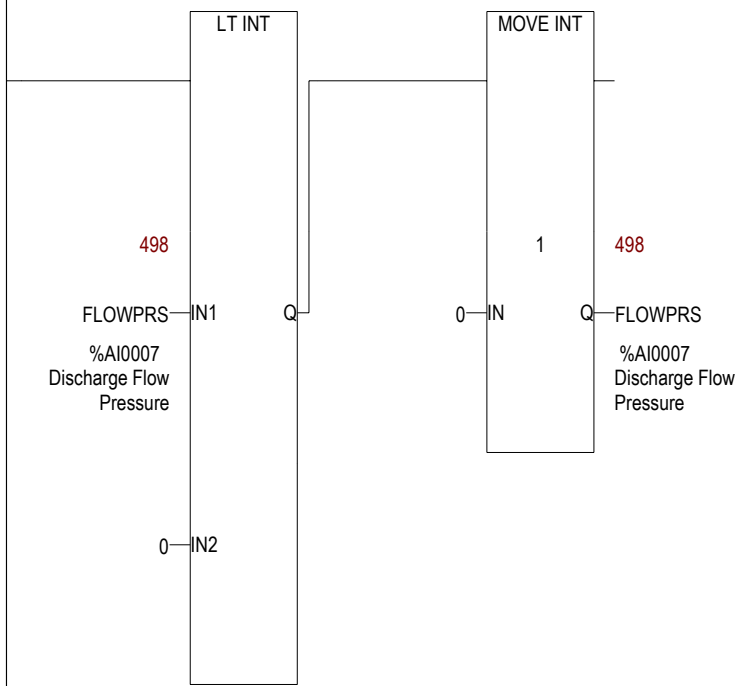
M01022 %M01022
LD Block,'FRSTOUT': NCCON 00017, 00022, 00028, 00033, 00039, 00044, 00050, 00055, 00061, 00066, 00072, 00077, 00083, 00088, 00094, 00099, 00105, 00110, 00127, 00131, 00139, 00143, 00147, 00151, 00155, 00159, 00163, 00167, 00171, 00175, 00179, 00183, 00187, 00191, 00257, 00262, 00267, 00272, 00277, 00282, 00287, 00292, 00297, 00302, 00307, 00312, 00317, 00322, 00327, 00332, 00337, 00342, 00347, 00352, 00357, 00362, 00367, 00373, 00375, 00381, 00383, 00389, 00391, 00397, 00399, 00405, 00407, 00413, 00415, 00421, 00423, 00429, 00431, 00437, 00439, 00445, 00450, 00455; COIL 00003;

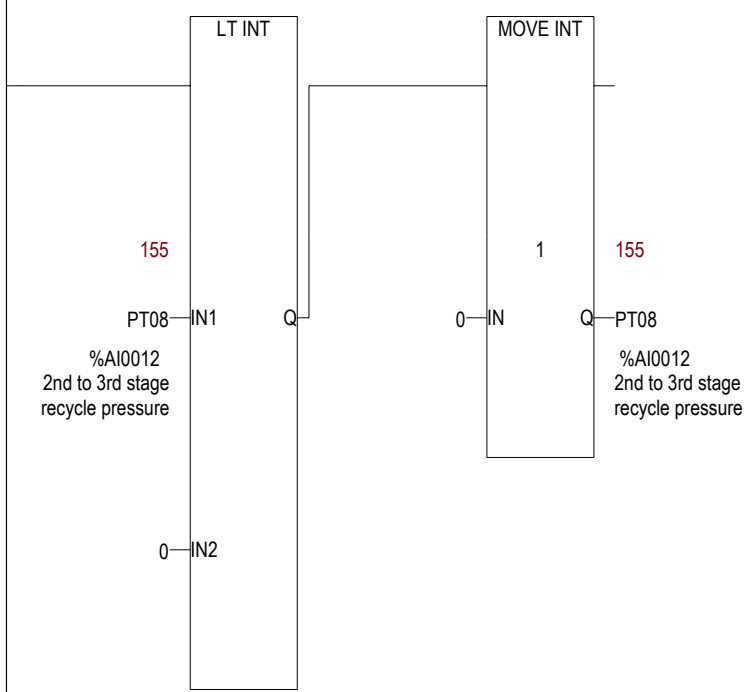
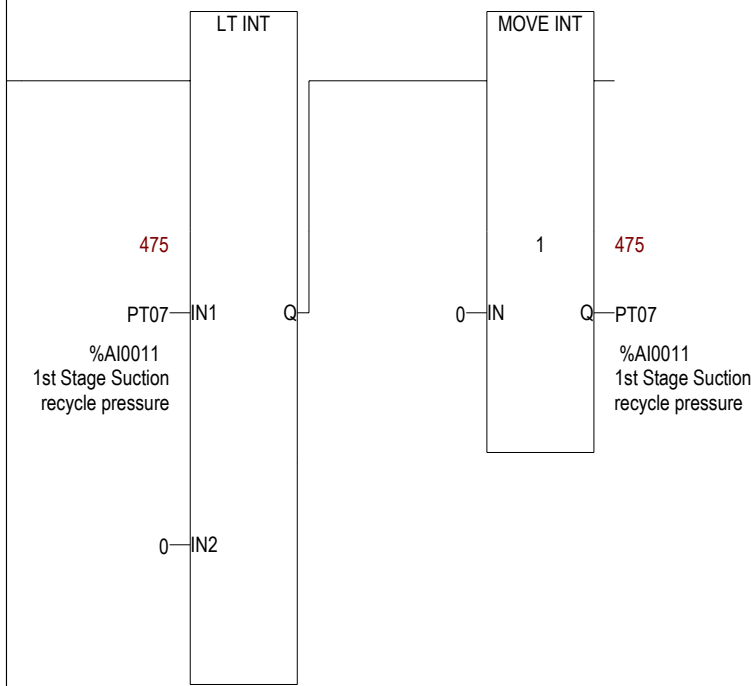
4 |  COMMENT6

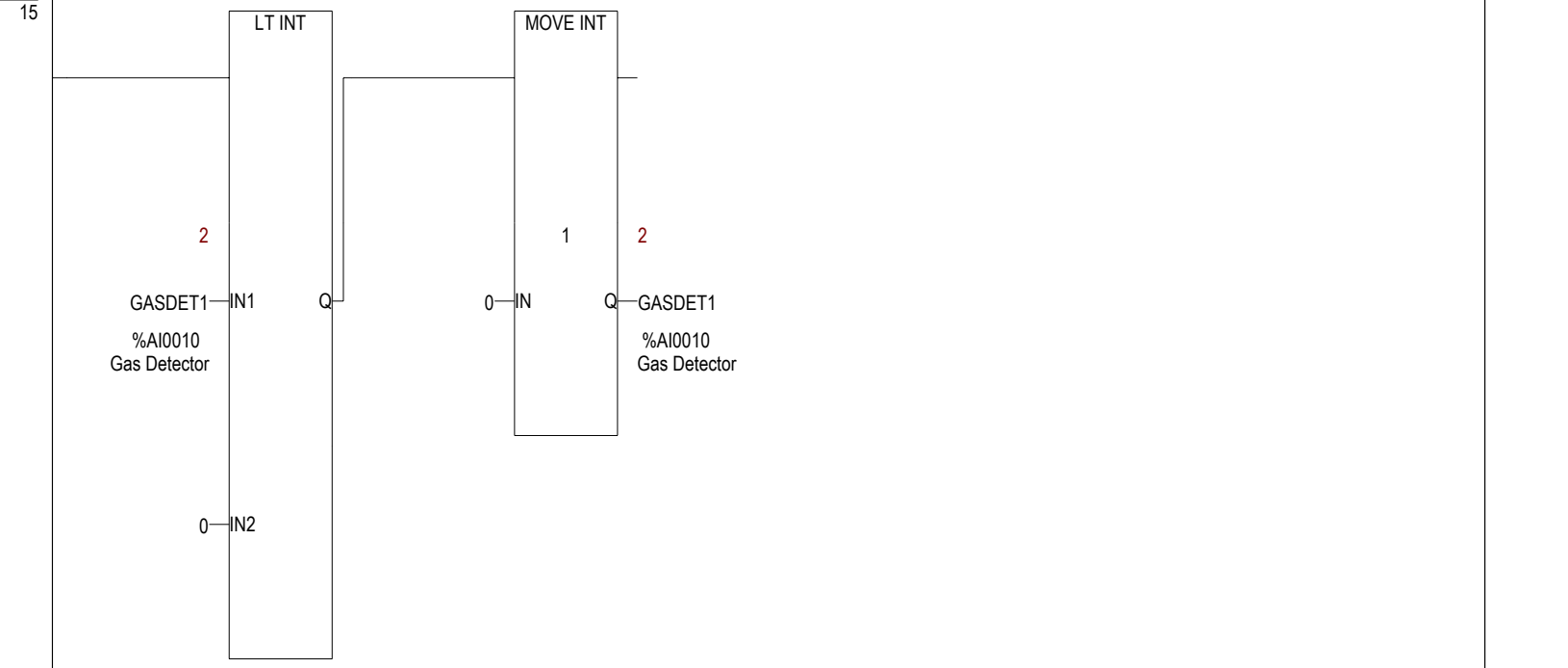












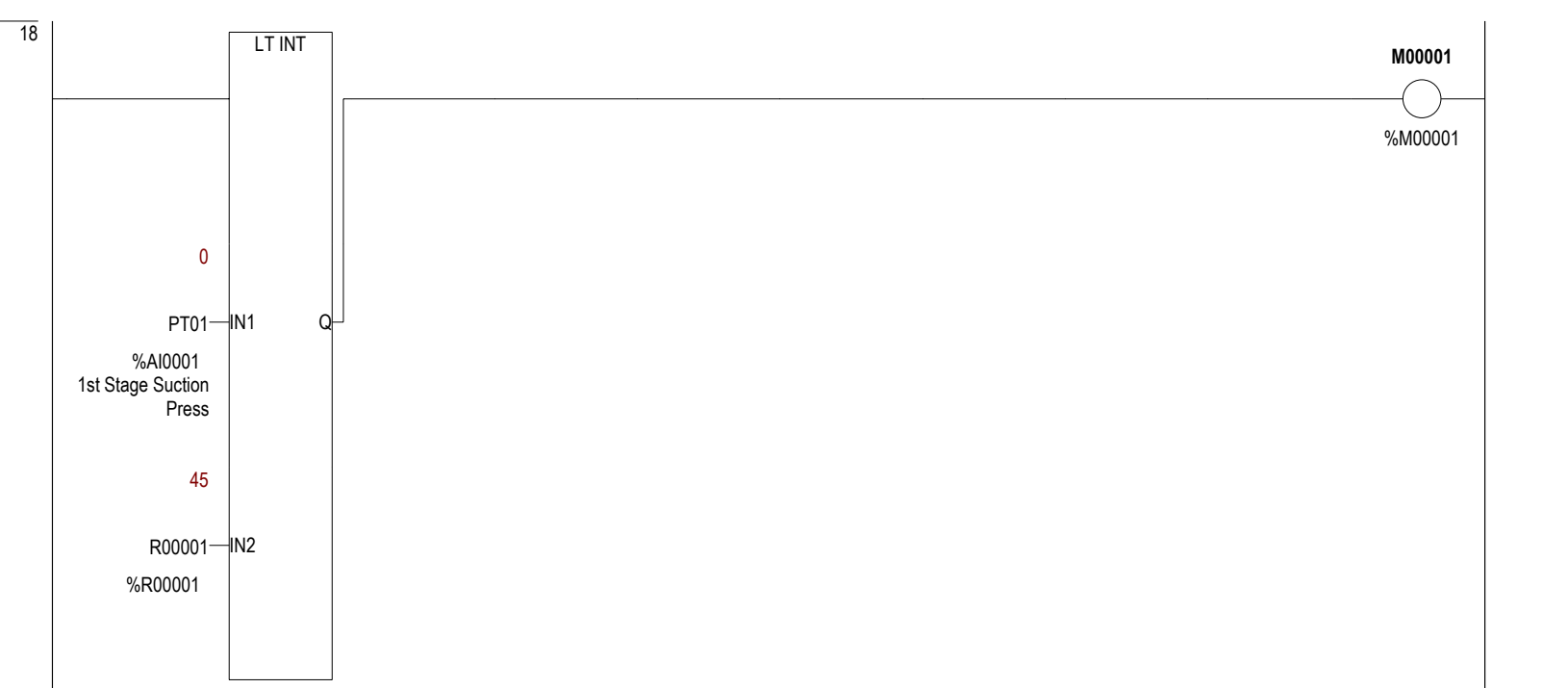
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

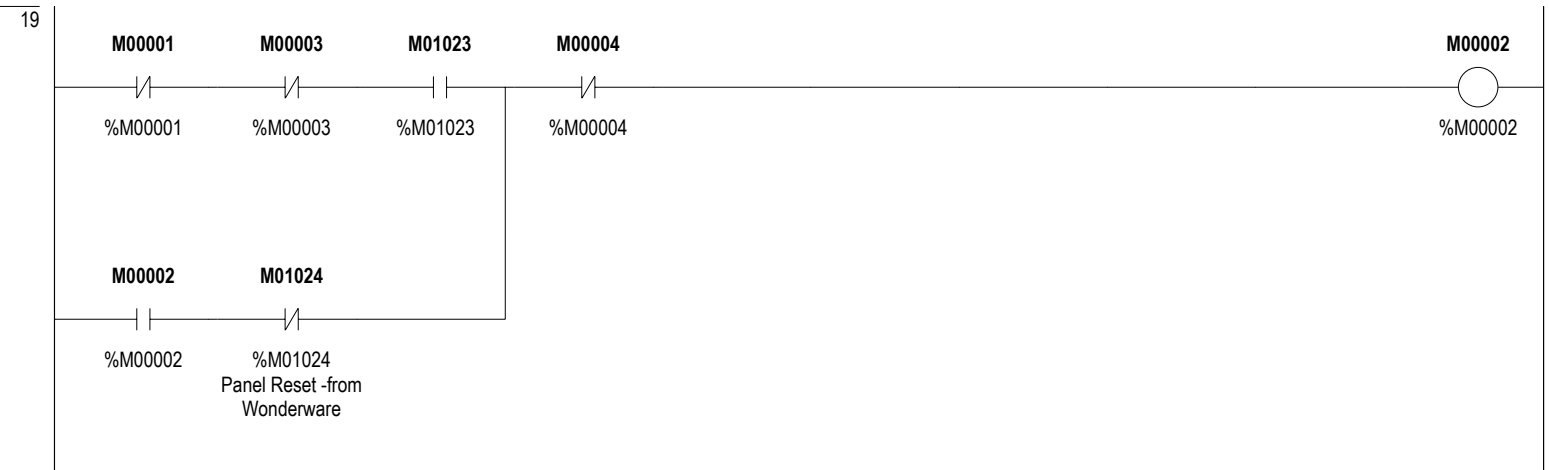
M00004 %M00004

LD Block,'FRSTOUT': NCCON 00019, 00020; NOCON 00017; COIL 00017;

LD Block,'SUMMARY': NCCON 00002;



M00001 %M00001
LD Block,'FRSTOUT': NCCON 00019, 00020; COIL 00018;



M00001 %M00001 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00018;

M00004 %M00004 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00017;

M00002 %M00002

LD Block,'FRSTOUT': NOCON 00019; COIL 00019;

LD Block,'SUMMARY': NCCON 00021;



M00001 %M00001 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00018;

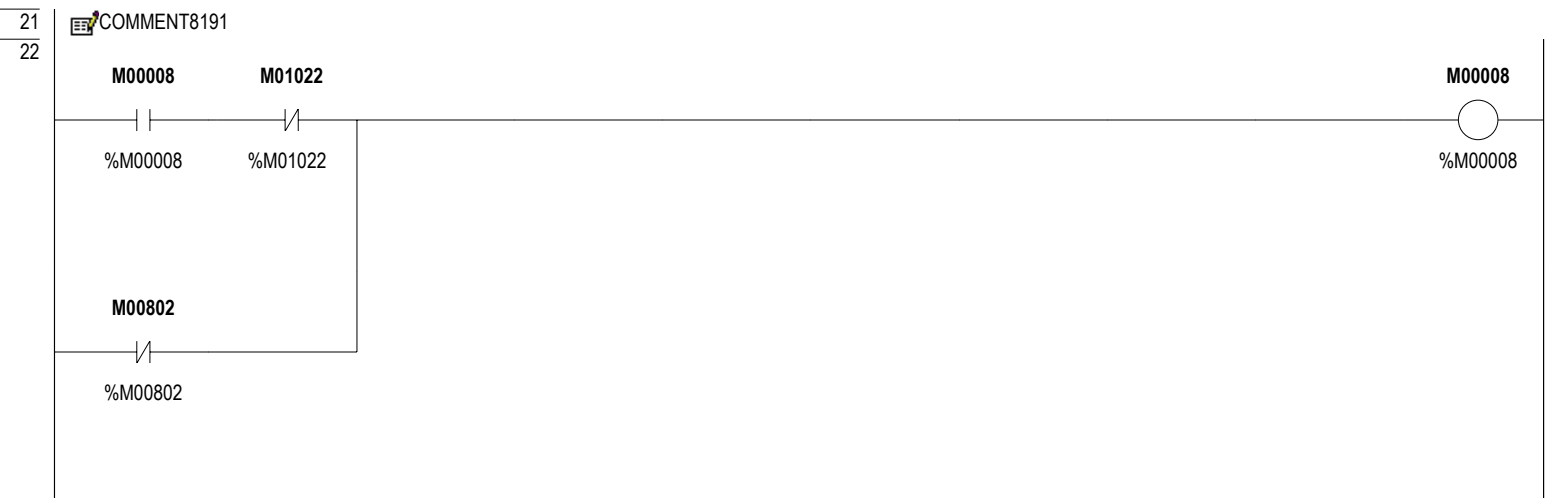
M00004 %M00004 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00017;

M00003 %M00003

LD Block,'FRSTOUT': NCCON 00019; NOCON 00020; COIL 00020;

LD Block,'SUMMARY': NOCON 00039;



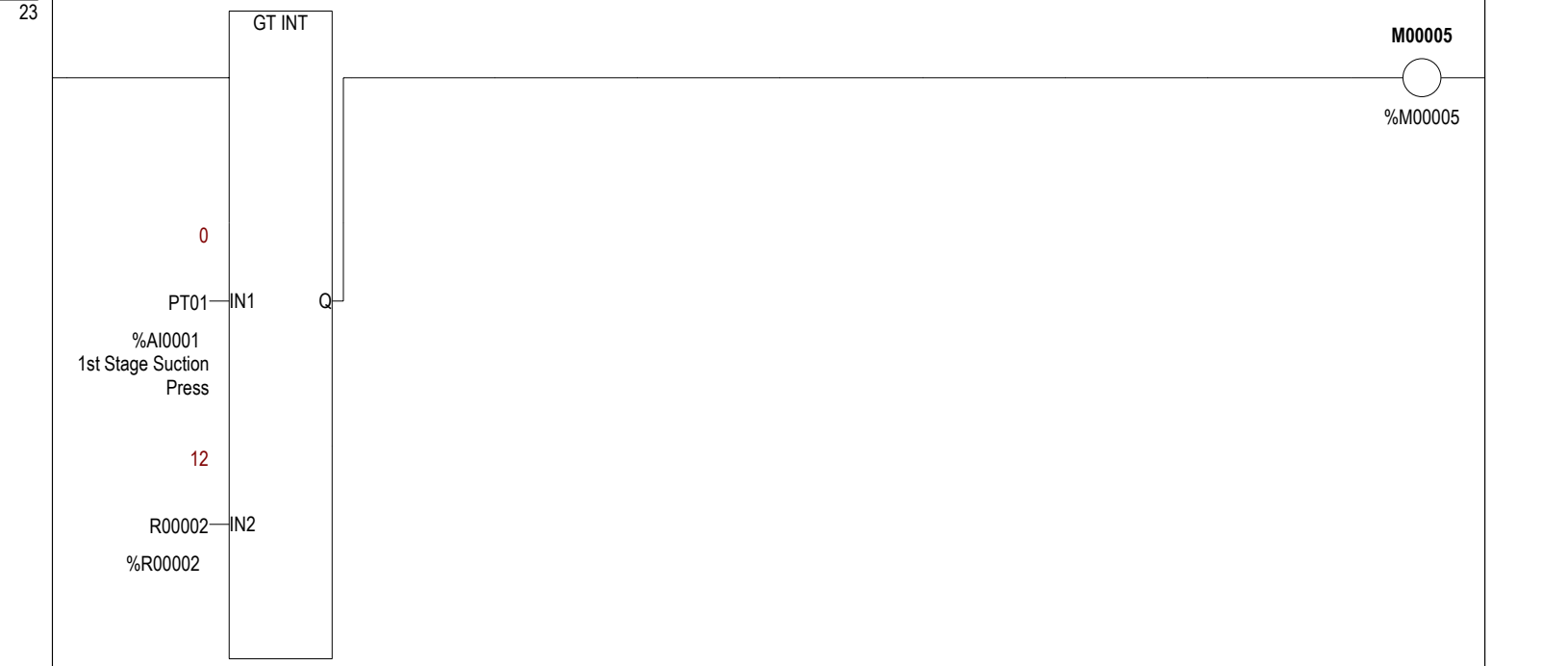
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

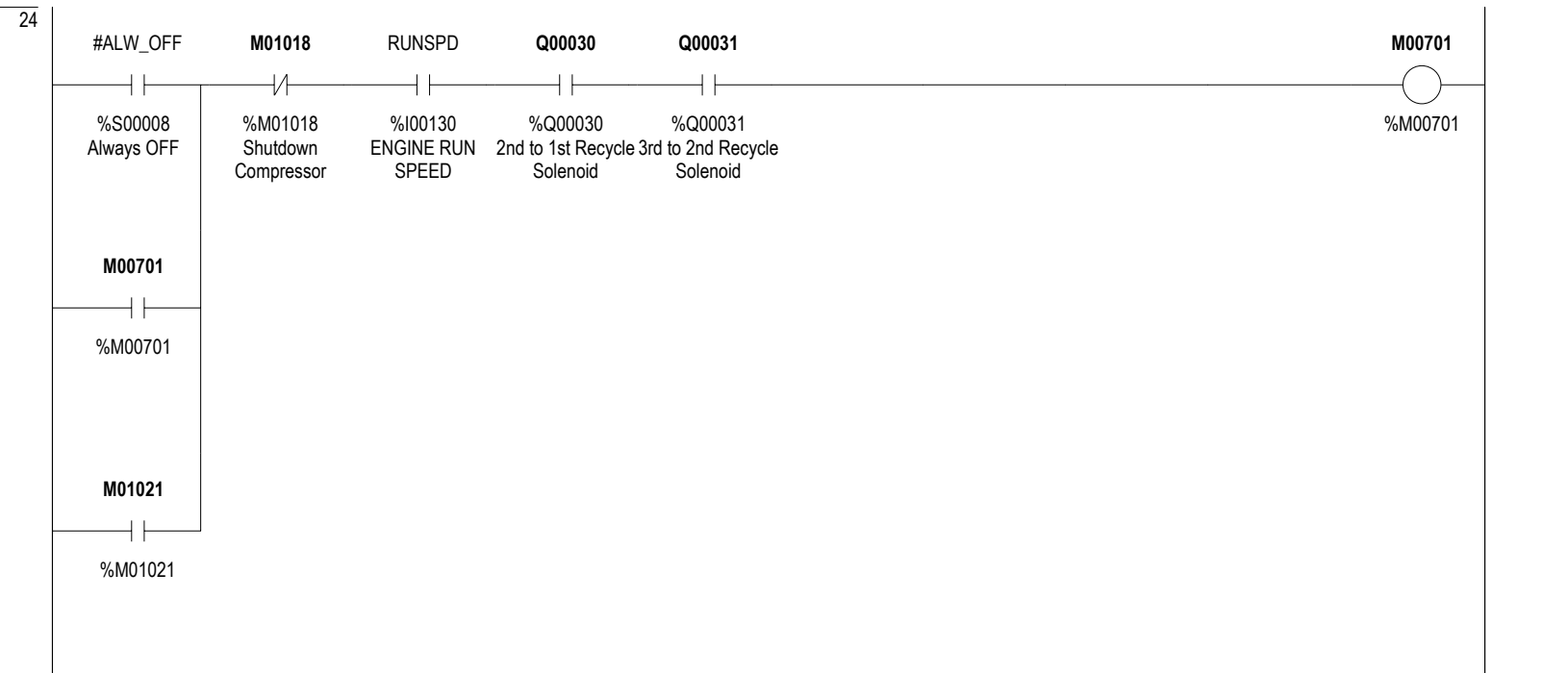
M00008 %M00008

LD Block,'FRSTOUT': NCCON 00025, 00026; NOCON 00022; COIL 00022;

LD Block,'SUMMARY': NCCON 00002;

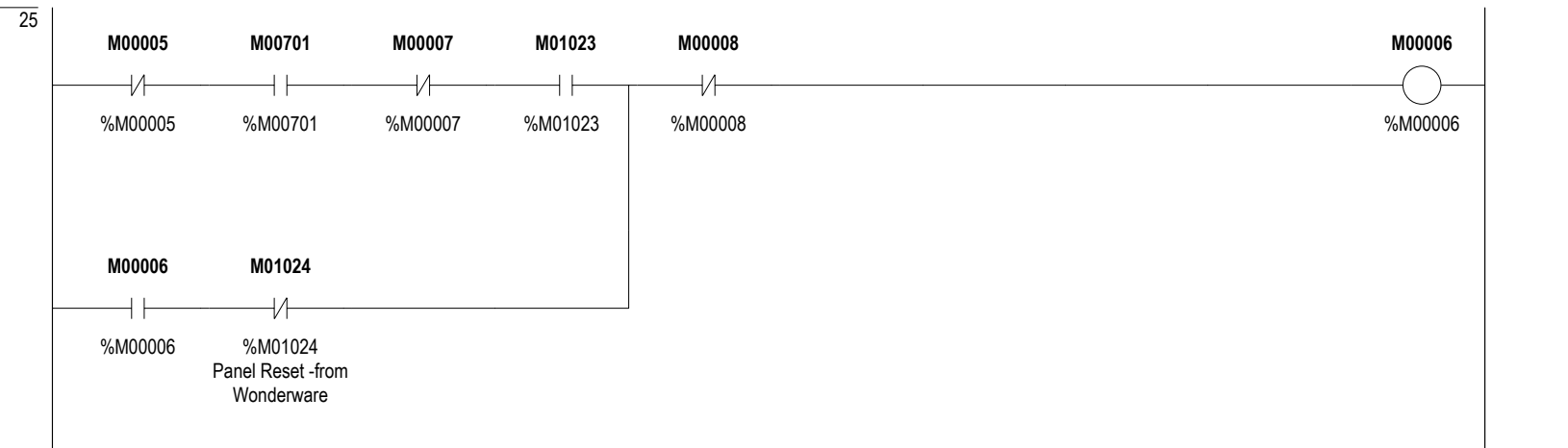


M00005 %M00005
LD Block,'FRSTOUT': NCCON 00025, 00026; COIL 00023;



M00701 %M00701
LD Block,'FRSTOUT': NOCON 00024, 00025, 00026; COIL 00024;

M01021 %M01021 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00002;



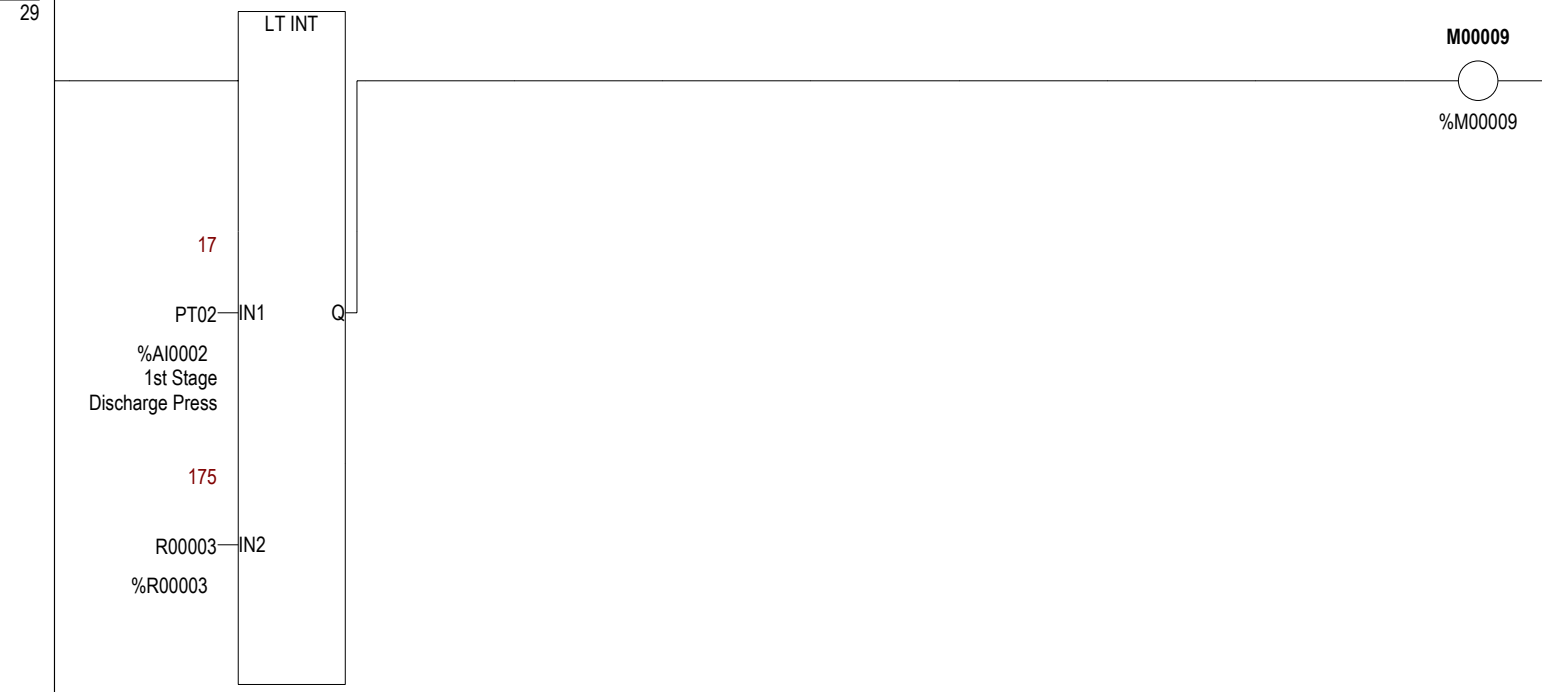
M00005 %M00005 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00023;
M00701 %M00701 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00024;
M00008 %M00008 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00022;
M00006 %M00006
LD Block,'FRSTOUT': NOCON 00025; COIL 00025;
LD Block,'SUMMARY': NCCON 00021;



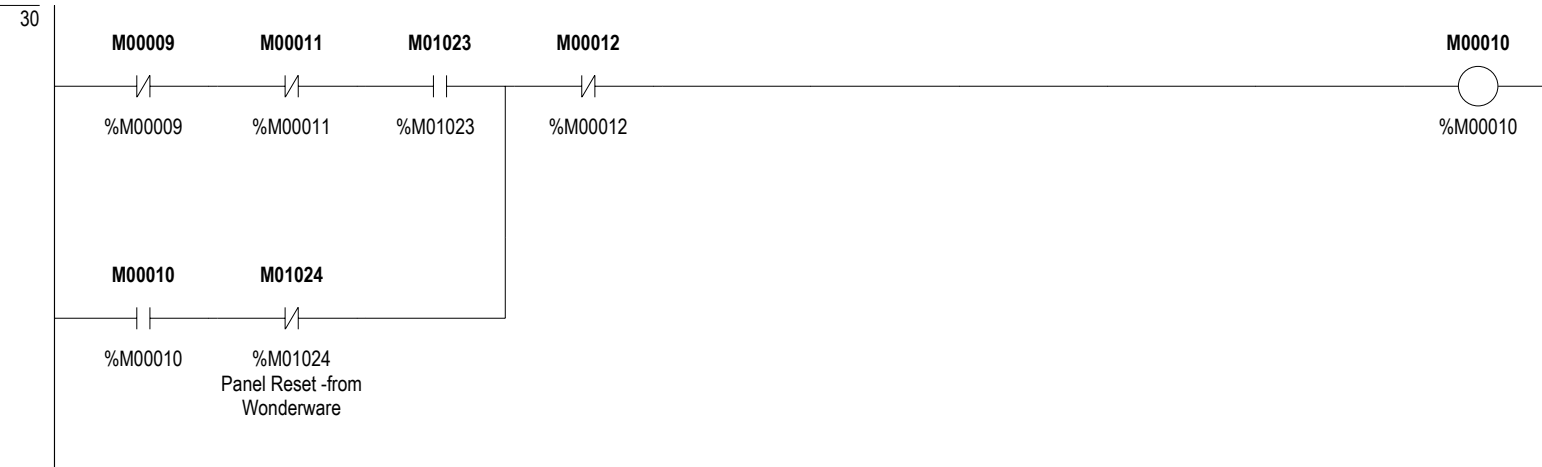
M00005 %M00005 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00023;
M00701 %M00701 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00024;
M00008 %M00008 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00022;
M00007 %M00007
LD Block,'FRSTOUT': NCCON 00025; NOCON 00026; COIL 00026;
LD Block,'SUMMARY': NOCON 00039;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00012 %M00012
LD Block,'FRSTOUT': NCCON 00030, 00031; NOCON 00028; COIL 00028;
LD Block,'SUMMARY': NCCON 00002;



M00009 %M00009
LD Block,'FRSTOUT': NCCON 00030, 00031; COIL 00029;



M00009 %M00009 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00029;

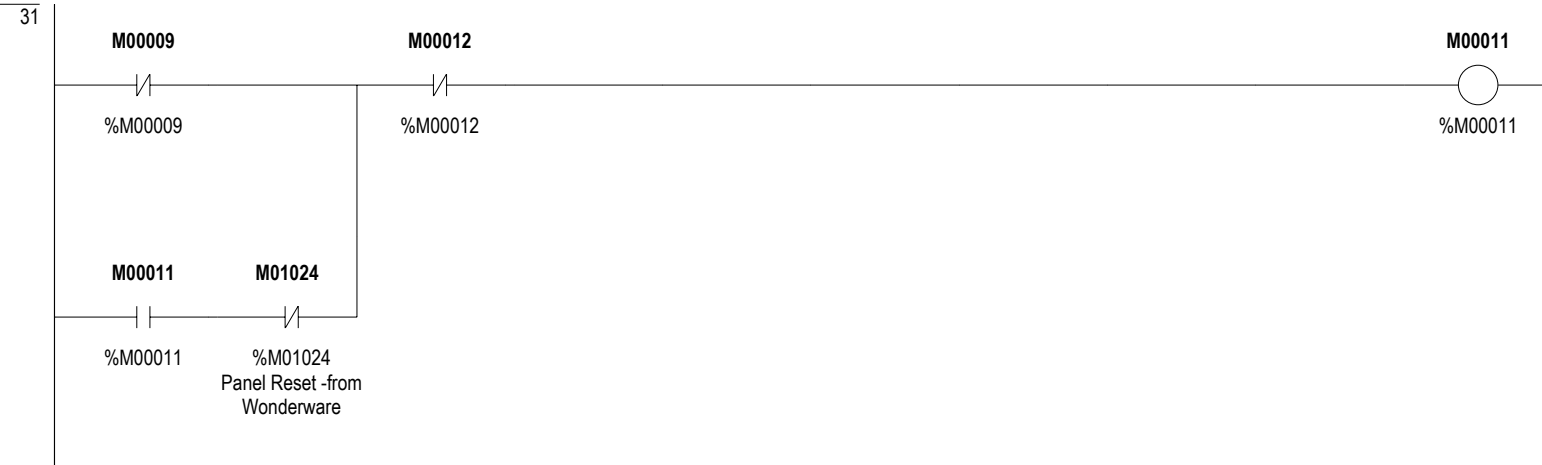
M00012 %M00012 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00028;

M00010 %M00010

LD Block,'FRSTOUT': NOCON 00030; COIL 00030;

LD Block,'SUMMARY': NCCON 00021;



M00009 %M00009 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00029;

M00012 %M00012 (Controlling Rung Reference)

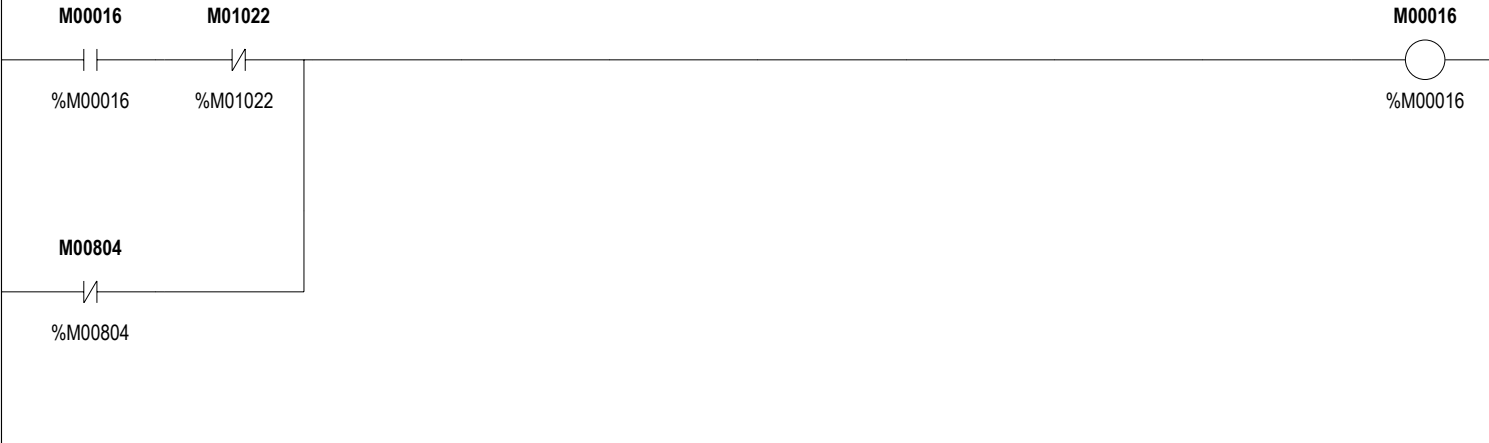
LD Block,'FRSTOUT': COIL 00028;

M00011 %M00011

LD Block,'FRSTOUT': NCCON 00030; NOCON 00031; COIL 00031;

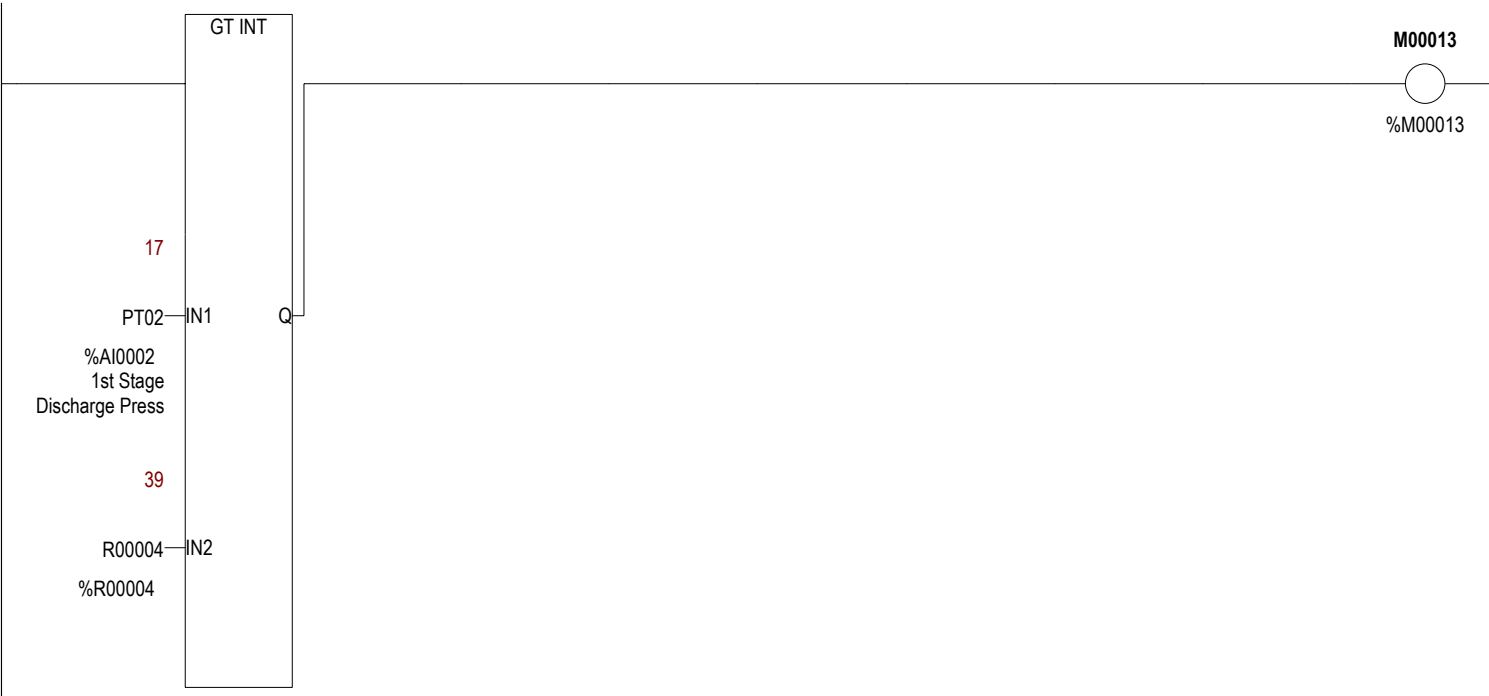
32
33

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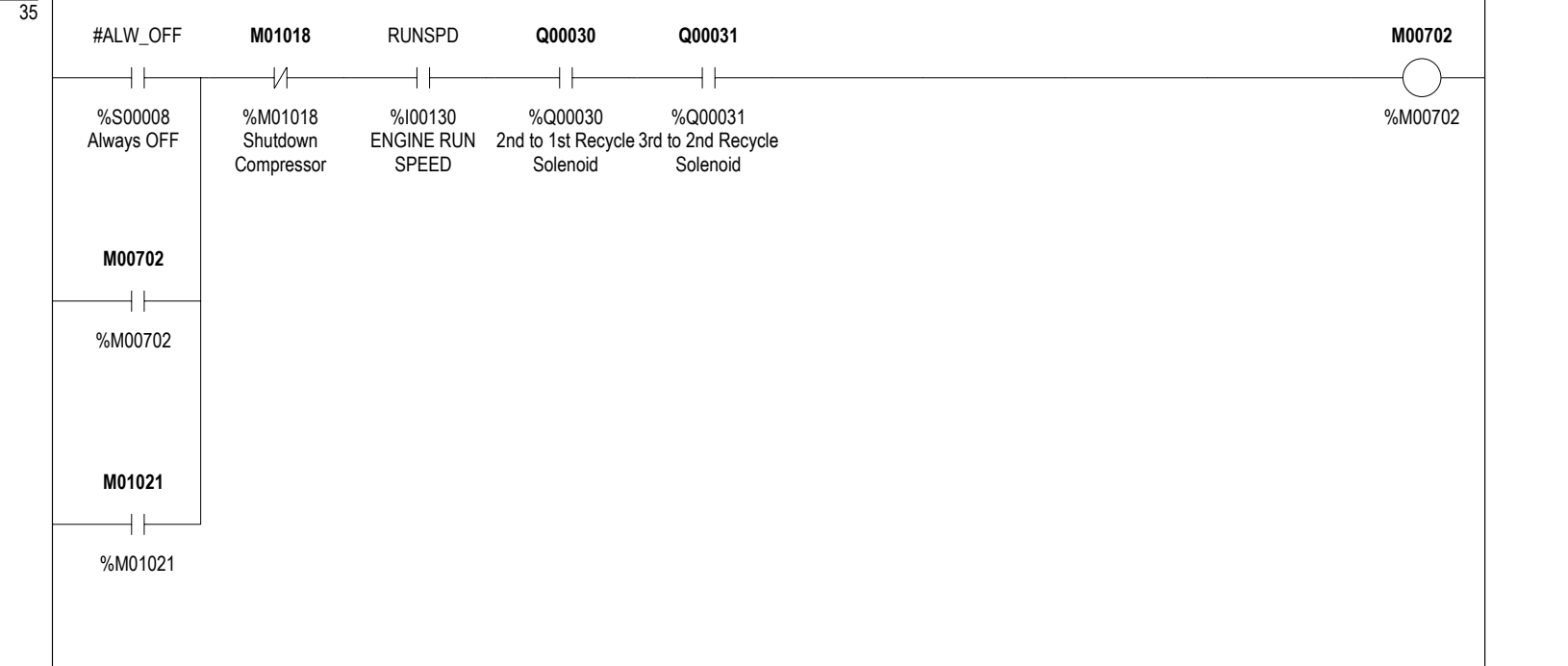


M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00016 %M00016
LD Block,'FRSTOUT': NCCON 00036, 00037; NOCON 00033; COIL 00033;
LD Block,'SUMMARY': NCCON 00002;

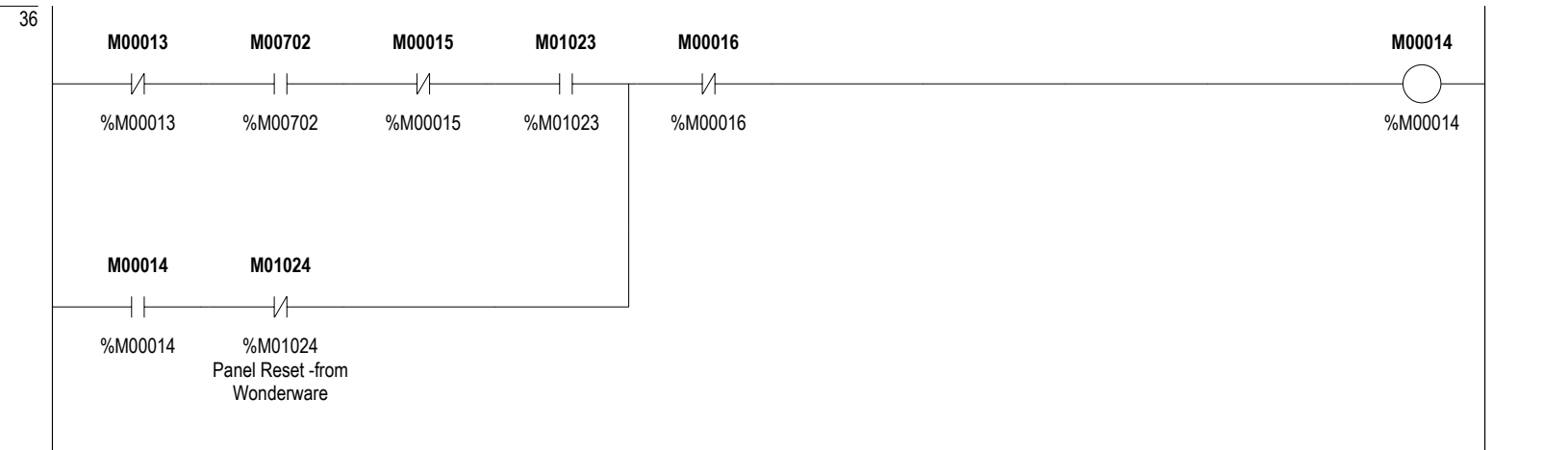
34



M00013 %M00013
LD Block,'FRSTOUT': NCCON 00036, 00037; COIL 00034;



M00702 %M00702
LD Block,'FRSTOUT': NOCON 00035, 00036, 00037; COIL 00035;
M01021 %M01021 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00002;



M00013 %M00013 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00034;
M00702 %M00702 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00035;
M00016 %M00016 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00033;
M00014 %M00014
LD Block,'FRSTOUT': NOCON 00036; COIL 00036;
LD Block,'SUMMARY': NCCON 00021;

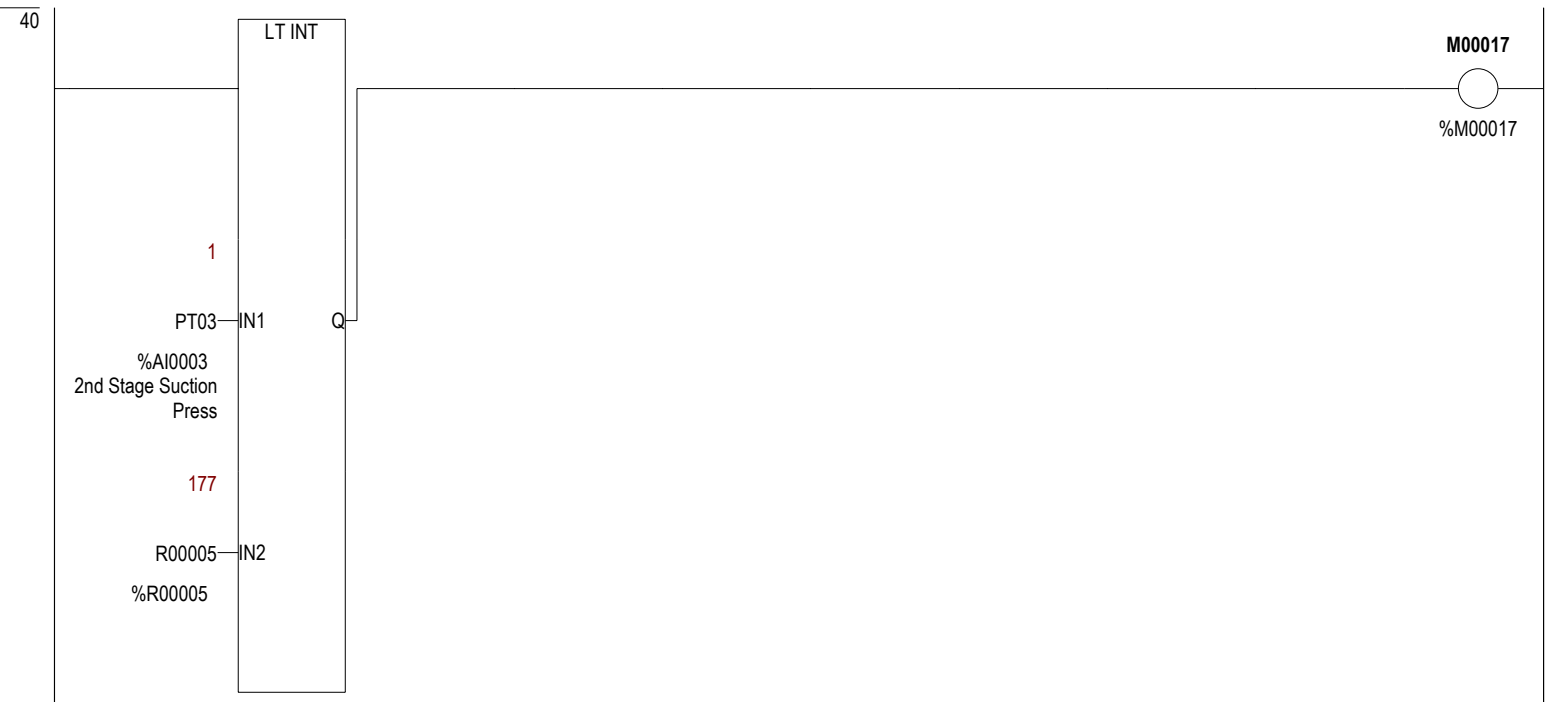


M00013 %M00013 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00034;

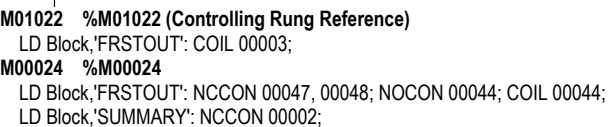
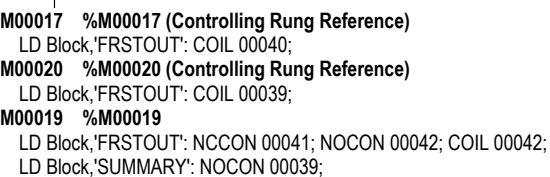
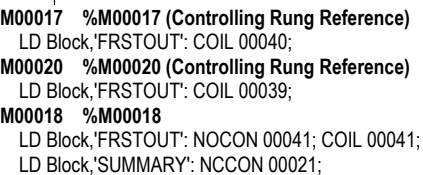
M00702 %M00702 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00035;
M00016 %M00016 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00033;
M00015 %M00015
LD Block,'FRSTOUT': NCCON 00036; NOCON 00037; COIL 00037;
LD Block,'SUMMARY': NOCON 00039;

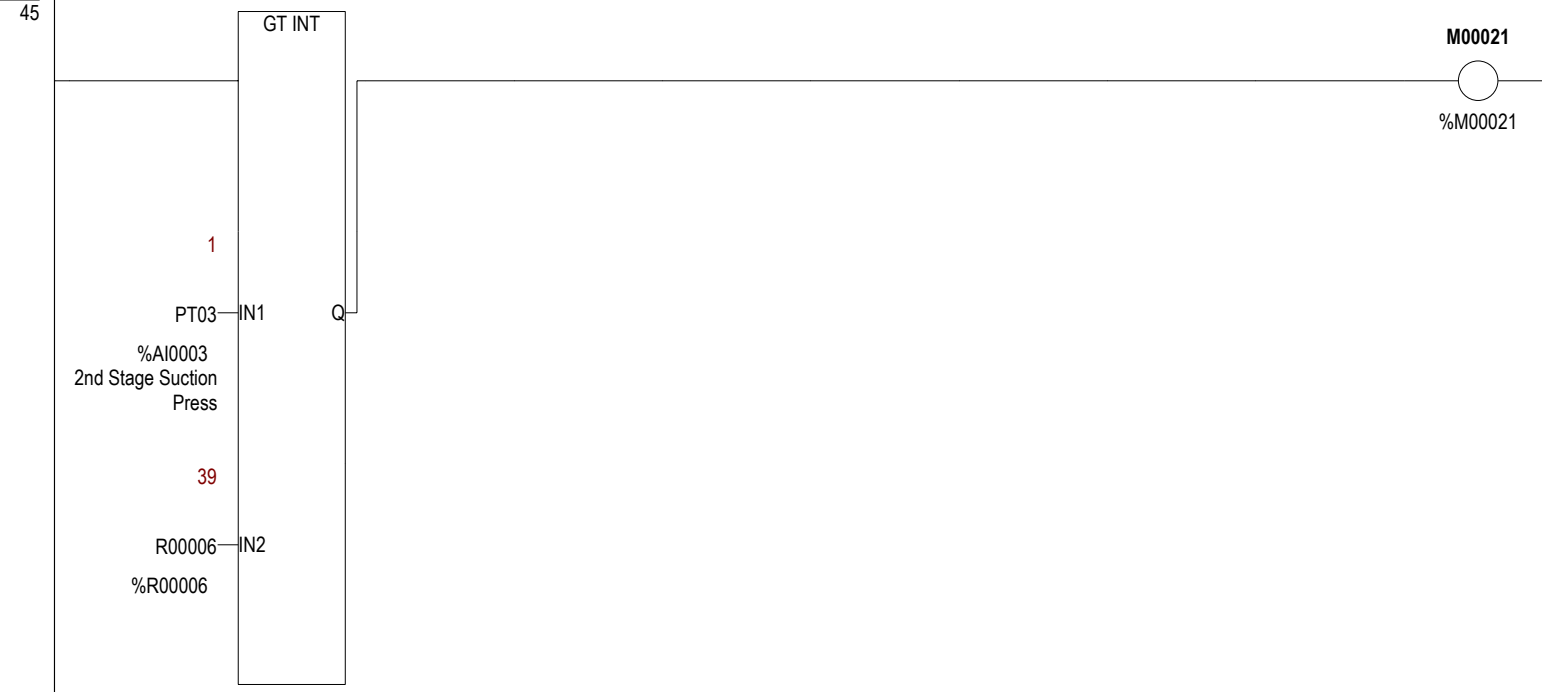


M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00020 %M00020
LD Block,'FRSTOUT': NCCON 00041, 00042; NOCON 00039; COIL 00039;
LD Block,'SUMMARY': NCCON 00002;

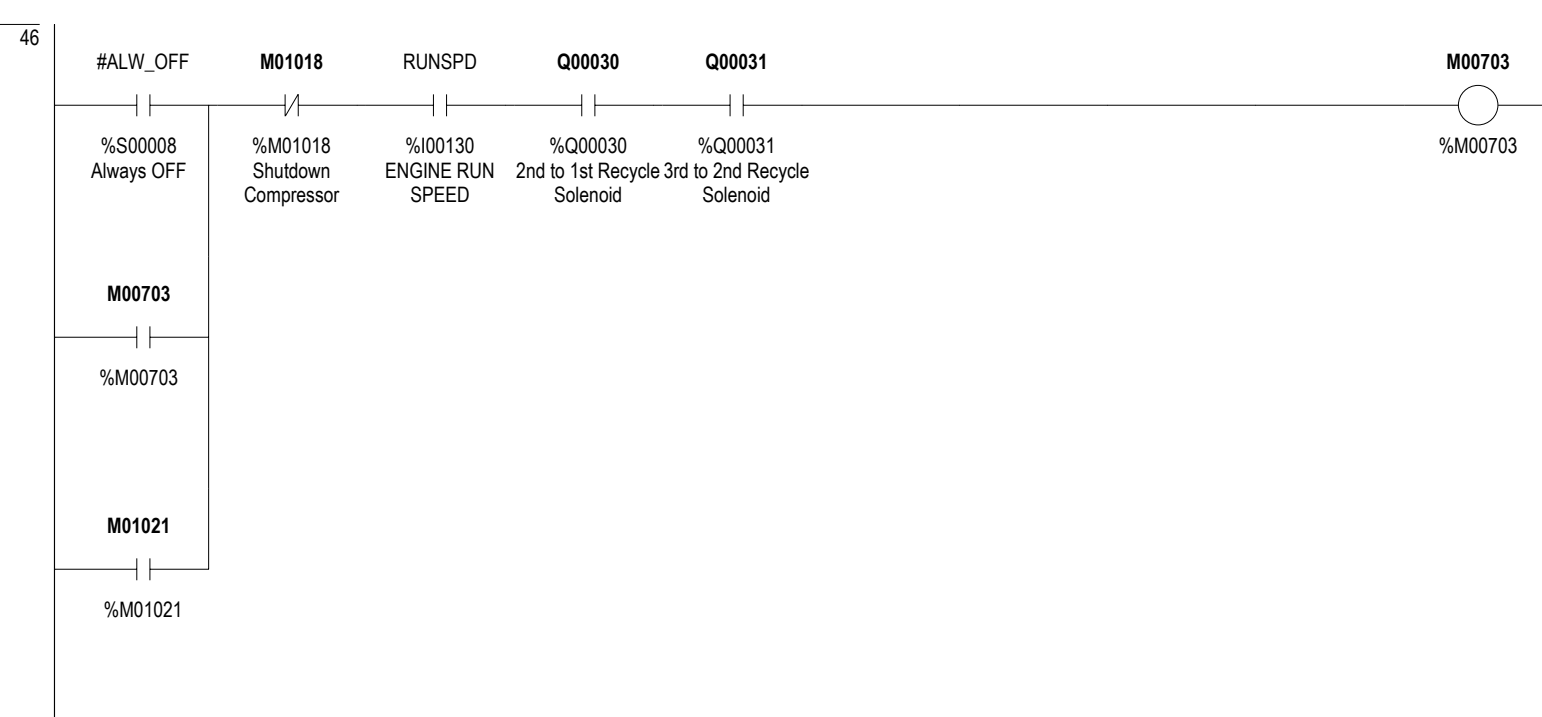


M00017 %M00017
LD Block,'FRSTOUT': NCCON 00041, 00042; COIL 00040;



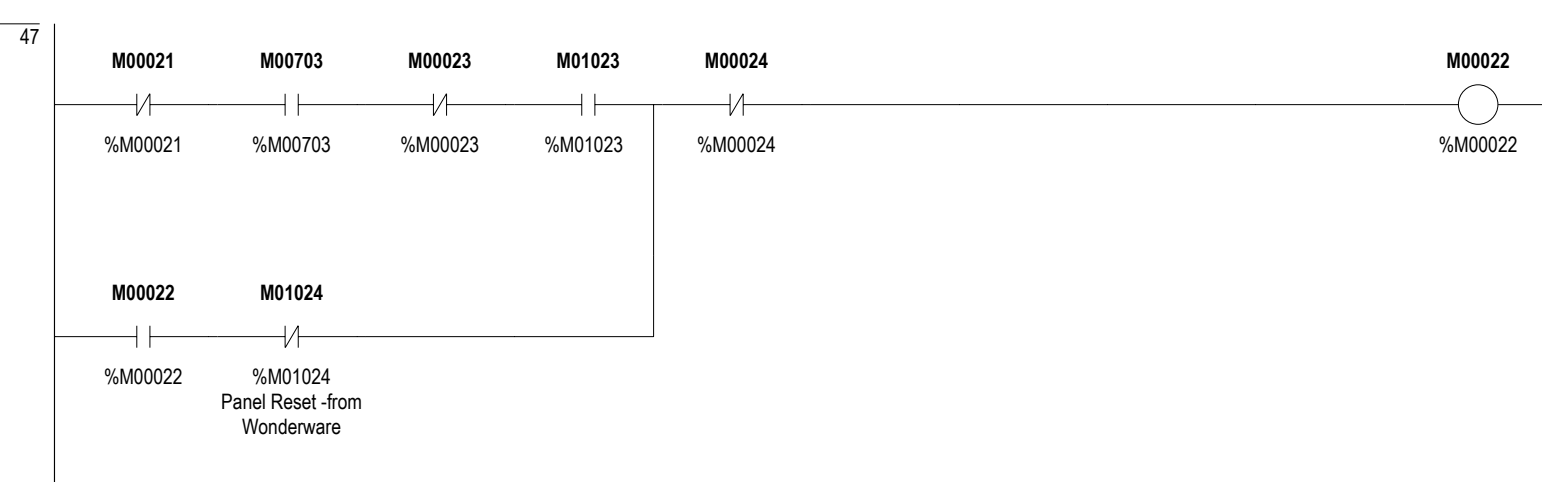


M00021 %M00021
LD Block,'FRSTOUT': NCCON 00047, 00048; COIL 00045;



M00703 %M00703
LD Block,'FRSTOUT': NOCON 00046, 00047, 00048; COIL 00046;

M01021 %M01021 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00002;



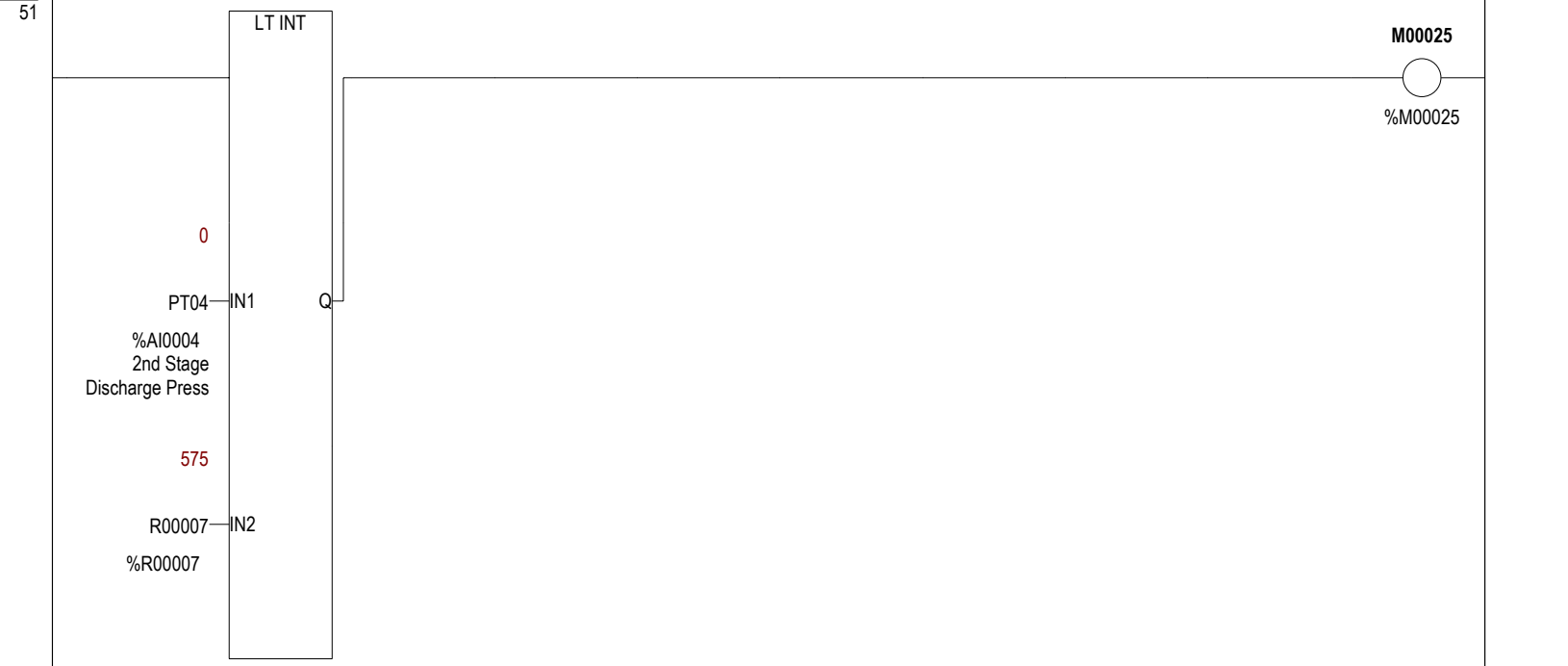
M00021 %M00021 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00045;
M00703 %M00703 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00046;
M00024 %M00024 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00044;
M00022 %M00022
LD Block,'FRSTOUT': NOCON 00047; COIL 00047;
LD Block,'SUMMARY': NCCON 00021;



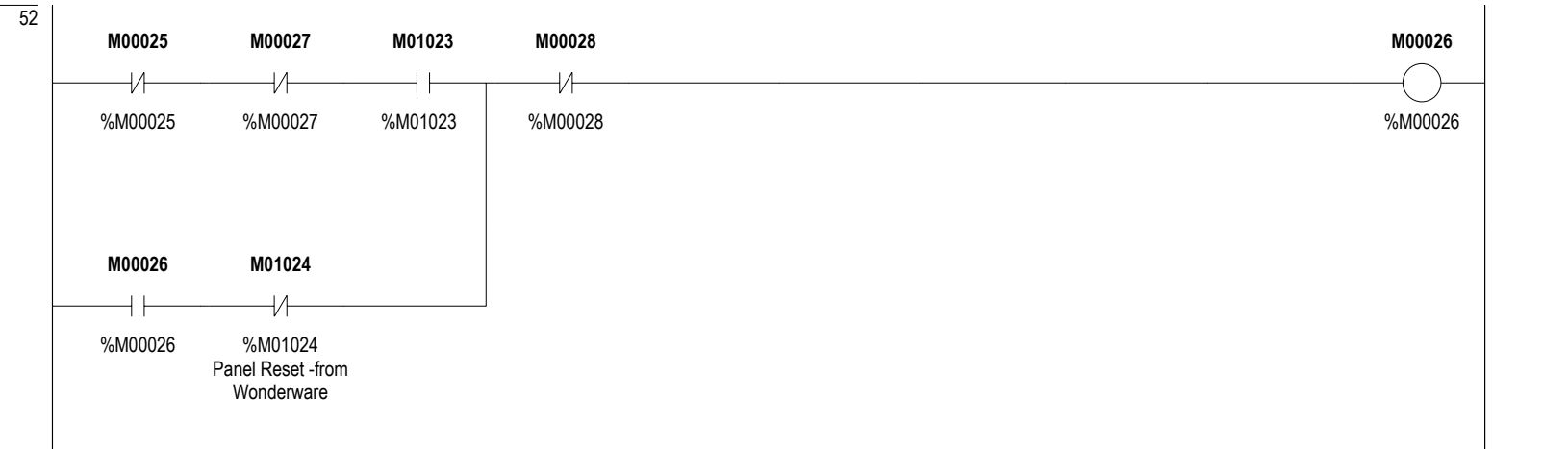
M00021 %M00021 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00045;
M00703 %M00703 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00046;
M00024 %M00024 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00044;
M00023 %M00023
LD Block,'FRSTOUT': NCCON 00047; NOCON 00048; COIL 00048;
LD Block,'SUMMARY': NOCON 00039;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00028 %M00028
LD Block,'FRSTOUT': NCCON 00052, 00053; NOCON 00050; COIL 00050;
LD Block,'SUMMARY': NCCON 00002;



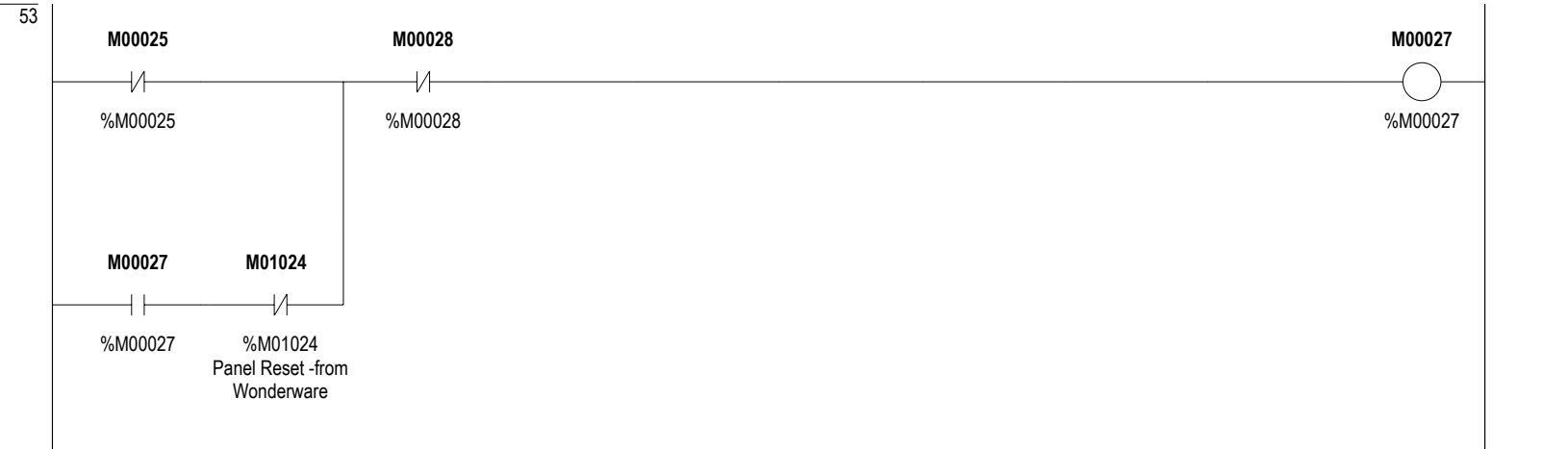
M00025 %M00025
LD Block,'FRSTOUT': NCCON 00052, 00053; COIL 00051;



M00025 %M00025 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00051;

M00028 %M00028 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00050;

M00026 %M00026
LD Block,'FRSTOUT': NOCON 00052; COIL 00052;
LD Block,'SUMMARY': NCCON 00021;



M00025 %M00025 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00051;

M00028 %M00028 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00050;

M00027 %M00027
LD Block,'FRSTOUT': NCCON 00052; NOCON 00053; COIL 00053;

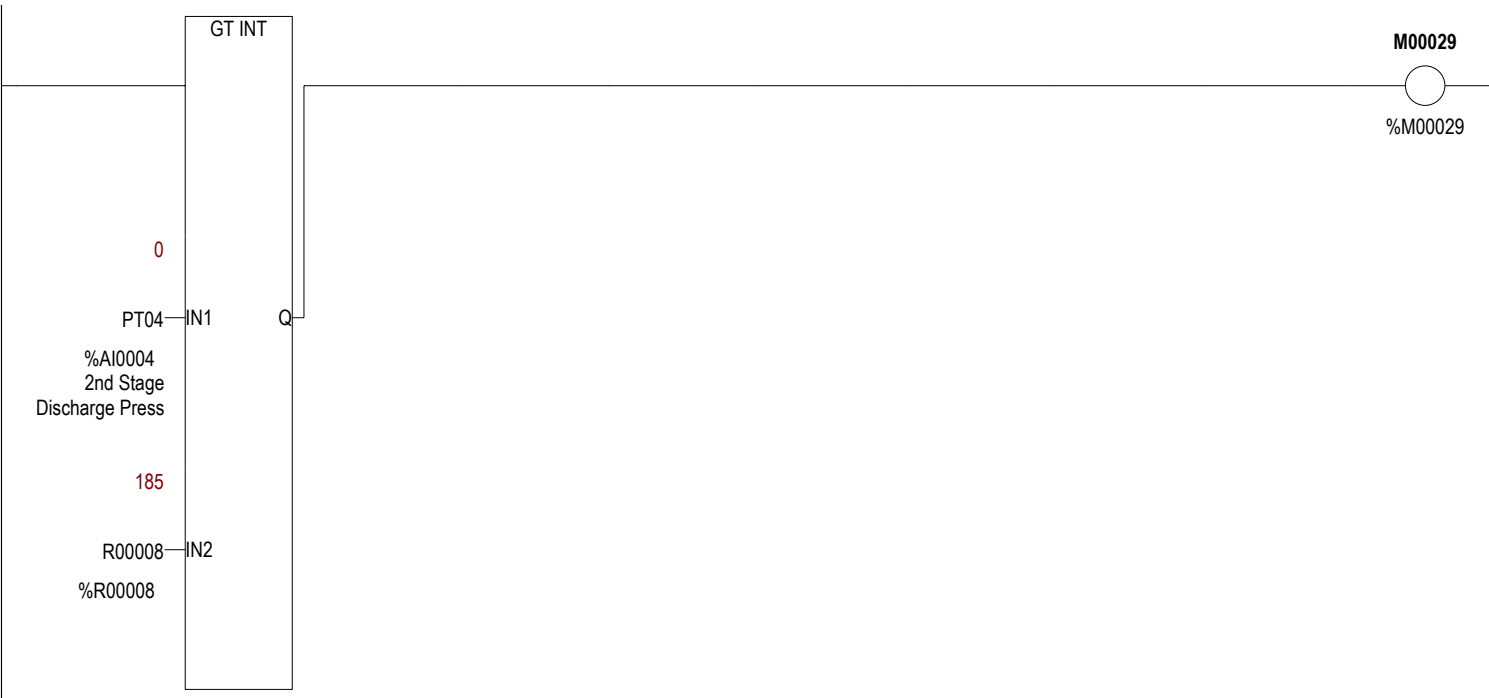
54
55

COMMENT8191

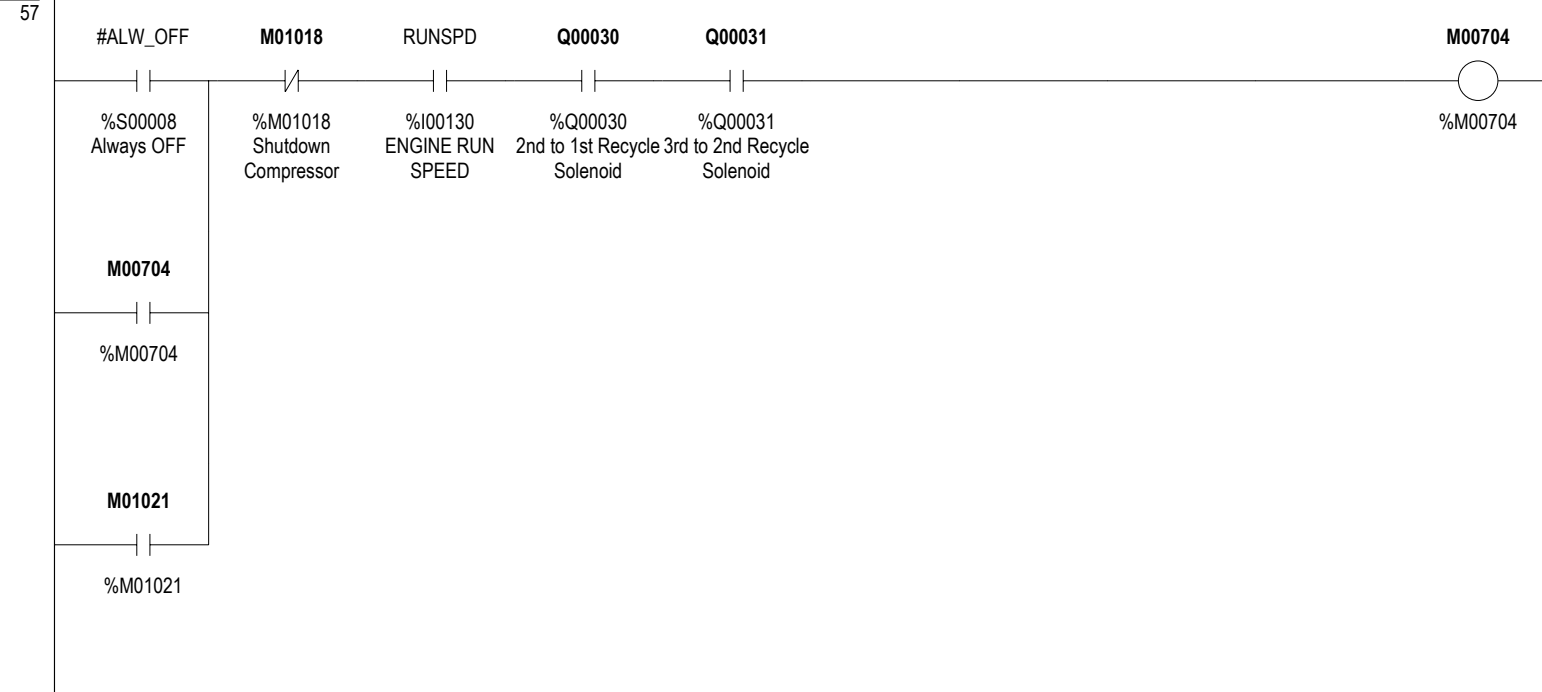


M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00032 %M00032
LD Block,'FRSTOUT': NCCON 00058, 00059; NOCON 00055; COIL 00055;
LD Block,'SUMMARY': NCCON 00002;

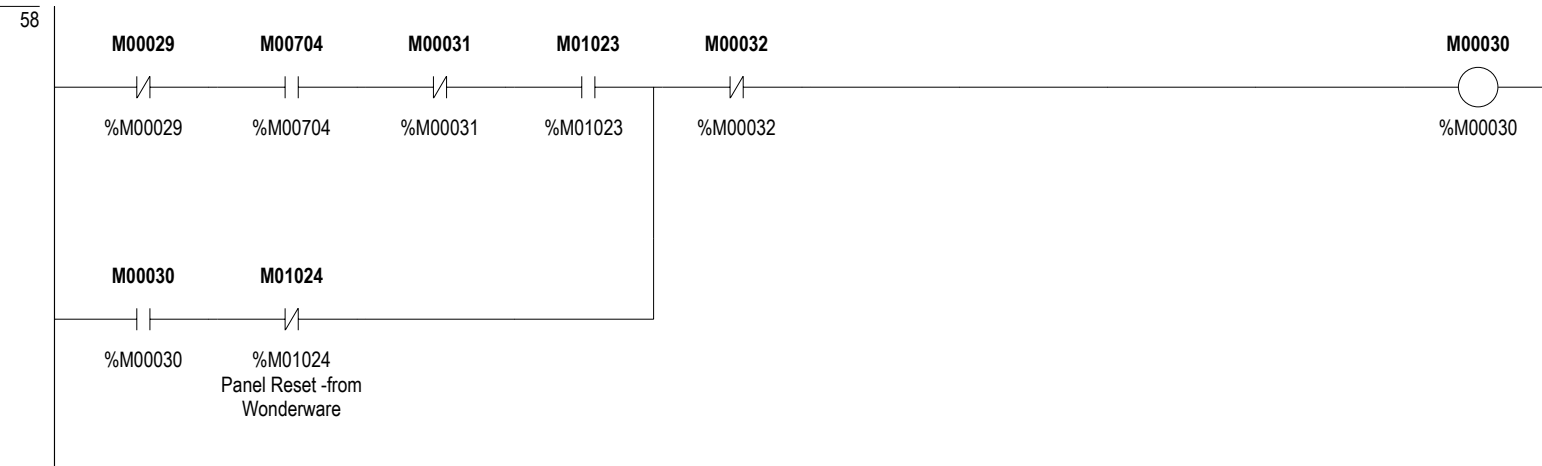
56



M00029 %M00029
LD Block,'FRSTOUT': NCCON 00058, 00059; COIL 00056;



M00704 %M00704
LD Block,'FRSTOUT': NOCON 00057, 00058, 00059; COIL 00057;
M01021 %M01021 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00002;



M00029 %M00029 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00056;
M00704 %M00704 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00057;
M00032 %M00032 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00055;
M00030 %M00030
LD Block,'FRSTOUT': NOCON 00058; COIL 00058;
LD Block,'SUMMARY': NCCON 00021;

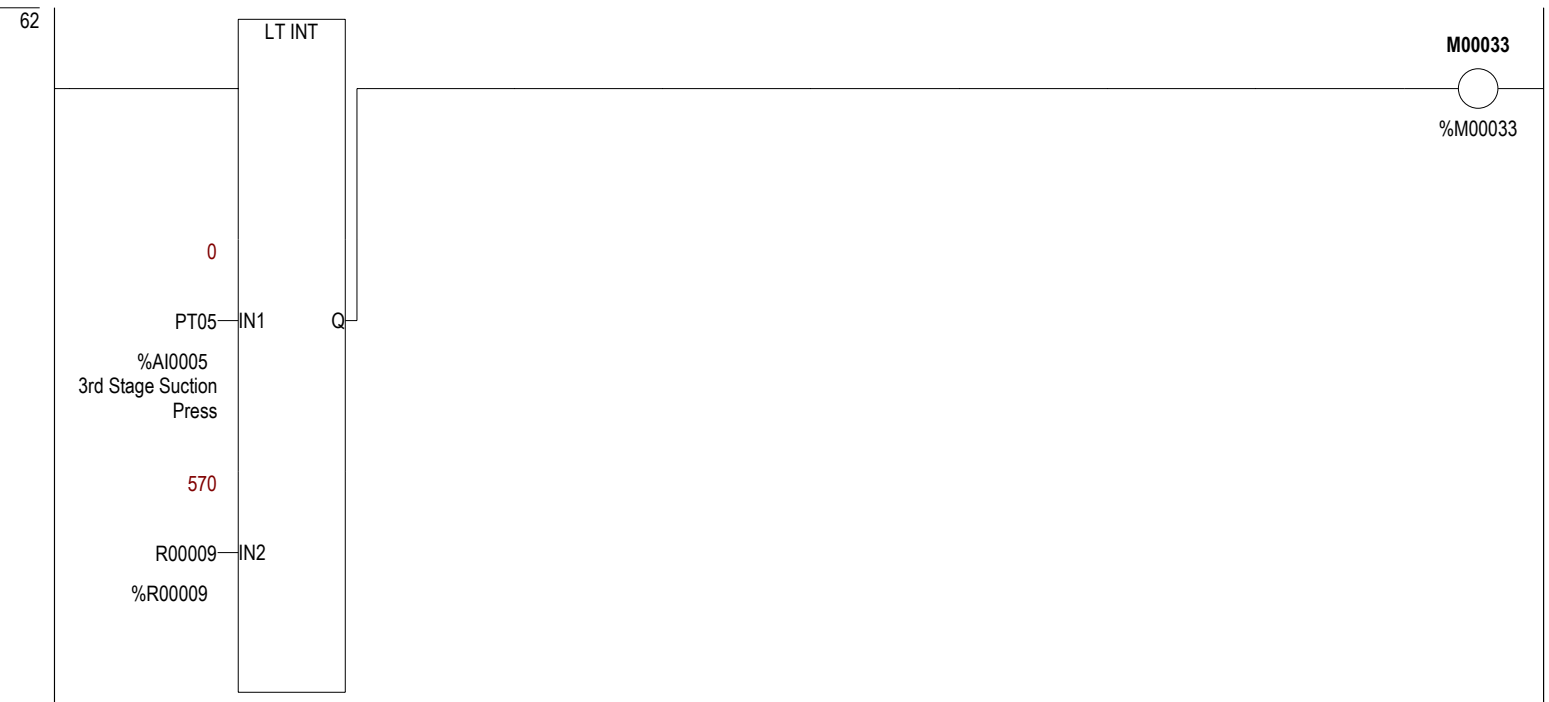


M00029 %M00029 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00056;

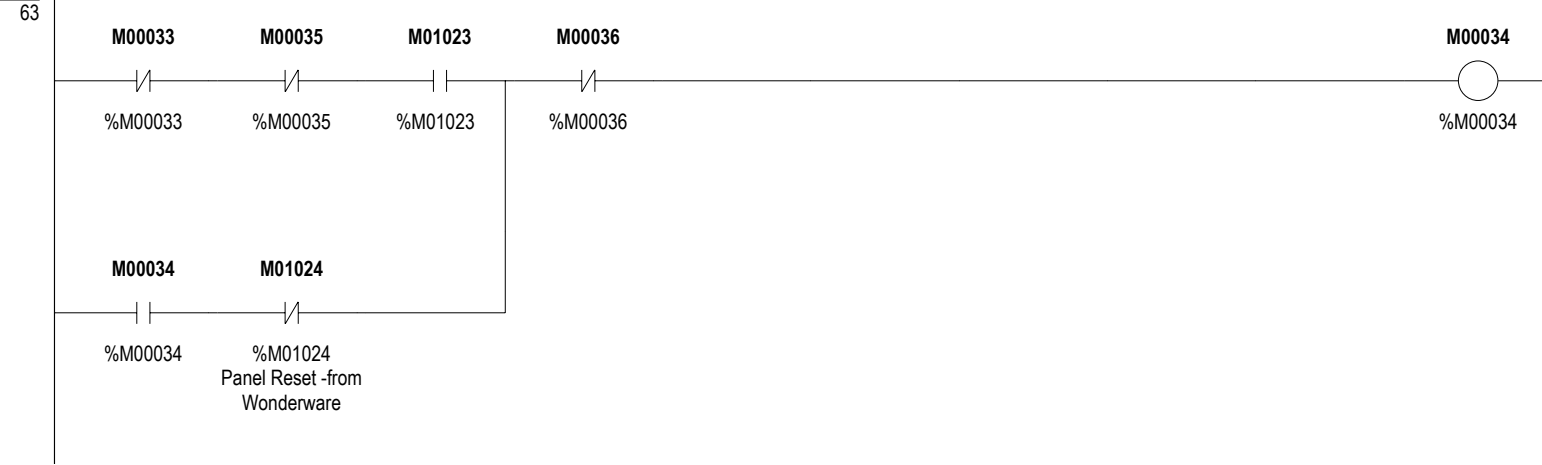
M00704 %M00704 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00057;
M00032 %M00032 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00055;
M00031 %M00031
LD Block,'FRSTOUT': NCCON 00058; NOCON 00059; COIL 00059;
LD Block,'SUMMARY': NOCON 00039;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00036 %M00036
LD Block,'FRSTOUT': NCCON 00063, 00064; NOCON 00061; COIL 00061;
LD Block,'SUMMARY': NCCON 00002;



M00033 %M00033
LD Block,'FRSTOUT': NCCON 00063, 00064; COIL 00062;



M00034

%M00034

M00033

%M00033 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00062;

M00036

%M00036 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00061;

M00034

%M00034

LD Block,'FRSTOUT': NOCON 00063; COIL 00063;

LD Block,'SUMMARY': NCCON 00021;



M00033

%M00033 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00062;

M00036

%M00036 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00061;

M00035

%M00035

LD Block,'FRSTOUT': NCCON 00063; NOCON 00064; COIL 00064;

LD Block,'SUMMARY': NOCON 00040;



M01022

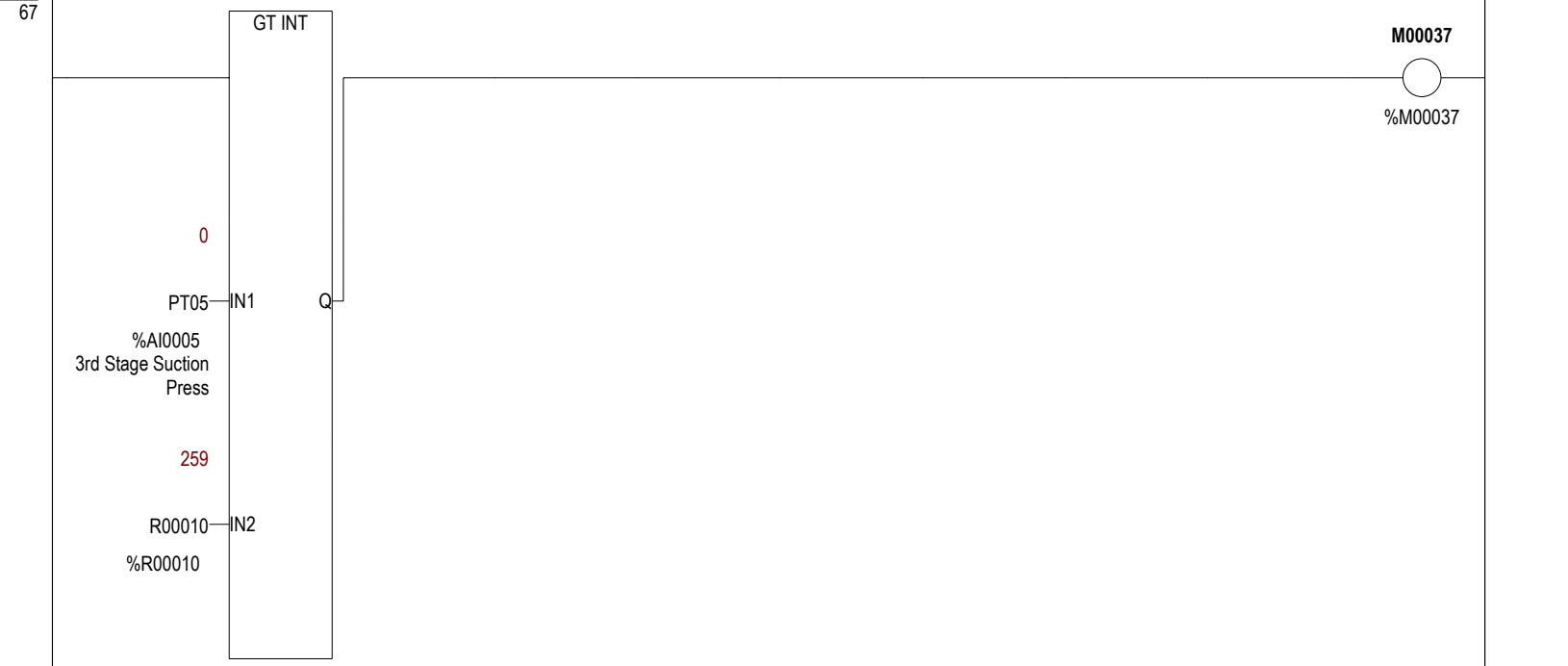
%M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

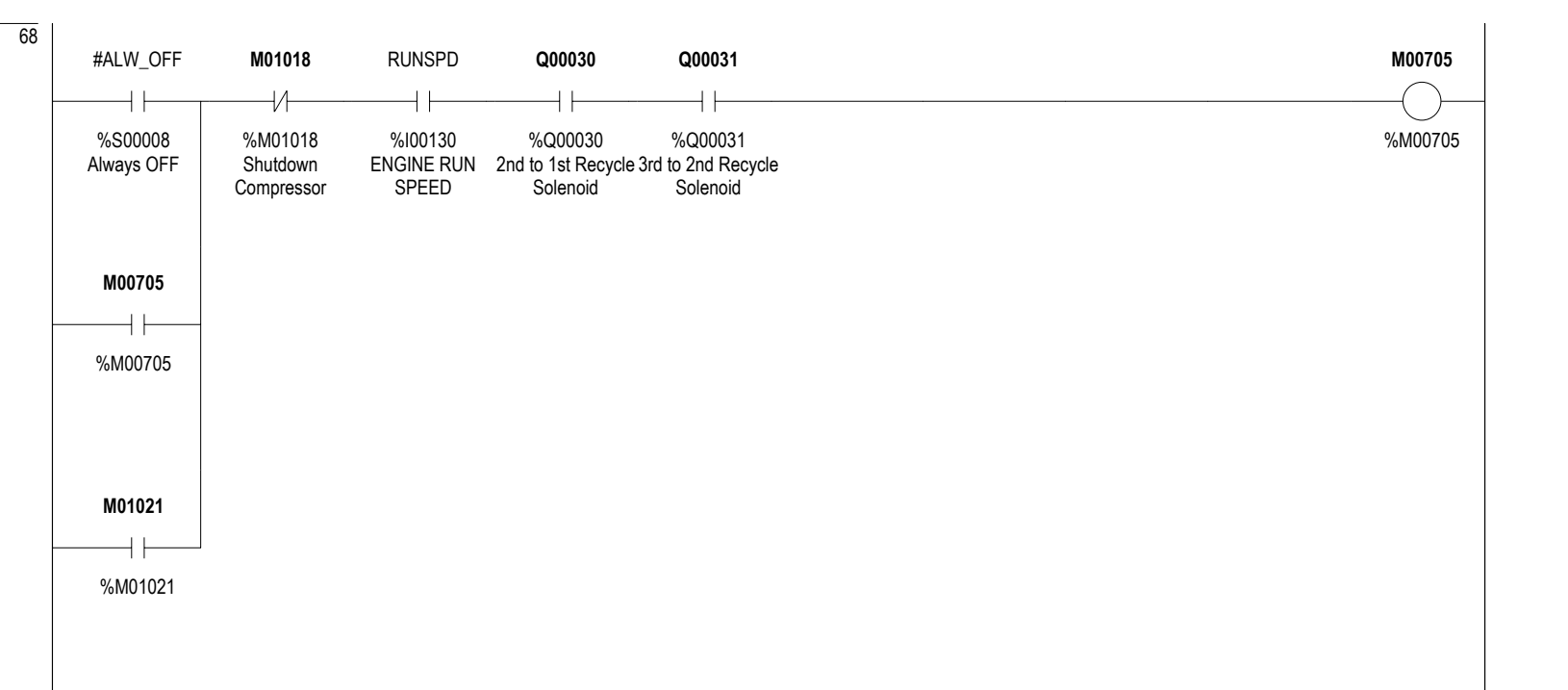
M00040

%M00040

LD Block,'FRSTOUT': NCCON 00069, 00070; NOCON 00066; COIL 00066;

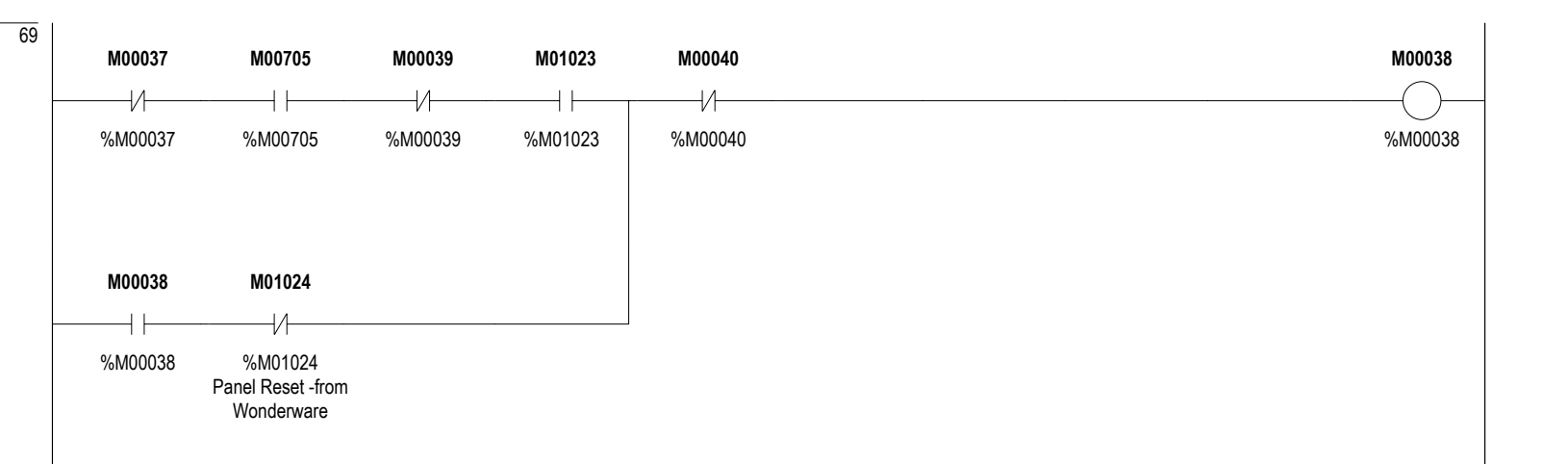


M00037 %M00037
LD Block,'FRSTOUT': NCCON 00069, 00070; COIL 00067;



M00705 %M00705
LD Block,'FRSTOUT': NOCON 00068, 00069, 00070; COIL 00068;

M01021 %M01021 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00002;



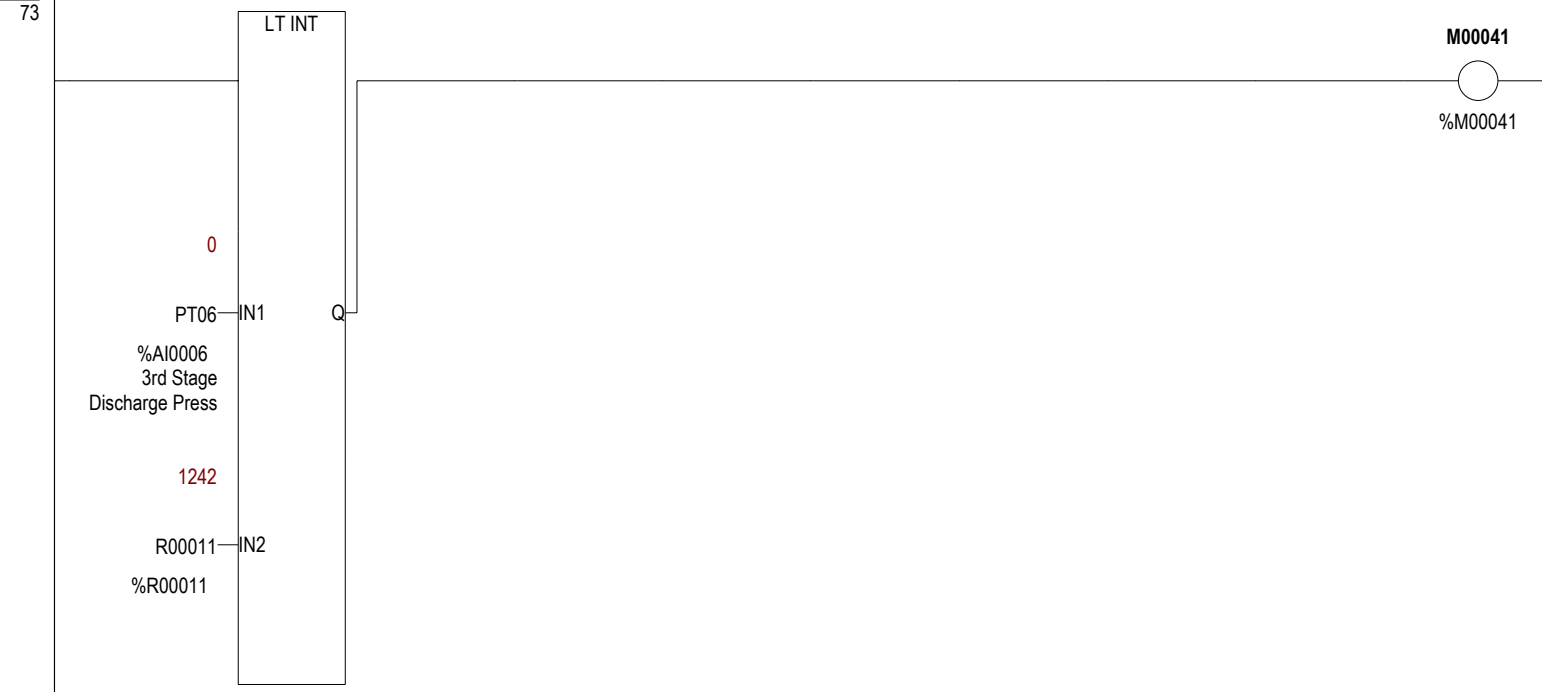
M00037 %M00037 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00067;
M00705 %M00705 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00068;
M00040 %M00040 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00066;
M00038 %M00038
LD Block,'FRSTOUT': NOCON 00069; COIL 00069;
LD Block,'SUMMARY': NCCON 00022;



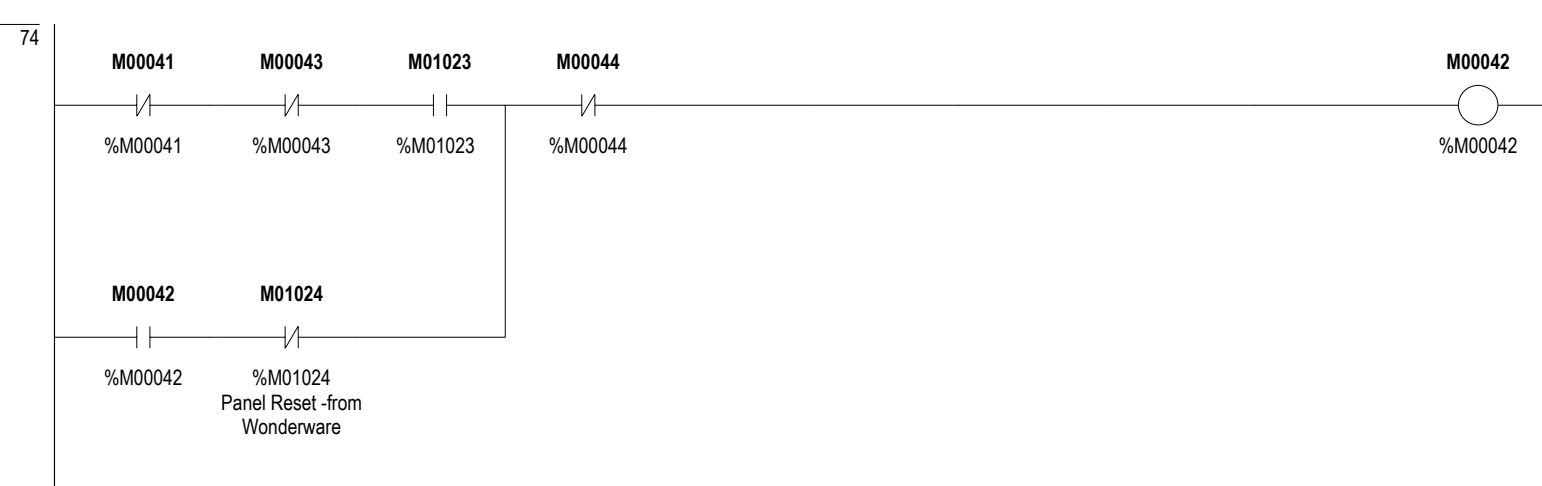
M00037 %M00037 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00067;
M00705 %M00705 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00068;
M00040 %M00040 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00066;
M00039 %M00039
LD Block,'FRSTOUT': NCCON 00069; NOCON 00070; COIL 00070;
LD Block,'SUMMARY': NOCON 00040;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00044 %M00044
LD Block,'FRSTOUT': NCCON 00074, 00075; NOCON 00072; COIL 00072;
LD Block,'SUMMARY': NCCON 00003;



M00041 %M00041
LD Block,'FRSTOUT': NCCON 00074, 00075; COIL 00073;



M00041 %M00041 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00073;

M00044 %M00044 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00072;

M00042 %M00042

LD Block,'FRSTOUT': NOCON 00074; COIL 00074;

LD Block,'SUMMARY': NCCON 00022;



M00041 %M00041 (Controlling Rung Reference)

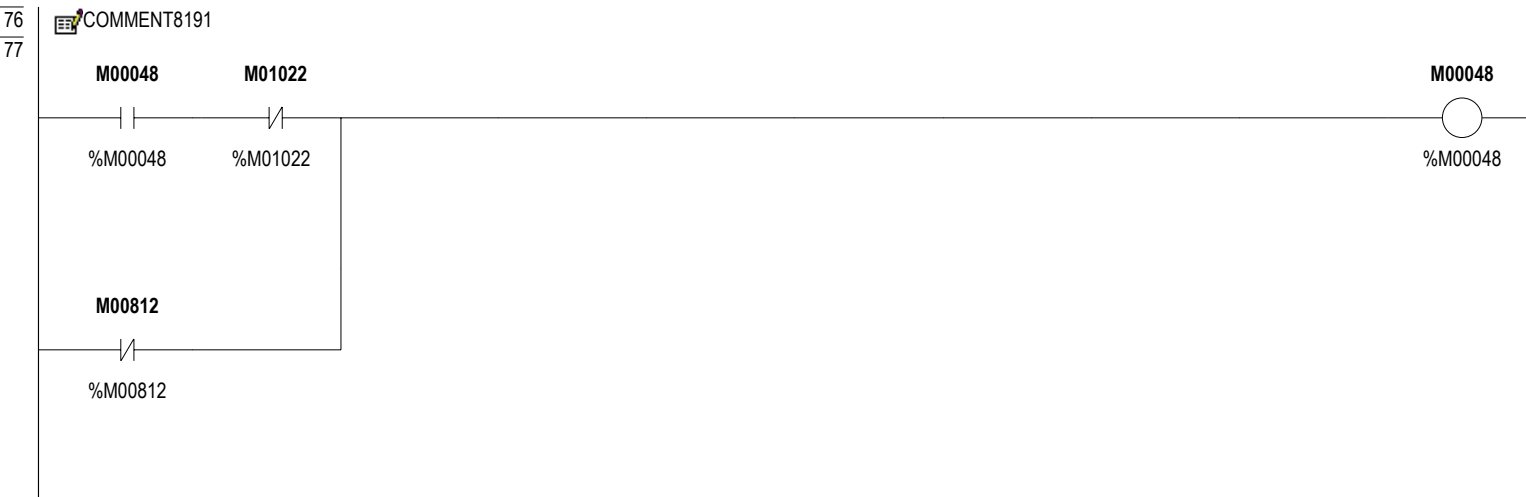
LD Block,'FRSTOUT': COIL 00073;

M00044 %M00044 (Controlling Rung Reference)

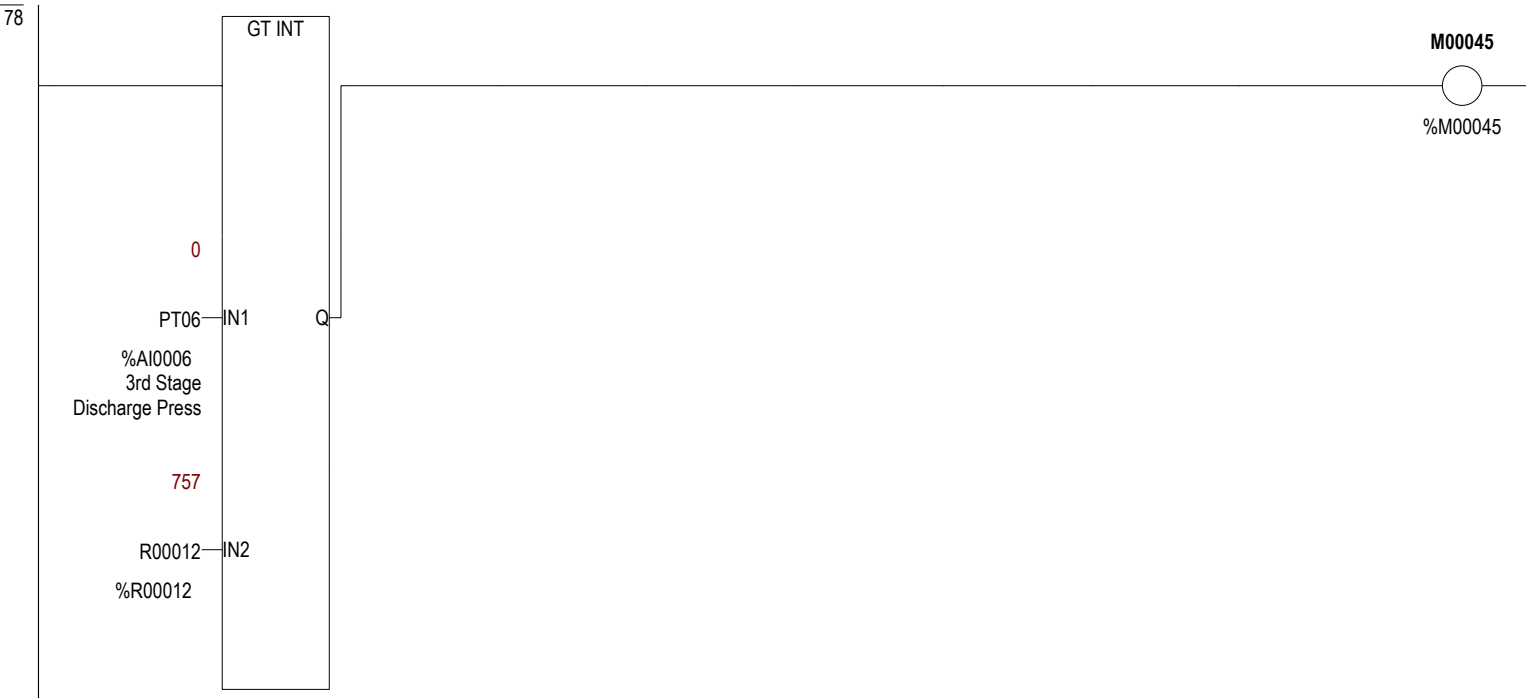
LD Block,'FRSTOUT': COIL 00072;

M00043 %M00043

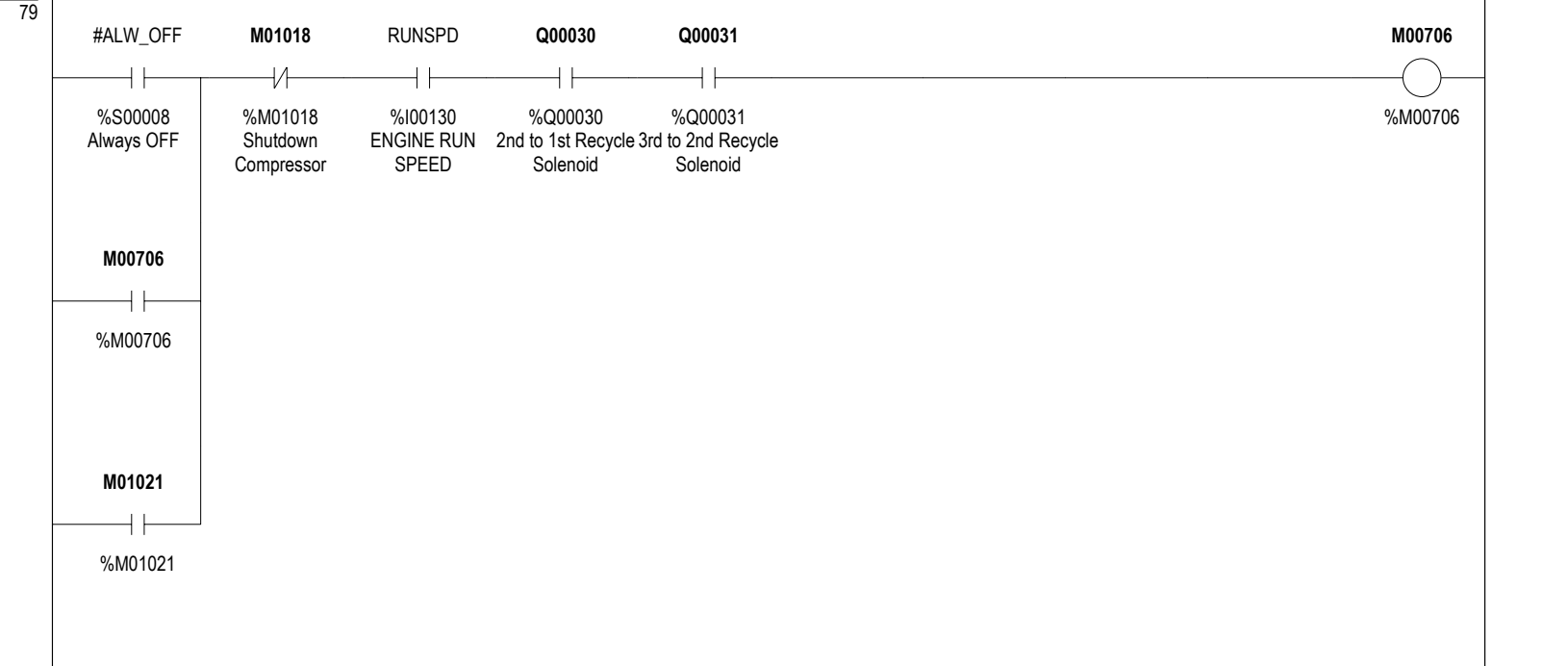
LD Block,'FRSTOUT': NCCON 00074; NOCON 00075; COIL 00075;



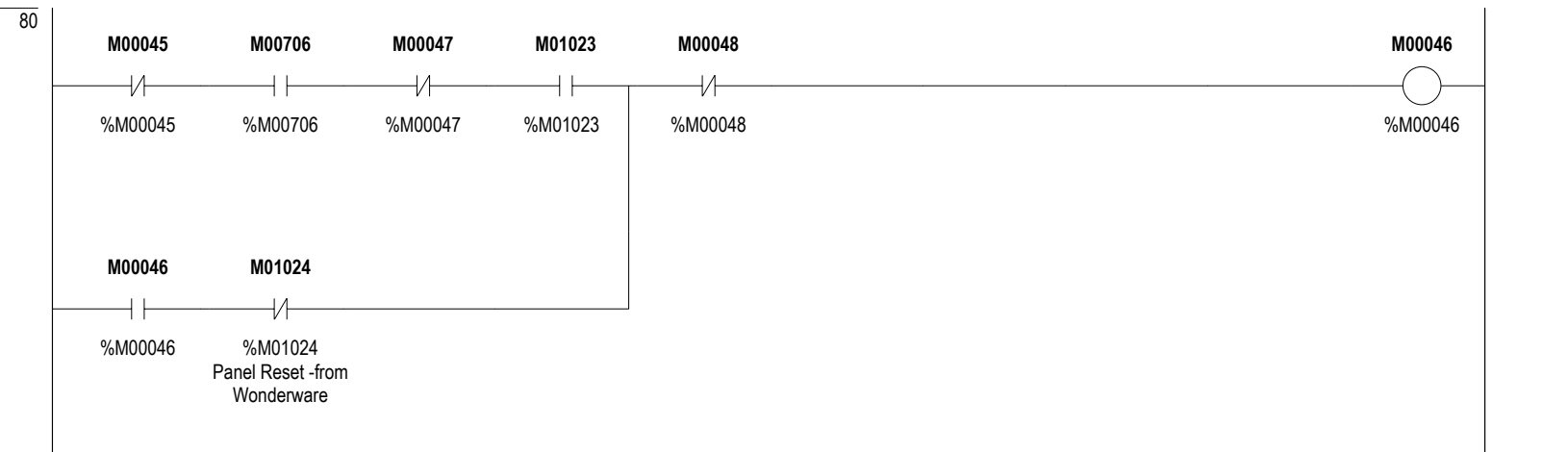
M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00048 %M00048
LD Block,'FRSTOUT': NCCON 00080, 00081; NOCON 00077; COIL 00077;
LD Block,'SUMMARY': NCCON 00003;



M00045 %M00045
LD Block,'FRSTOUT': NCCON 00080, 00081; COIL 00078;



M00706 %M00706
LD Block,'FRSTOUT': NOCON 00079, 00080, 00081; COIL 00079;
M01021 %M01021 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00002;



M00045 %M00045 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00078;
M00706 %M00706 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00079;
M00048 %M00048 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00077;
M00046 %M00046
LD Block,'FRSTOUT': NOCON 00080; COIL 00080;
LD Block,'SUMMARY': NCCON 00022;

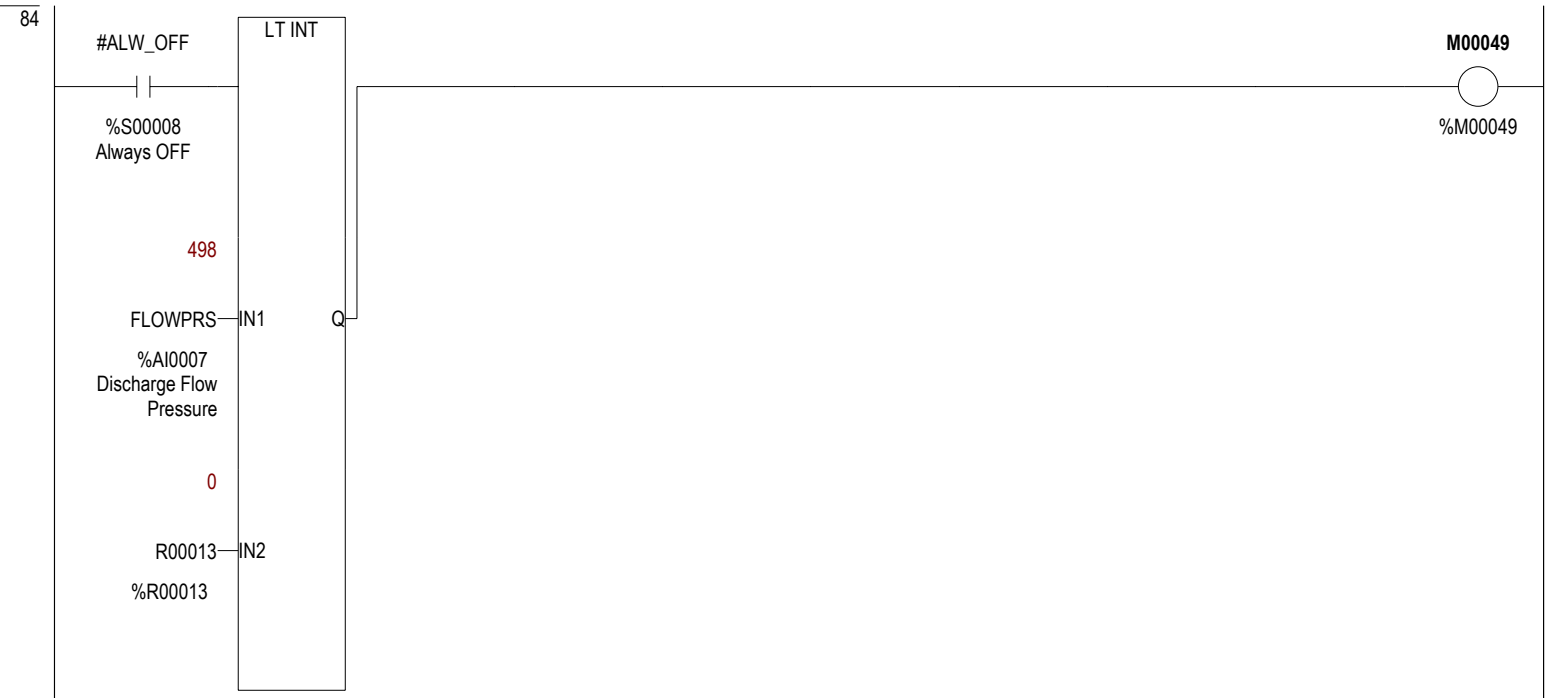


M00045 %M00045 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00078;

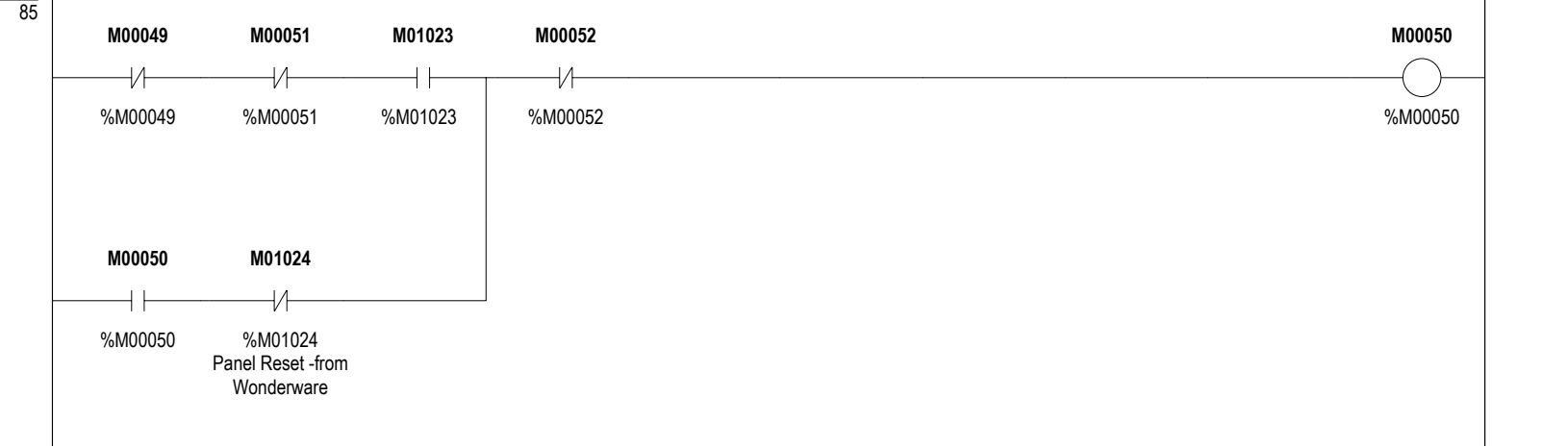
M00706 %M00706 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00079;
M00048 %M00048 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00077;
M00047 %M00047
LD Block,'FRSTOUT': NCCON 00080; NOCON 00081; COIL 00081;
LD Block,'SUMMARY': NOCON 00040;



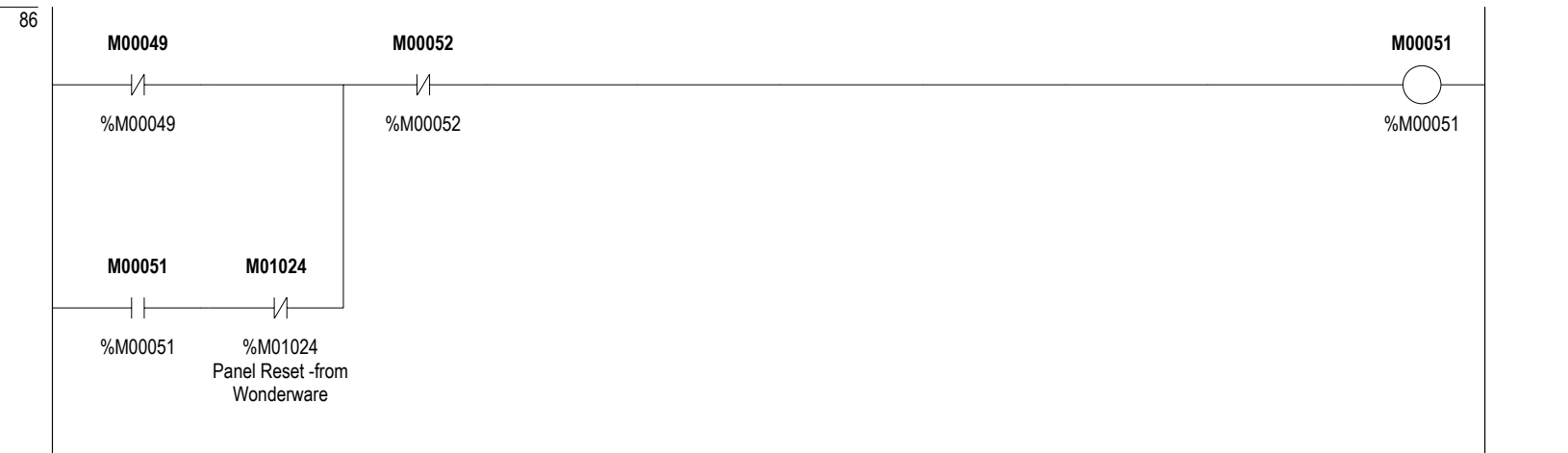
M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00052 %M00052
LD Block,'FRSTOUT': NCCON 00085, 00086; NOCON 00083; COIL 00083;
LD Block,'SUMMARY': NCCON 00003;



M00049 %M00049
LD Block,'FRSTOUT': NCCON 00085, 00086; COIL 00084;



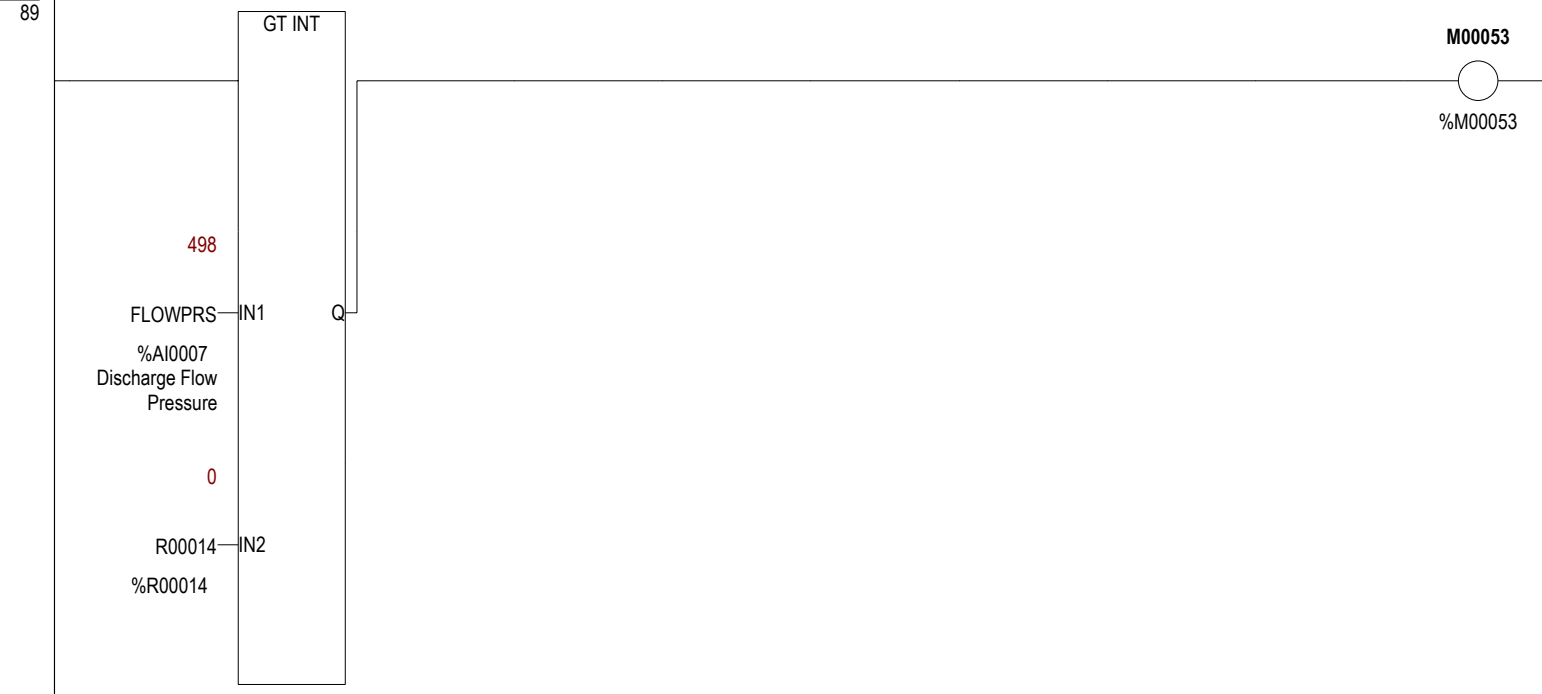
M00049 %M00049 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00084;
M00052 %M00052 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00083;
M00050 %M00050
LD Block,'FRSTOUT': NOCON 00085; COIL 00085;



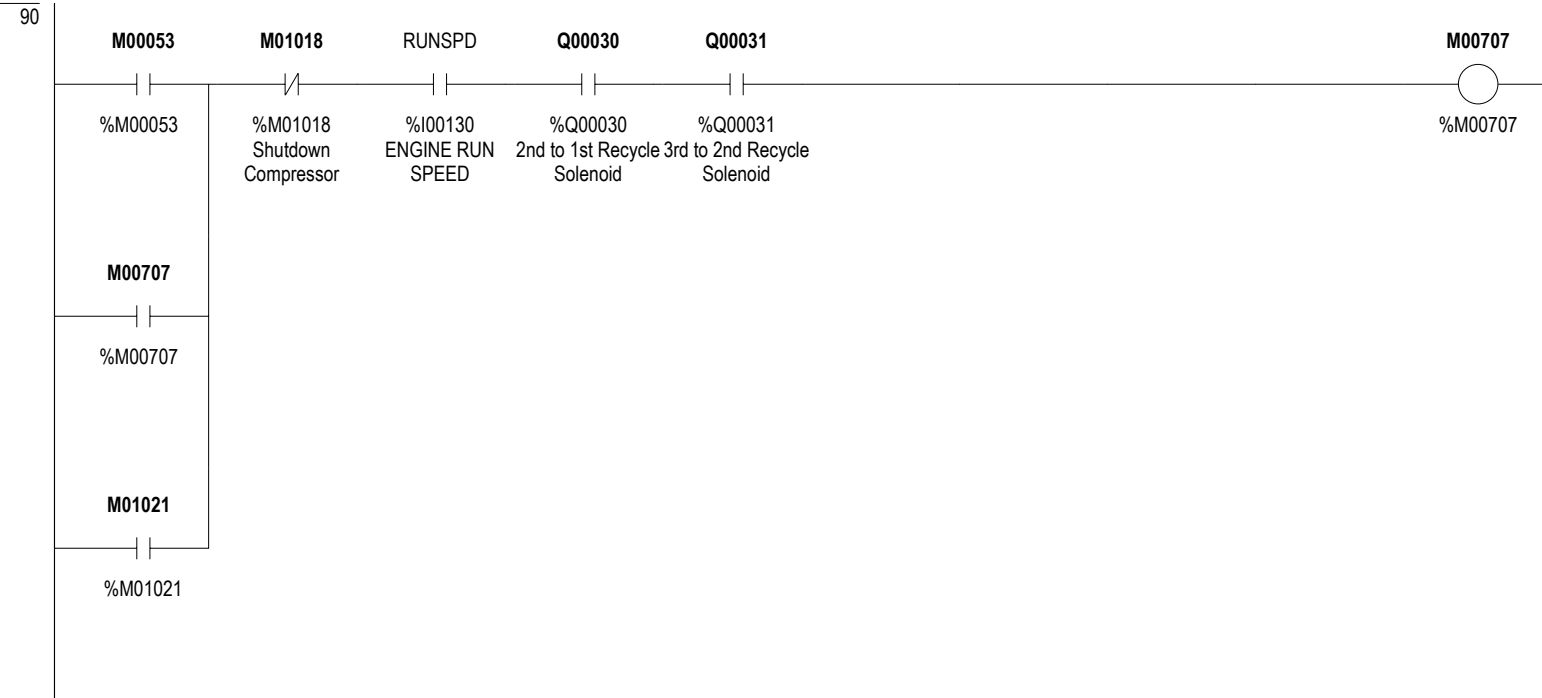
M00049 %M00049 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00084;
M00052 %M00052 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00083;
M00051 %M00051
LD Block,'FRSTOUT': NCCON 00085; NOCON 00086; COIL 00086;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00056 %M00056
LD Block,'FRSTOUT': NCCON 00091, 00092; NOCON 00088; COIL 00088;
LD Block,'SUMMARY': NCCON 00003;



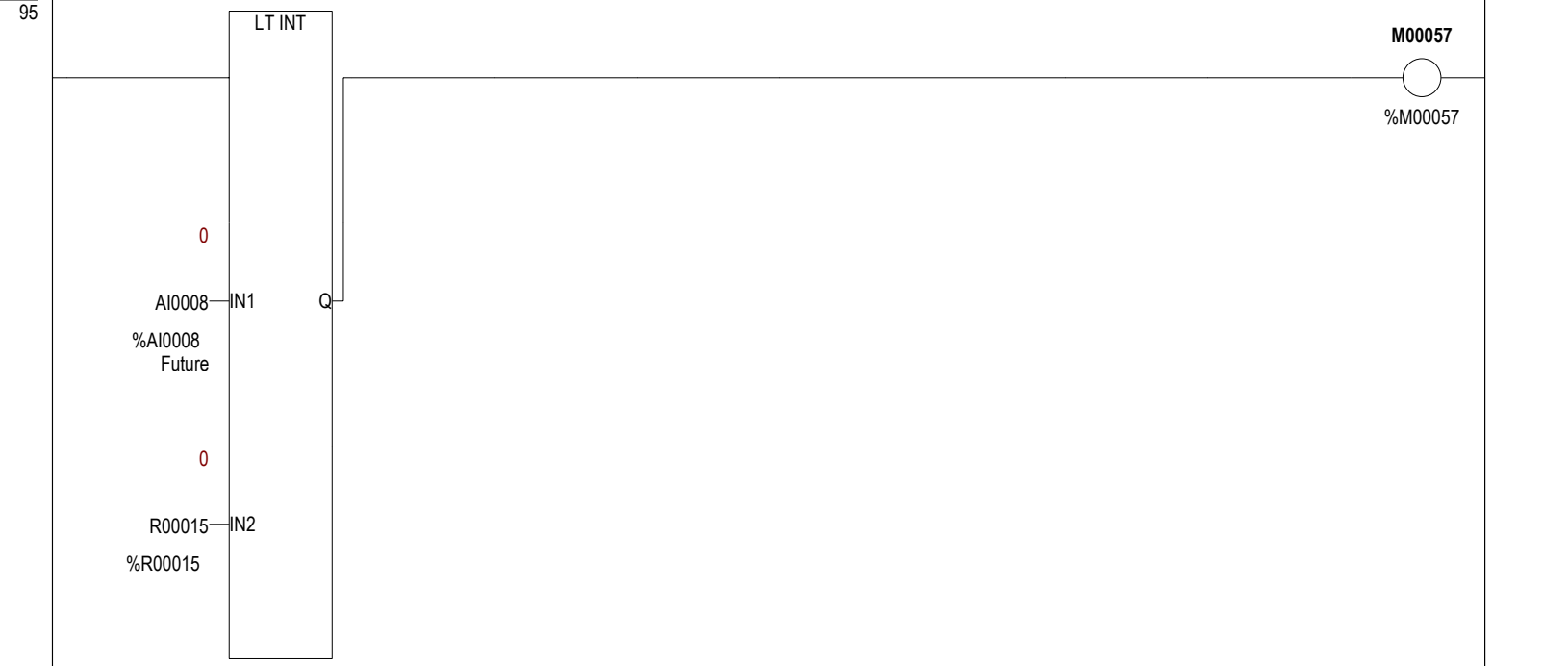
M00053 %M00053
LD Block,'FRSTOUT': NCCON 00091, 00092; NOCON 00090; COIL 00089;



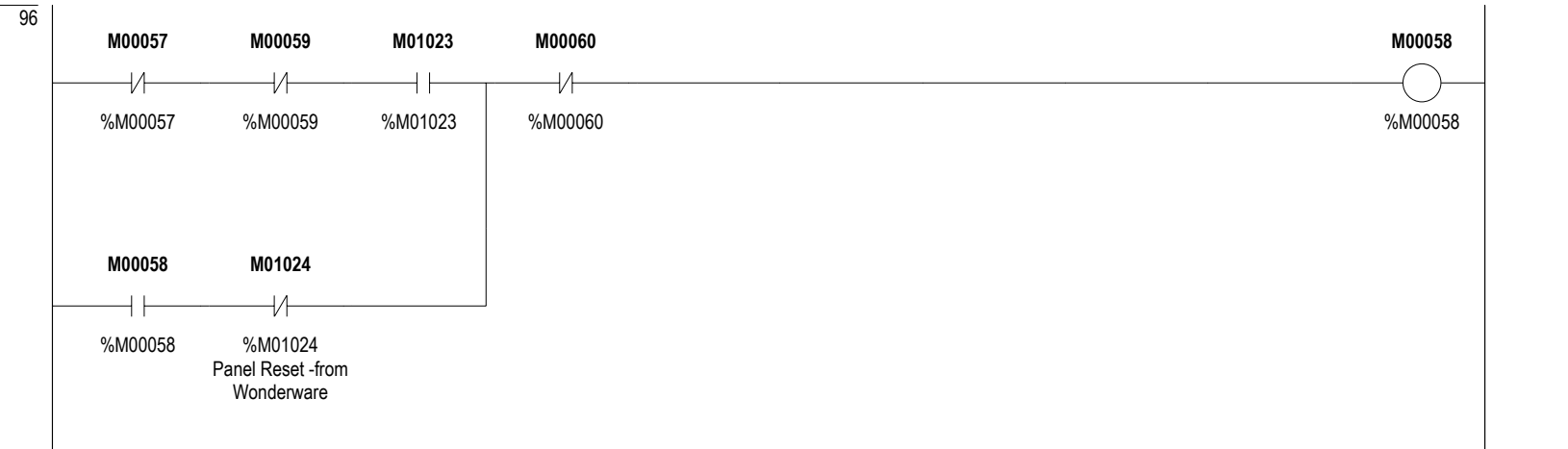
M00053 %M00053 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00089;

M00707 %M00707
LD Block,'FRSTOUT': NOCON 00090, 00091, 00092; COIL 00090;

M01021 %M01021 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00002;



M00057 %M00057
LD Block,'FRSTOUT': NCCON 00096, 00097; COIL 00095;



M00057 %M00057 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00095;

M00060 %M00060 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00094;

M00058 %M00058
LD Block,'FRSTOUT': NOCON 00096; COIL 00096;



M00057 %M00057 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00095;

M00060 %M00060 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00094;

M00059 %M00059
LD Block,'FRSTOUT': NCCON 00096; NOCON 00097; COIL 00097;



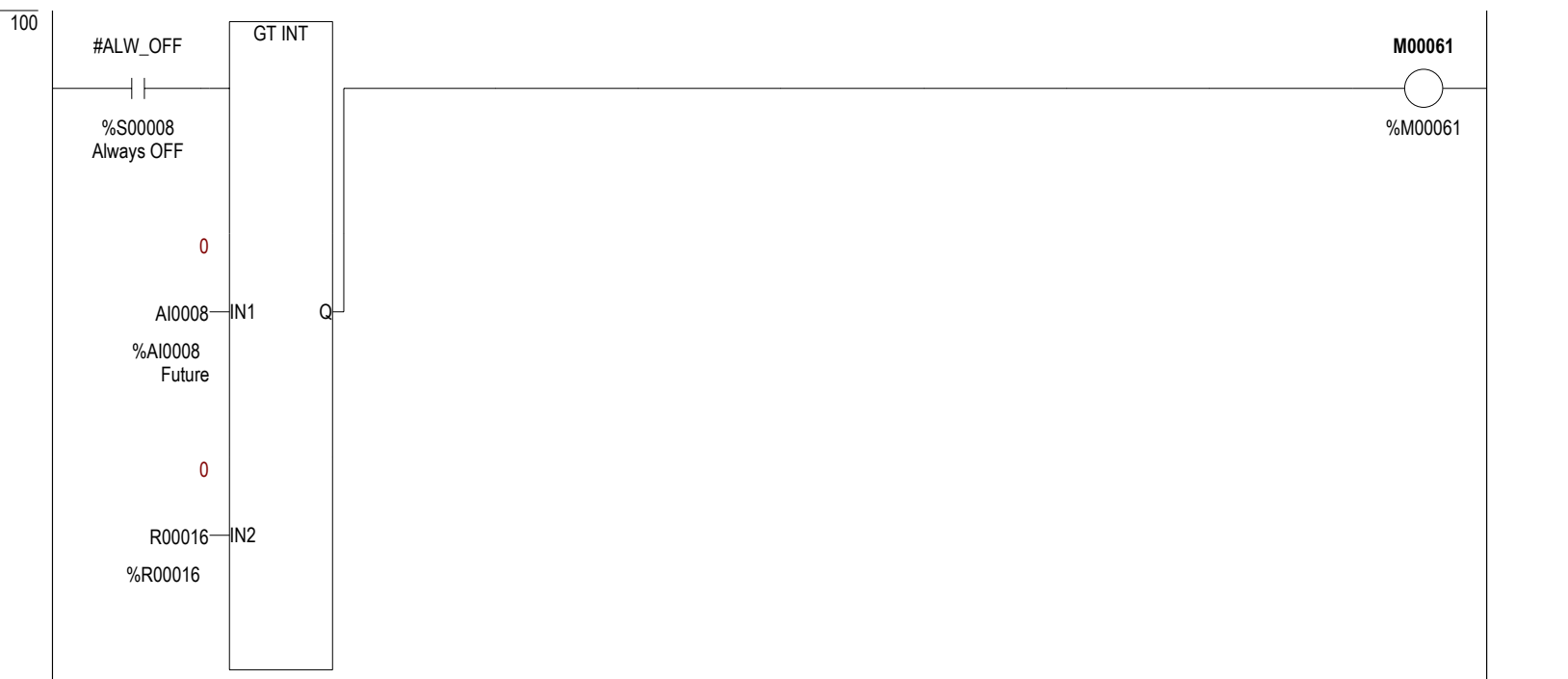
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00064 %M00064

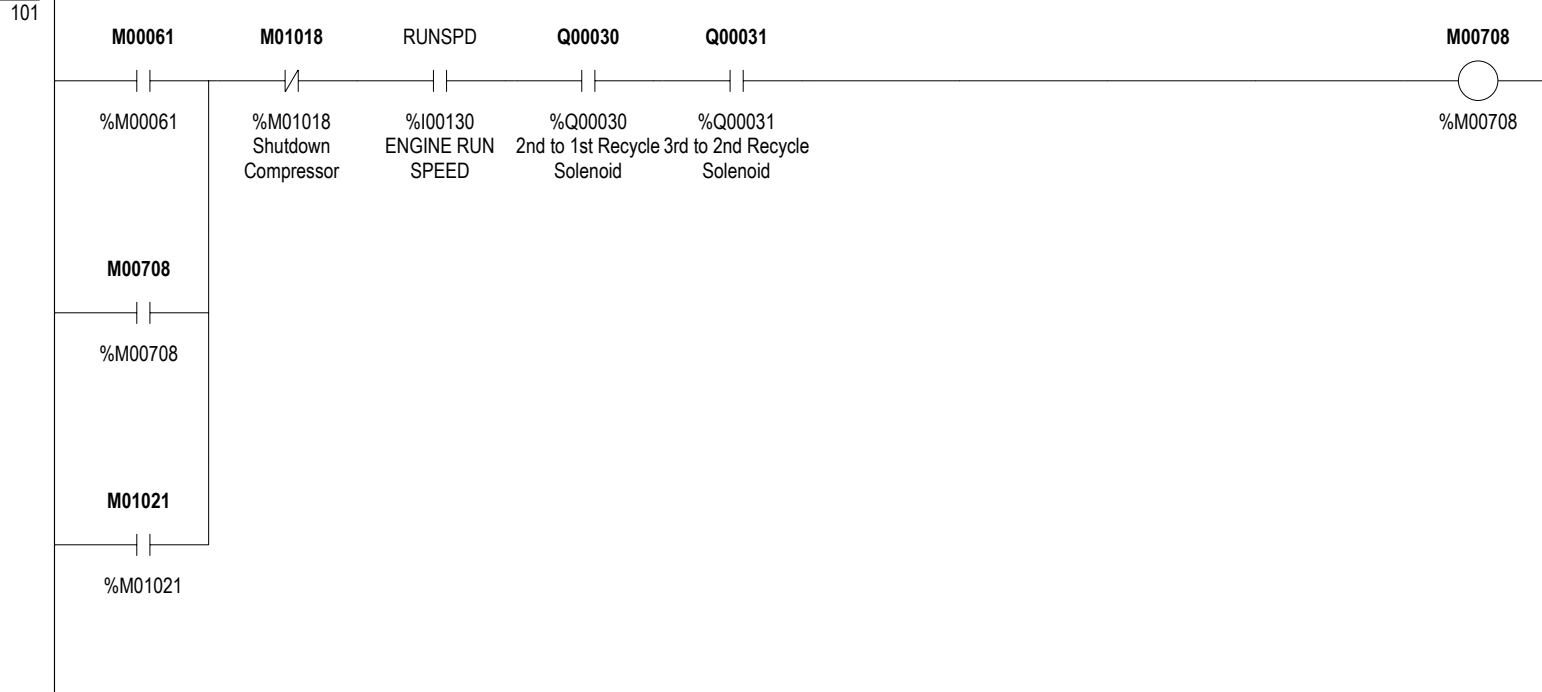
LD Block,'FRSTOUT': NCCON 00102, 00103; NOCON 00099; COIL 00099;

LD Block,'SUMMARY': NCCON 00003;

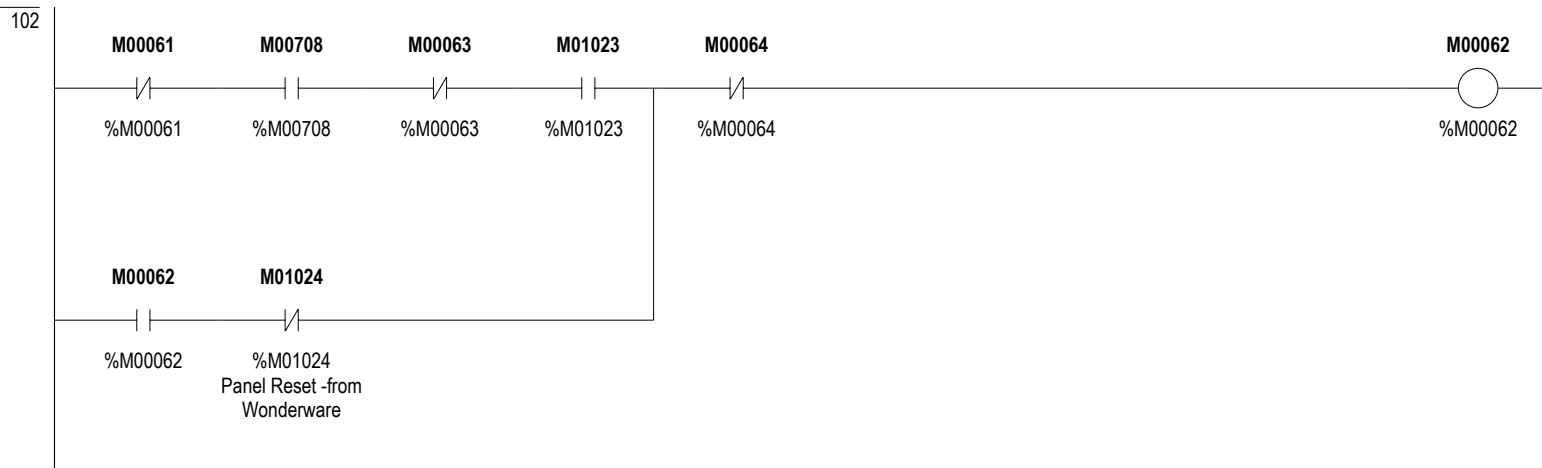


M00061 %M00061

LD Block,'FRSTOUT': NCCON 00102, 00103; NOCON 00101; COIL 00100;



M00061 %M00061 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00100;
M00708 %M00708
LD Block,'FRSTOUT': NOCON 00101, 00102, 00103; COIL 00101;
M01021 %M01021 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00002;



M00061 %M00061 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00100;
M00708 %M00708 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00101;
M00064 %M00064 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00099;
M00062 %M00062
LD Block,'FRSTOUT': NOCON 00102; COIL 00102;

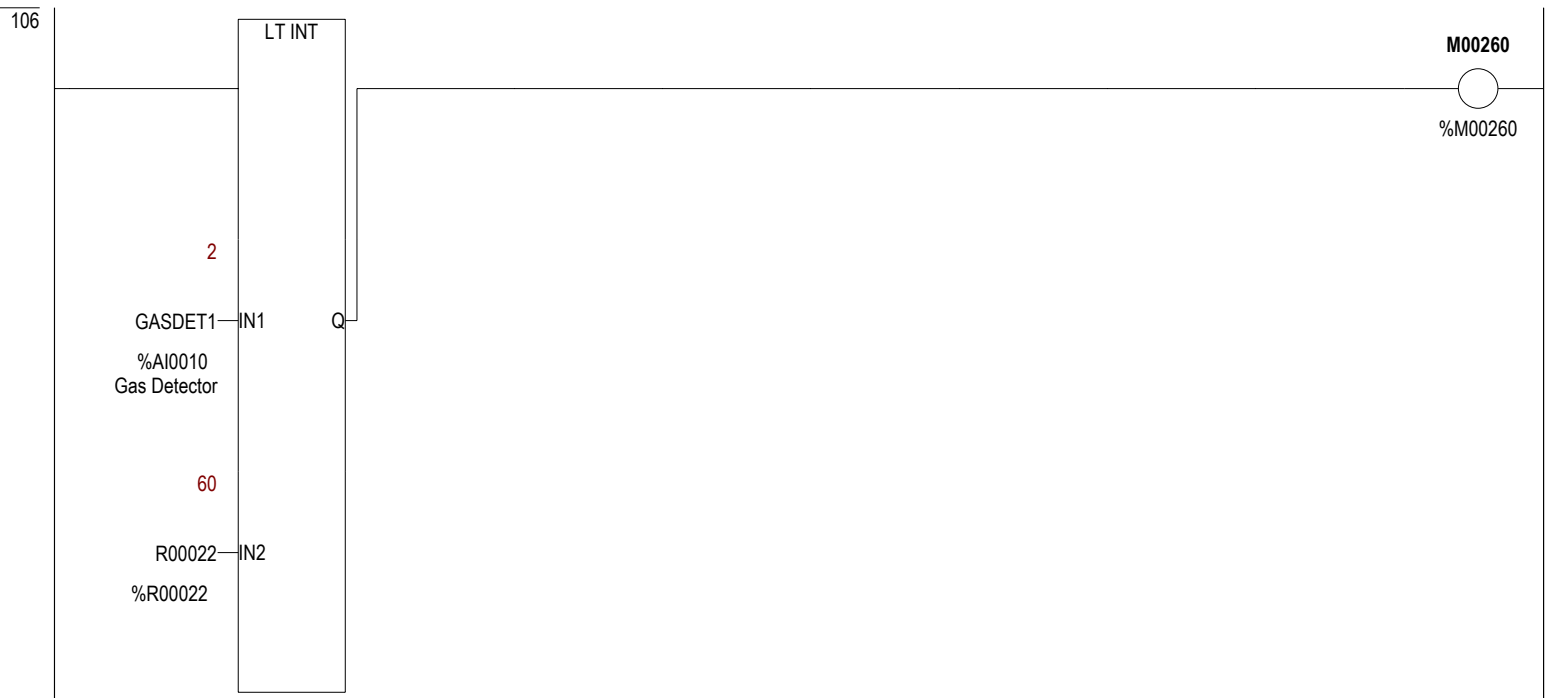


M00061 %M00061 (Controlling Rung Reference)

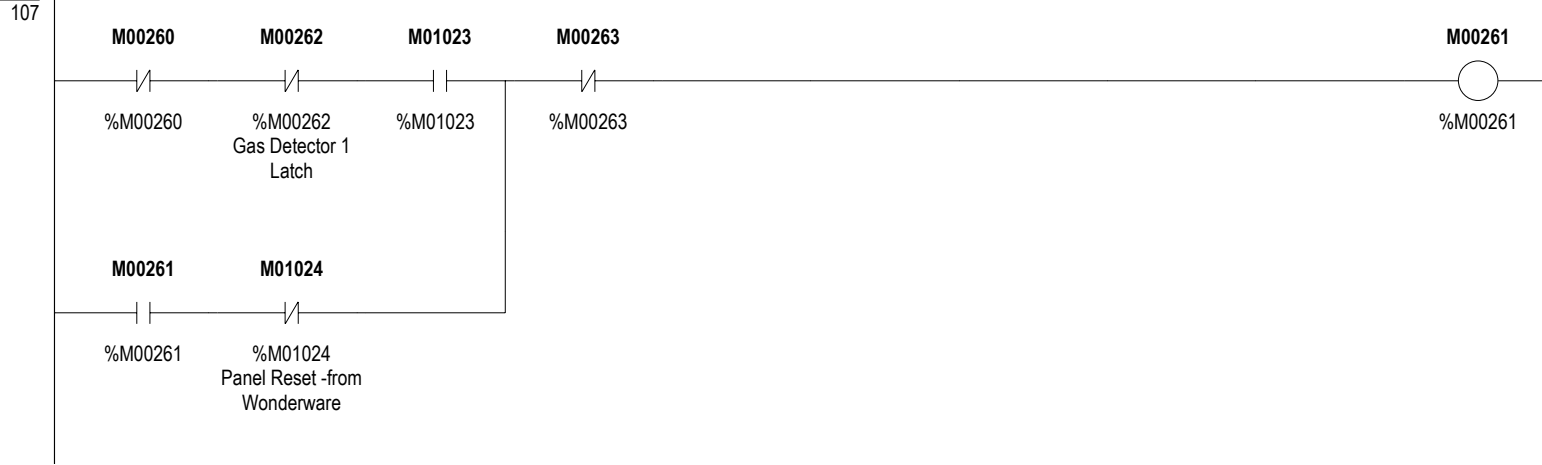
LD Block,'FRSTOUT': COIL 00100;
M00708 %M00708 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00101;
M00064 %M00064 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00099;
M00063 %M00063
LD Block,'FRSTOUT': NCCON 00102; NOCON 00103; COIL 00103;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00263 %M00263
LD Block,'FRSTOUT': NCCON 00107, 00108; NOCON 00105; COIL 00105;
LD Block,'SUMMARY': NCCON 00009;



M00260 %M00260
LD Block,'FRSTOUT': NCCON 00107, 00108; COIL 00106;



M00260 %M00260 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00106;

M00263 %M00263 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00105;

M00261 %M00261

LD Block,'FRSTOUT': NOCON 00107; COIL 00107;

LD Block,'SUMMARY': NCCON 00037;



M00260 %M00260 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00106;

M00263 %M00263 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00105;

M00262 %M00262

LD Block,'FRSTOUT': NCCON 00107; NOCON 00108; COIL 00108;

LD Block,'SUMMARY': NOCON 00050;

LD Block,'SEQUENC': NOCON 00003, 00004;



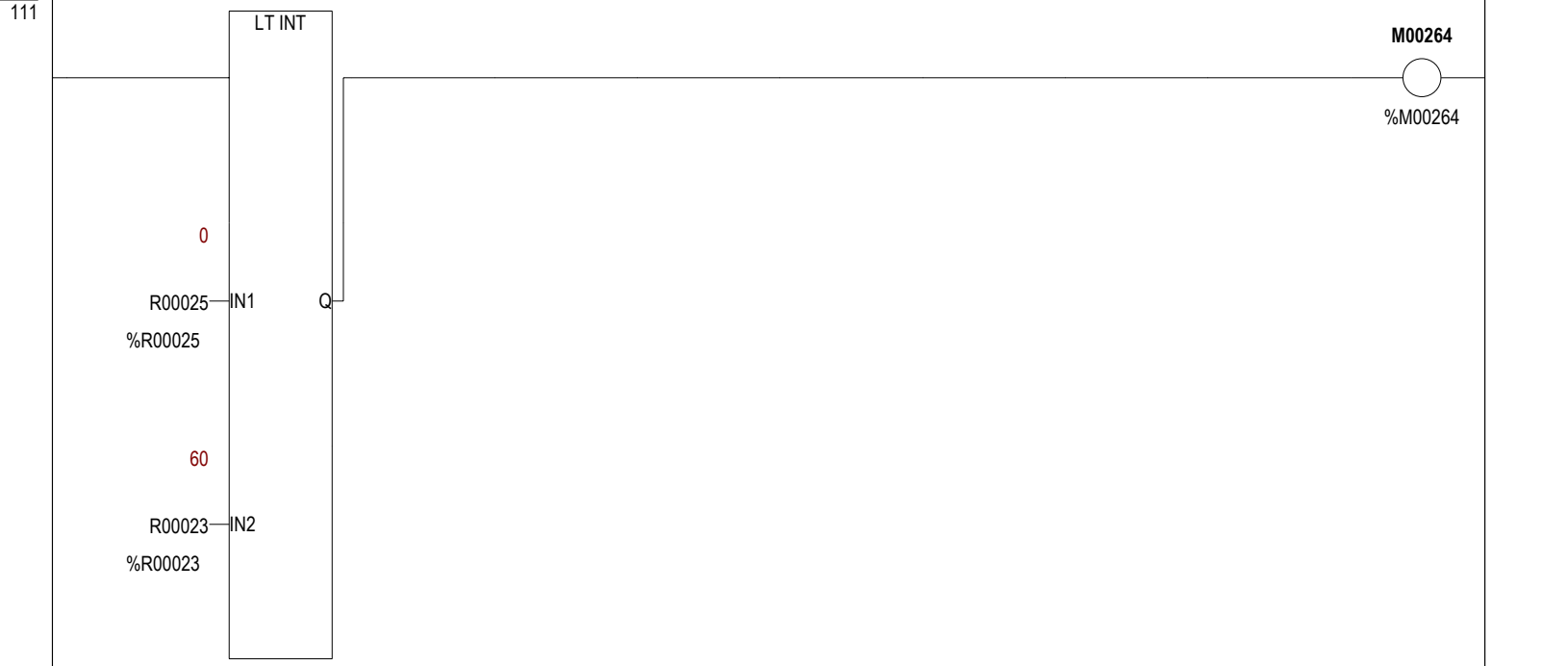
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

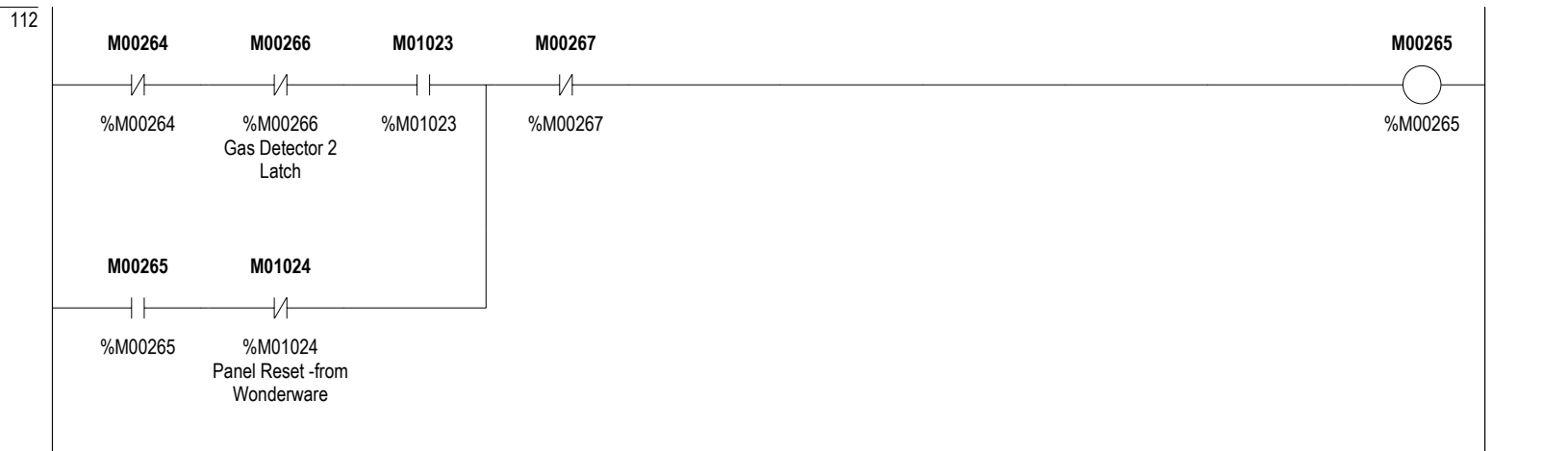
M00267 %M00267

LD Block,'FRSTOUT': NCCON 00112, 00113; NOCON 00110; COIL 00110;

LD Block,'SUMMARY': NCCON 00009;



M00264 %M00264
LD Block,'FRSTOUT': NCCON 00112, 00113; COIL 00111;



M00264 %M00264 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00111;

M00267 %M00267 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00110;

M00265 %M00265
LD Block,'FRSTOUT': NOCON 00112; COIL 00112;
LD Block,'SUMMARY': NCCON 00037;



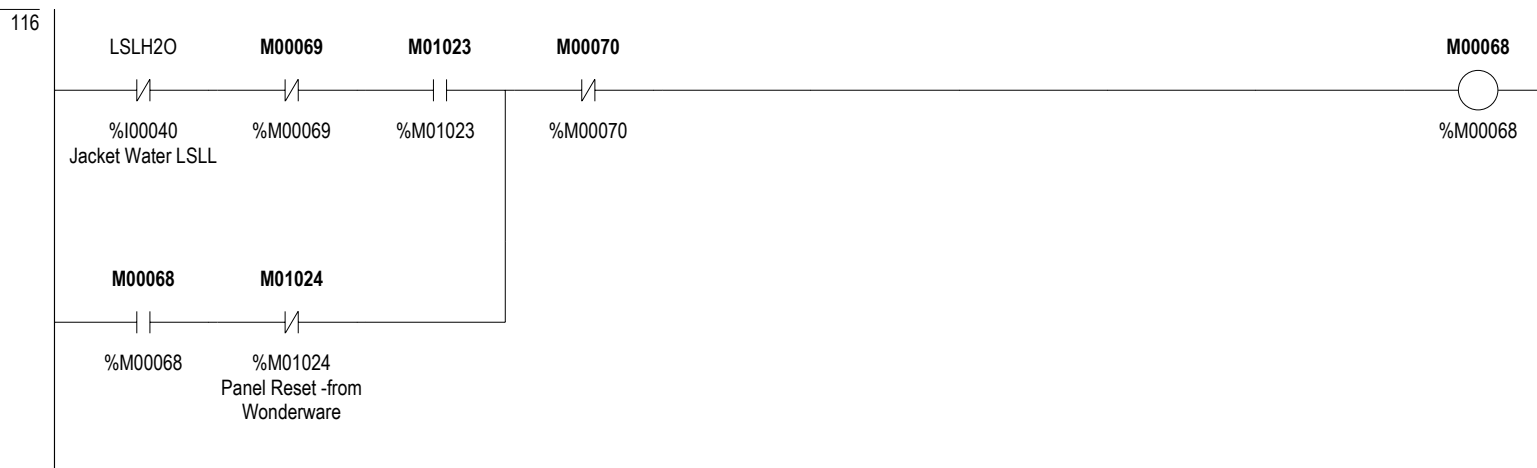
M00264 %M00264 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00111;

M00267 %M00267 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00110;

M00266 %M00266
LD Block,'FRSTOUT': NCCON 00112; NOCON 00113; COIL 00113;



M00070 %M00070
LD Block,'FRSTOUT': NCCON 00116, 00117; COIL 00115;
LD Block,'SUMMARY': NCCON 00003;



M00070 %M00070 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00115;

M00068 %M00068
LD Block,'FRSTOUT': NOCON 00116; COIL 00116;
LD Block,'SUMMARY': NCCON 00022;



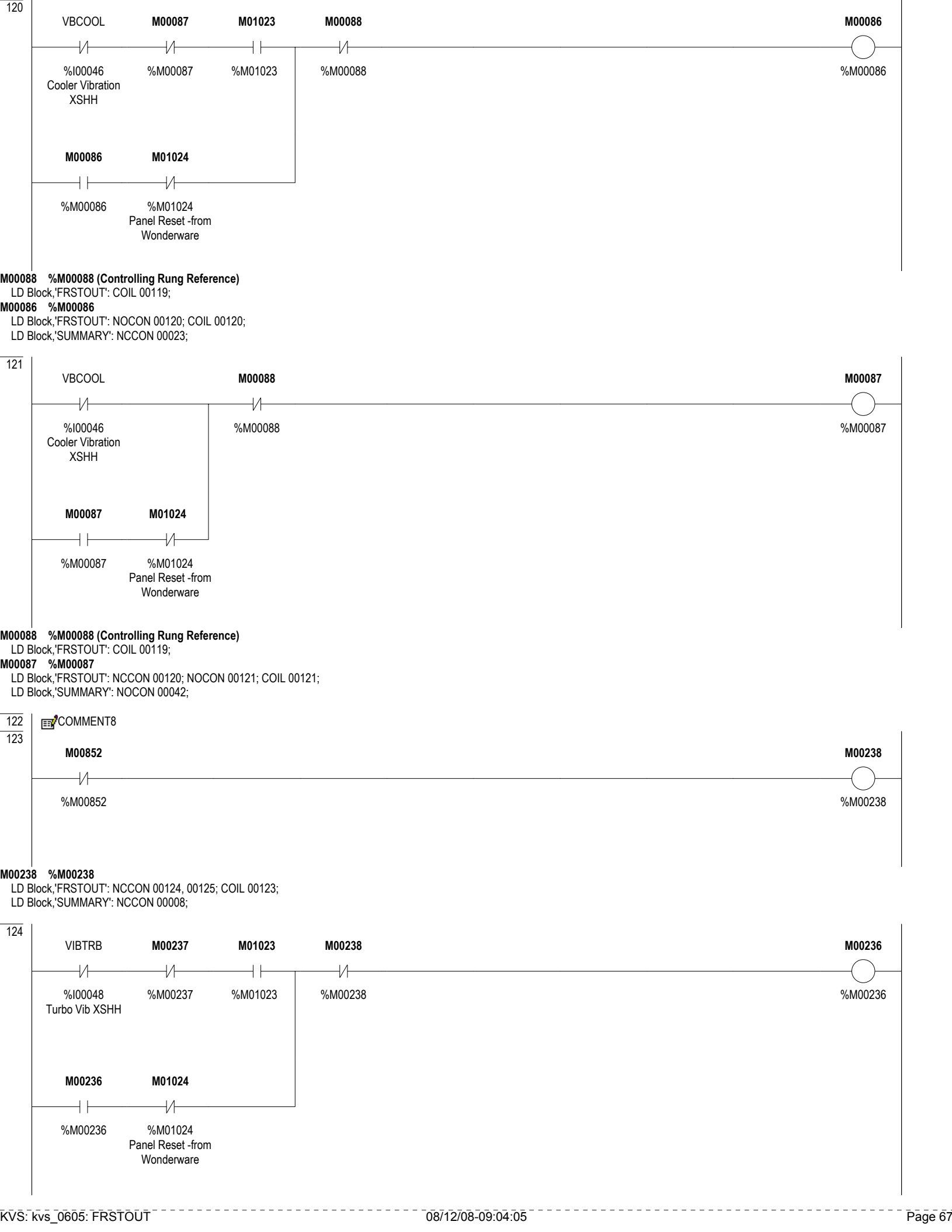
M00070 %M00070 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00115;

M00069 %M00069
LD Block,'FRSTOUT': NCCON 00116; NOCON 00117; COIL 00117;
LD Block,'SUMMARY': NOCON 00041;



M00088 %M00088
LD Block,'FRSTOUT': NCCON 00120, 00121; COIL 00119;
LD Block,'SUMMARY': NCCON 00004;



121

VBCOOL

%I00046

Cooler Vibration

XSHH

M00087

M01024

%M00087

%M01024

Panel Reset -from

Wonderware

M00088

M00087

%M00087

M00088 %M00088 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00119;

M00087 %M00087

LD Block,'FRSTOUT': NCCON 00120; NOCON 00121; COIL 00121;

LD Block,'SUMMARY': NOCON 00042;

122

COMMENT8

M00852

%M00852

M00238

%M00238

M00238 %M00238

LD Block,'FRSTOUT': NCCON 00124, 00125; COIL 00123;

LD Block,'SUMMARY': NCCON 00008;

124

VIBTRB

%I00048

Turbo Vib XSHH

M00237

M01023

M00238

M00236

M01024

%M00236

%M01024

Panel Reset -from

Wonderware

M00236

%M00238

M00236

%M00236

M00238 %M00238

LD Block,'FRSTOUT': NCCON 00124, 00125; COIL 00123;

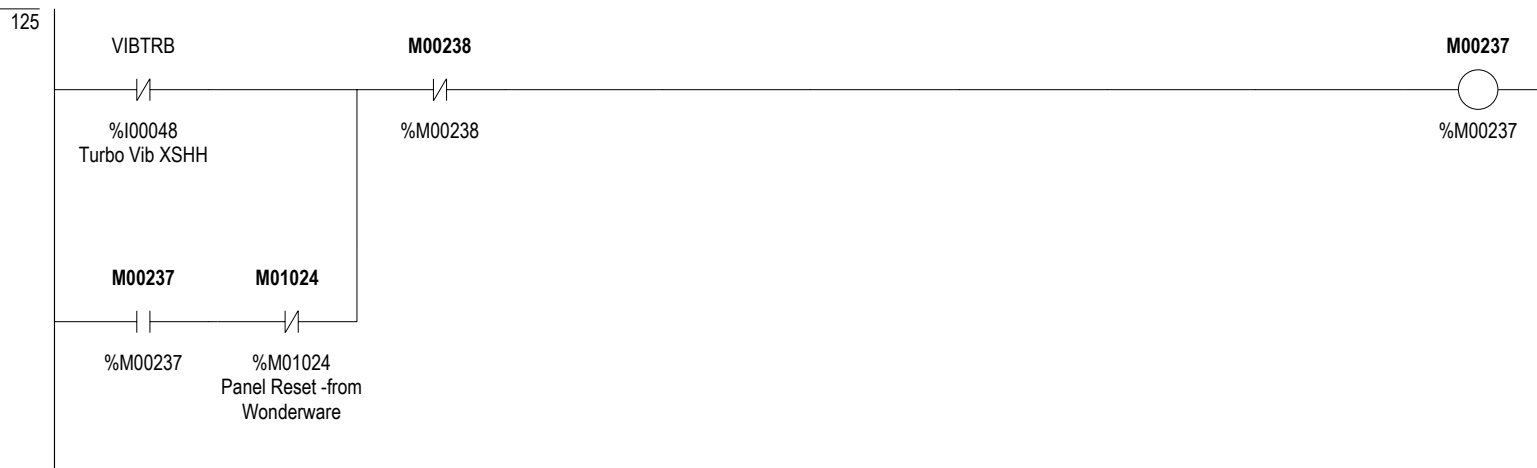
LD Block,'SUMMARY': NCCON 00008;

KVS: kvs_0605: FRSTOUT

08/12/08-09:04:05

Page 67

M00238 %M00238 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00123;
M00236 %M00236
LD Block,'FRSTOUT': NOCON 00124; COIL 00124;
LD Block,'SUMMARY': NCCON 00029;



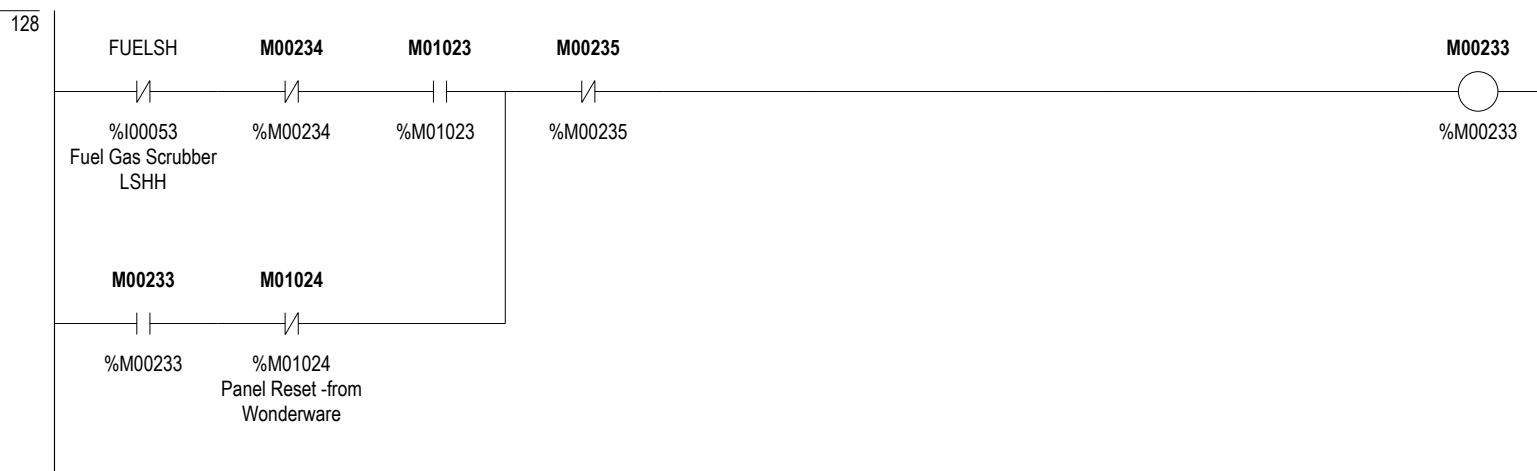
M00238 %M00238 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00123;
M00237 %M00237
LD Block,'FRSTOUT': NCCON 00124; NOCON 00125; COIL 00125;
LD Block,'SUMMARY': NOCON 00049;



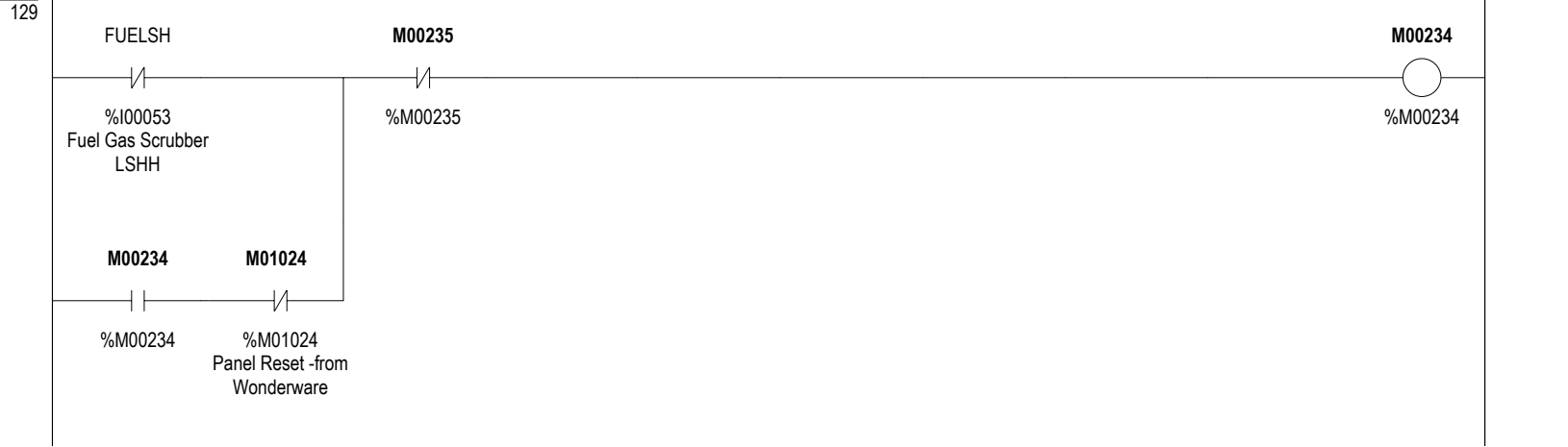
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;
M00235 %M00235
LD Block,'FRSTOUT': NCCON 00128, 00129; NOCON 00127; COIL 00127;
LD Block,'SUMMARY': NCCON 00019;



M00235 %M00235 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00127;
M00233 %M00233
LD Block,'FRSTOUT': NOCON 00128; COIL 00128;
LD Block,'SUMMARY': NCCON 00038;



M00235 %M00235 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00127;

M00234 %M00234

LD Block,'FRSTOUT': NCCON 00128; NOCON 00129; COIL 00129;

LD Block,'SUMMARY': NOCON 00041;



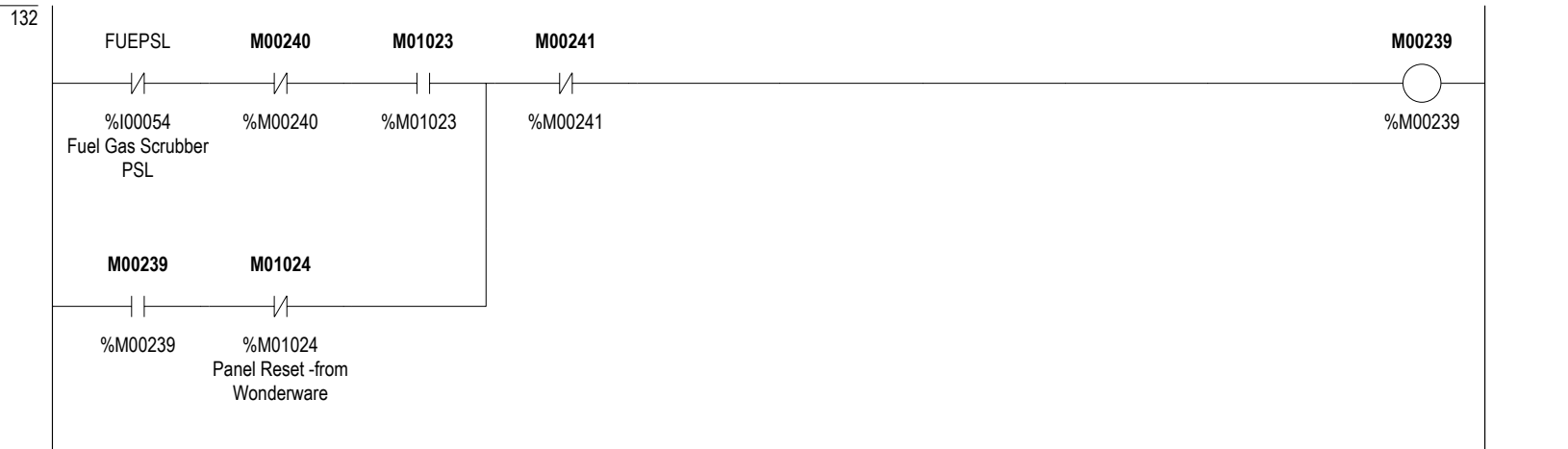
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00241 %M00241

LD Block,'FRSTOUT': NCCON 00132, 00133; NOCON 00131; COIL 00131;

LD Block,'SUMMARY': NCCON 00008;



M00241 %M00241 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00131;

M00239 %M00239

LD Block,'FRSTOUT': NOCON 00132; COIL 00132;

LD Block,'SUMMARY': NCCON 00029;



M00241 %M00241 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00131;

M00240 %M00240

LD Block,'FRSTOUT': NCCON 00132; NOCON 00133; COIL 00133;

LD Block,'SUMMARY': NOCON 00049;

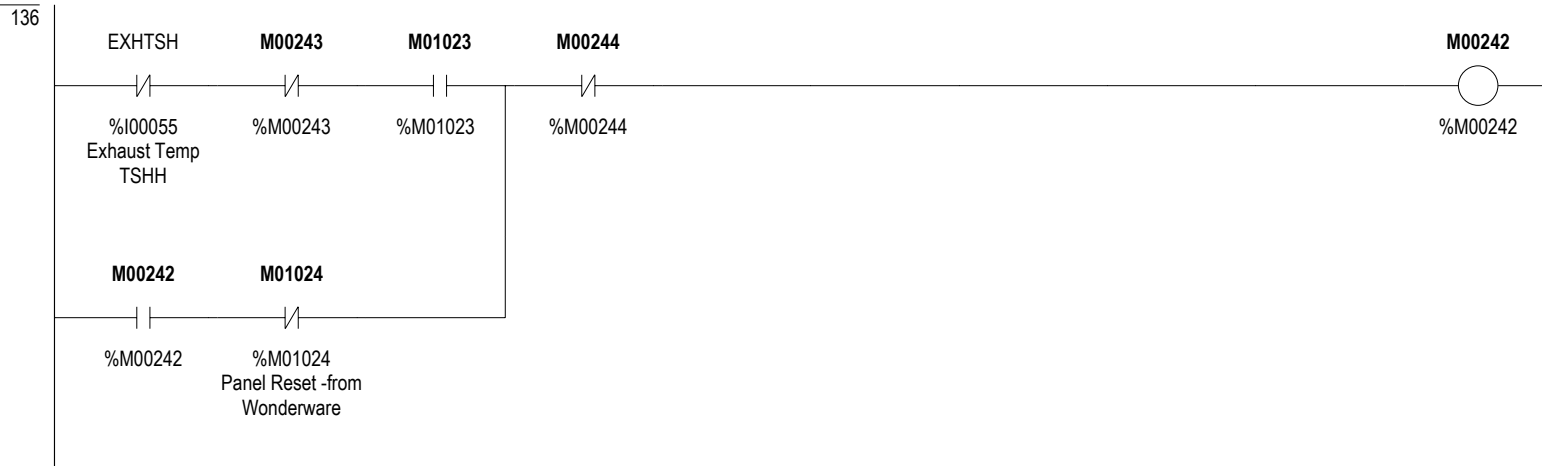
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M00244 %M00244

LD Block,'FRSTOUT': NCCON 00136, 00137; COIL 00135;

LD Block,'SUMMARY': NCCON 00008;

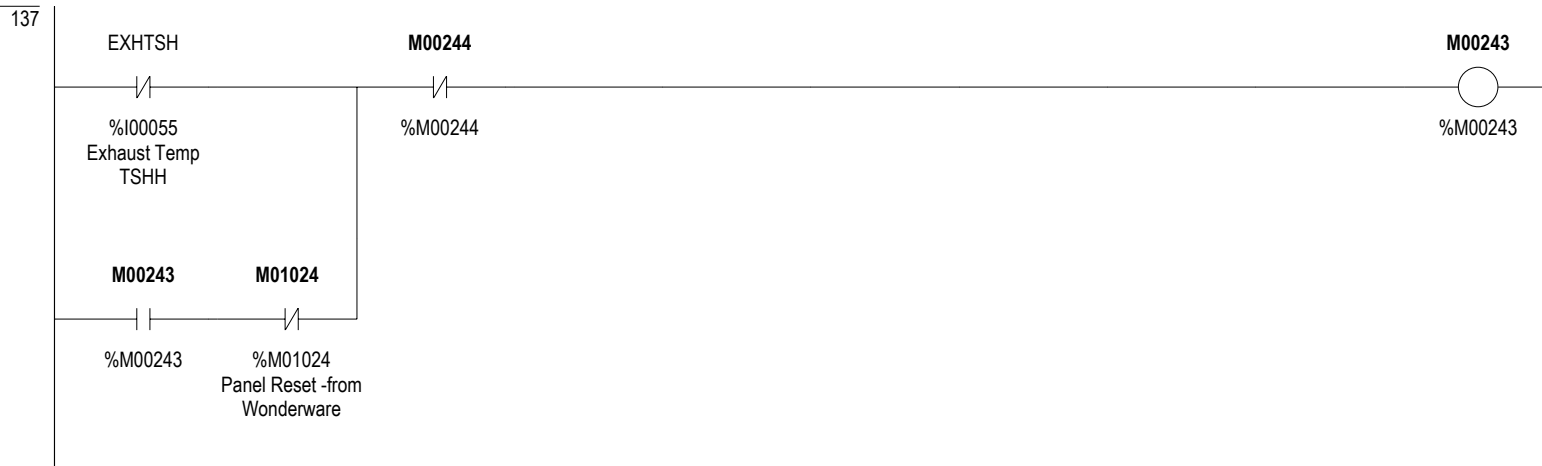


M00244 %M00244 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00135;

M00242 %M00242

LD Block,'FRSTOUT': NOCON 00136; COIL 00136;

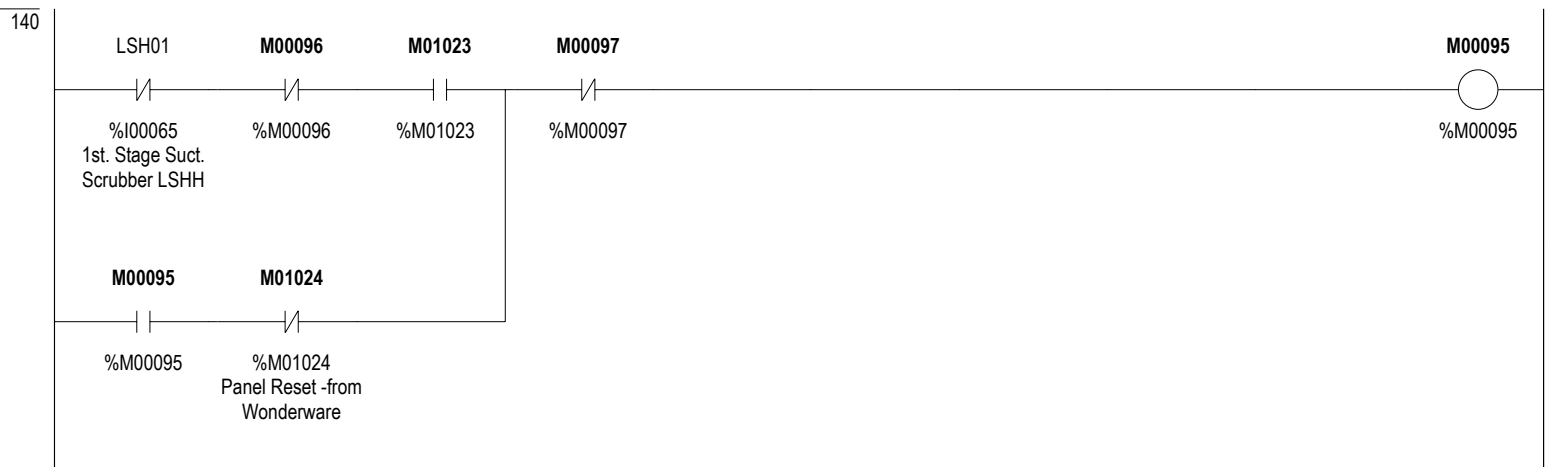


M00244 %M00244 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00135;
M00243 %M00243
LD Block,'FRSTOUT': NCCON 00136; NOCON 00137; COIL 00137;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00097 %M00097
LD Block,'FRSTOUT': NCCON 00140, 00141; NOCON 00139; COIL 00139;
LD Block,'SUMMARY': NCCON 00005;



M00097 %M00097 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00139;
M00095 %M00095
LD Block,'FRSTOUT': NOCON 00140; COIL 00140;
LD Block,'SUMMARY': NCCON 00024;



M00097 %M00097 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00139;
M00096 %M00096
LD Block,'FRSTOUT': NCCON 00140; NOCON 00141; COIL 00141;
LD Block,'SUMMARY': NOCON 00042;

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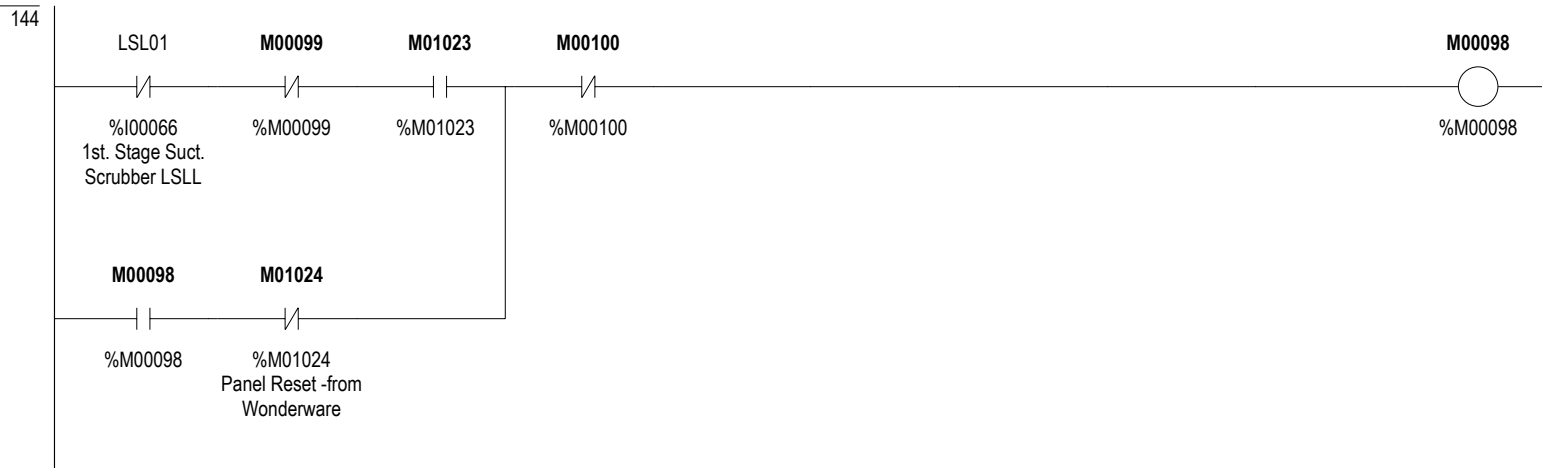
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00100 %M00100

LD Block,'FRSTOUT': NCCON 00144, 00145; NOCON 00143; COIL 00143;

LD Block,'SUMMARY': NCCON 00005;



M00100 %M00100 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00143;

M00098 %M00098

LD Block,'FRSTOUT': NOCON 00144; COIL 00144;



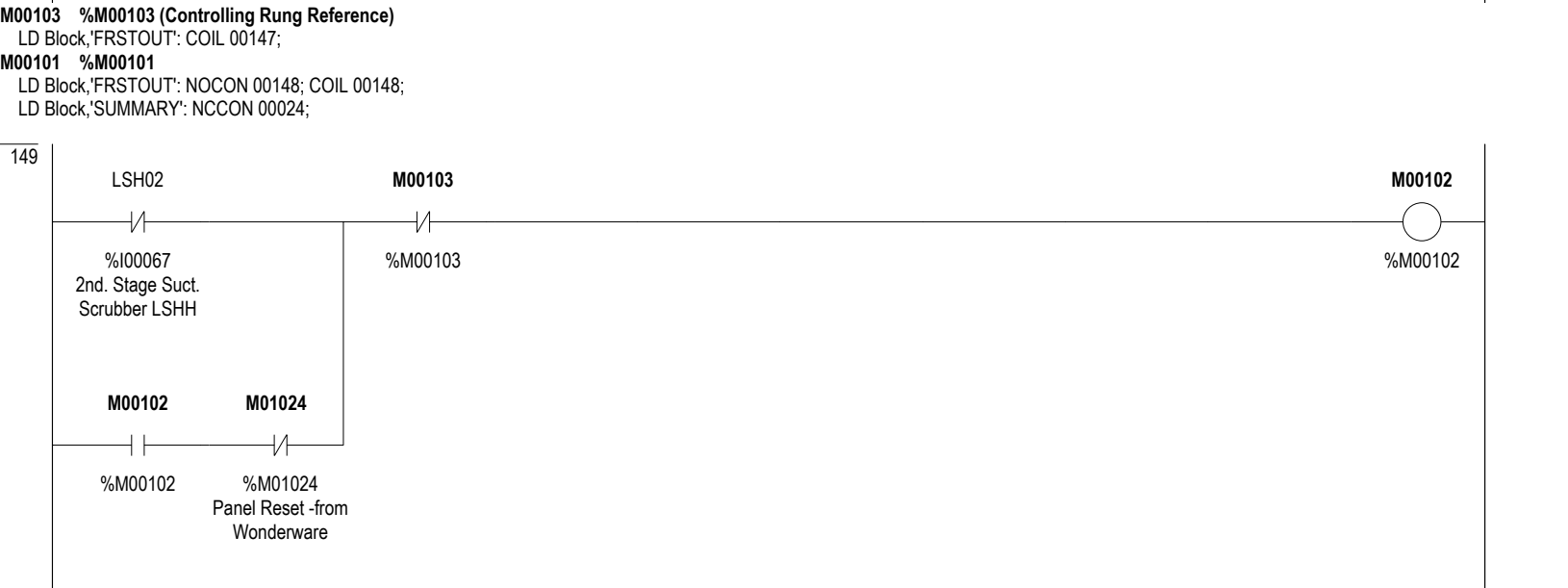
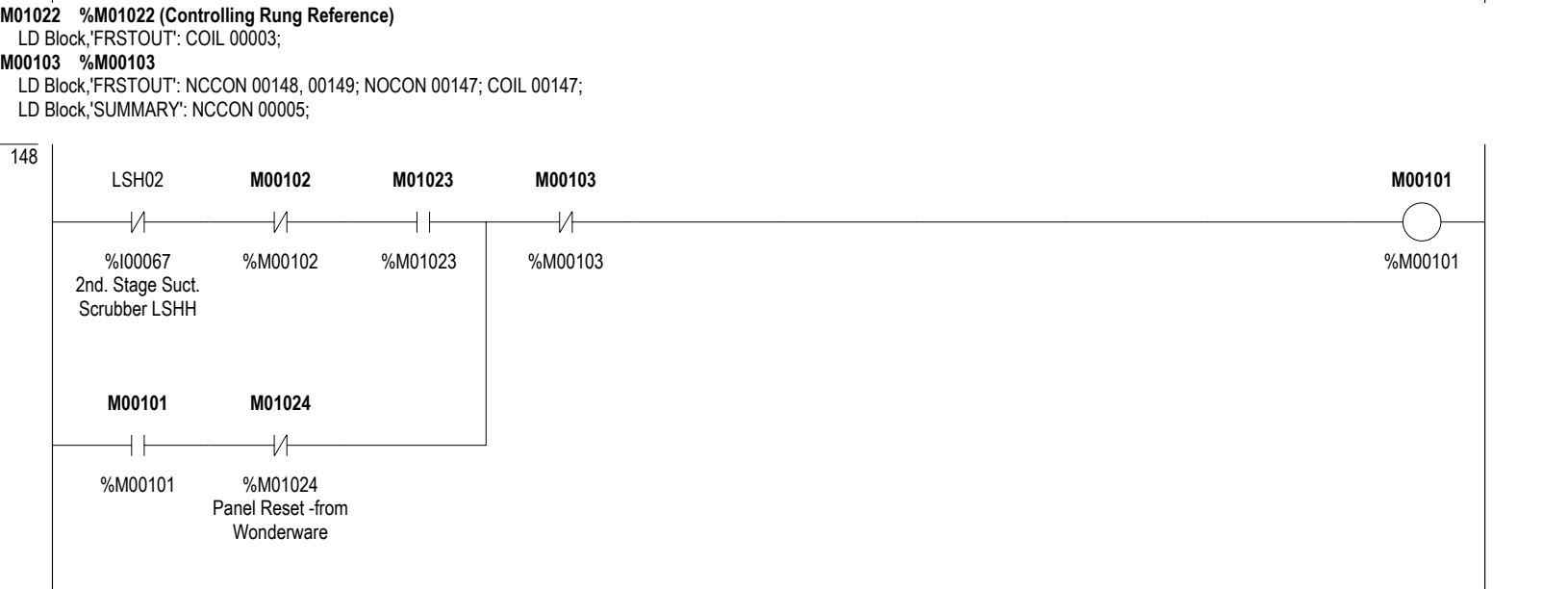
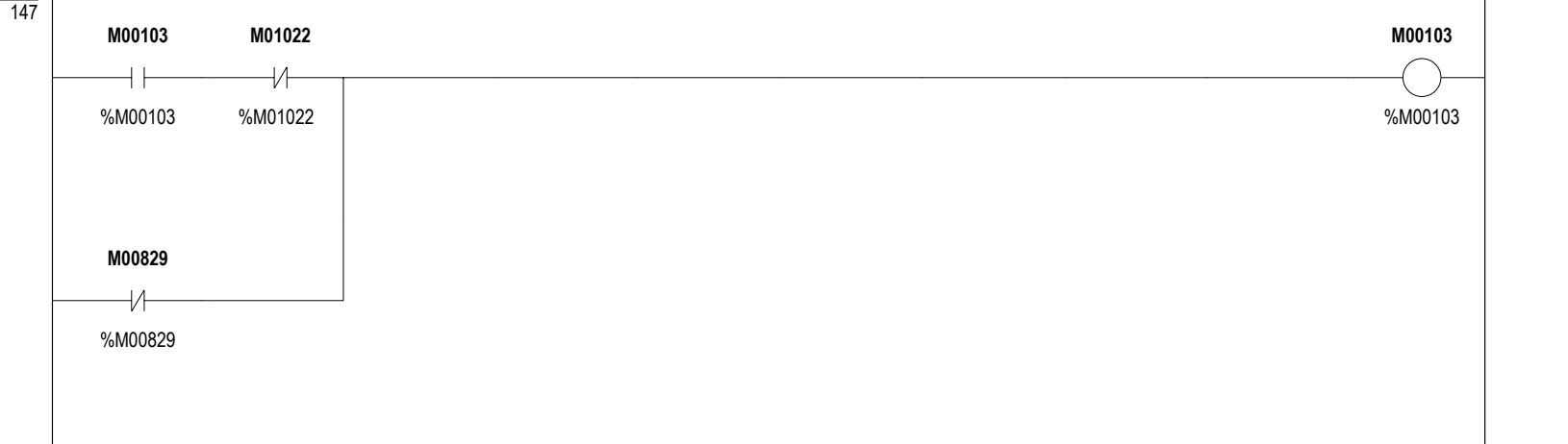
M00100 %M00100 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00143;

M00099 %M00099

LD Block,'FRSTOUT': NCCON 00144; NOCON 00145; COIL 00145;

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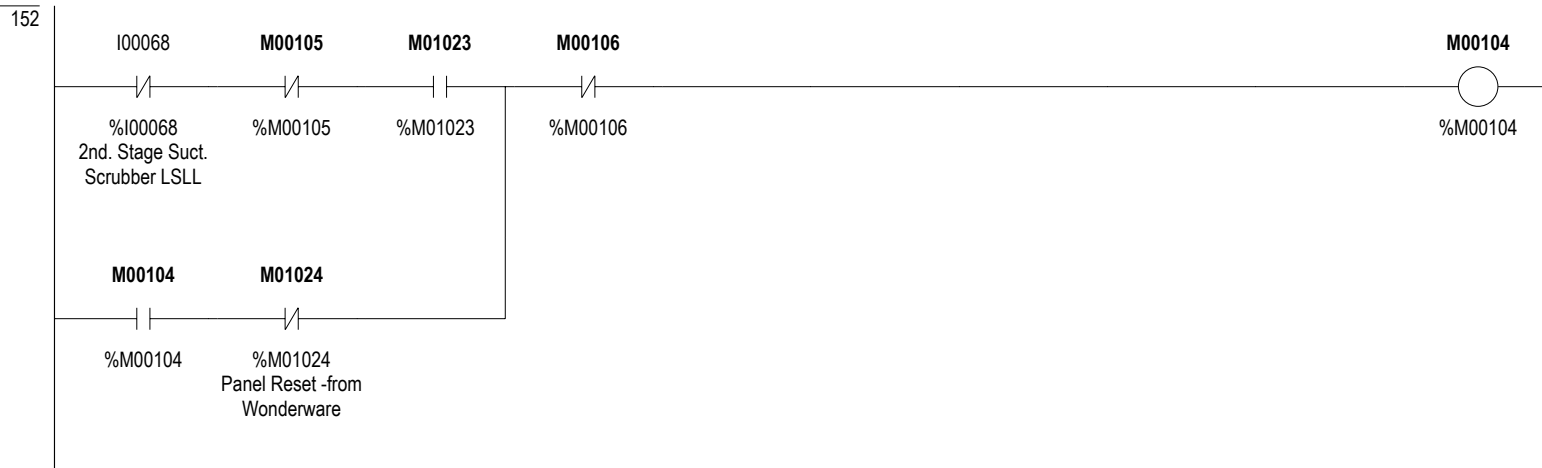
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00106 %M00106

LD Block,'FRSTOUT': NCCON 00152, 00153; NOCON 00151; COIL 00151;

LD Block,'SUMMARY': NCCON 00005;



M00106 %M00106 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00151;

M00104 %M00104

LD Block,'FRSTOUT': NOCON 00152; COIL 00152;



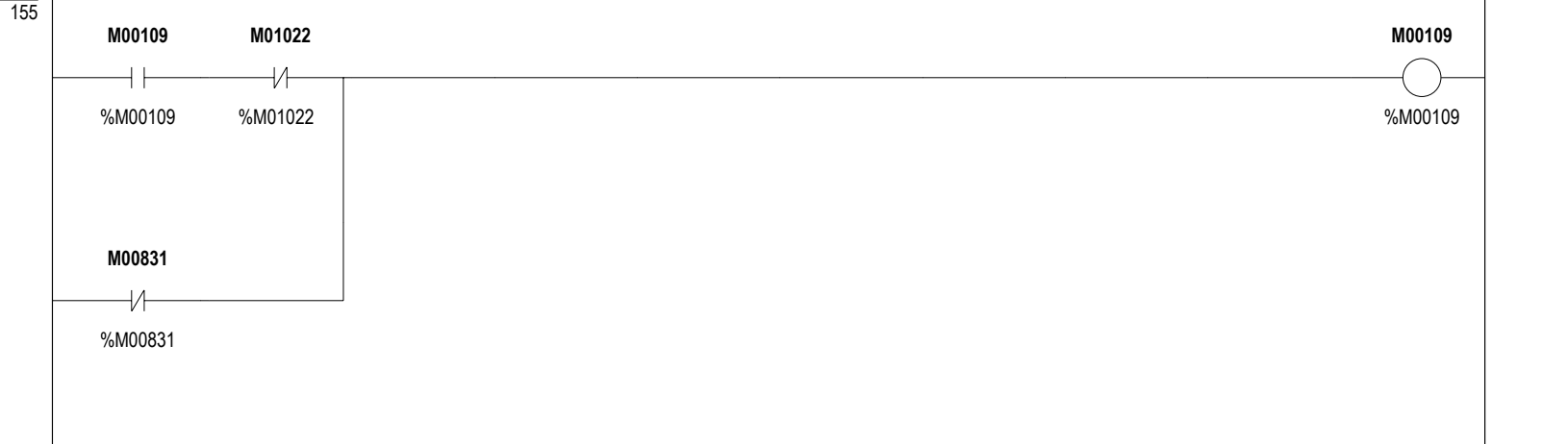
M00106 %M00106 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00151;

M00105 %M00105

LD Block,'FRSTOUT': NCCON 00152; NOCON 00153; COIL 00153;

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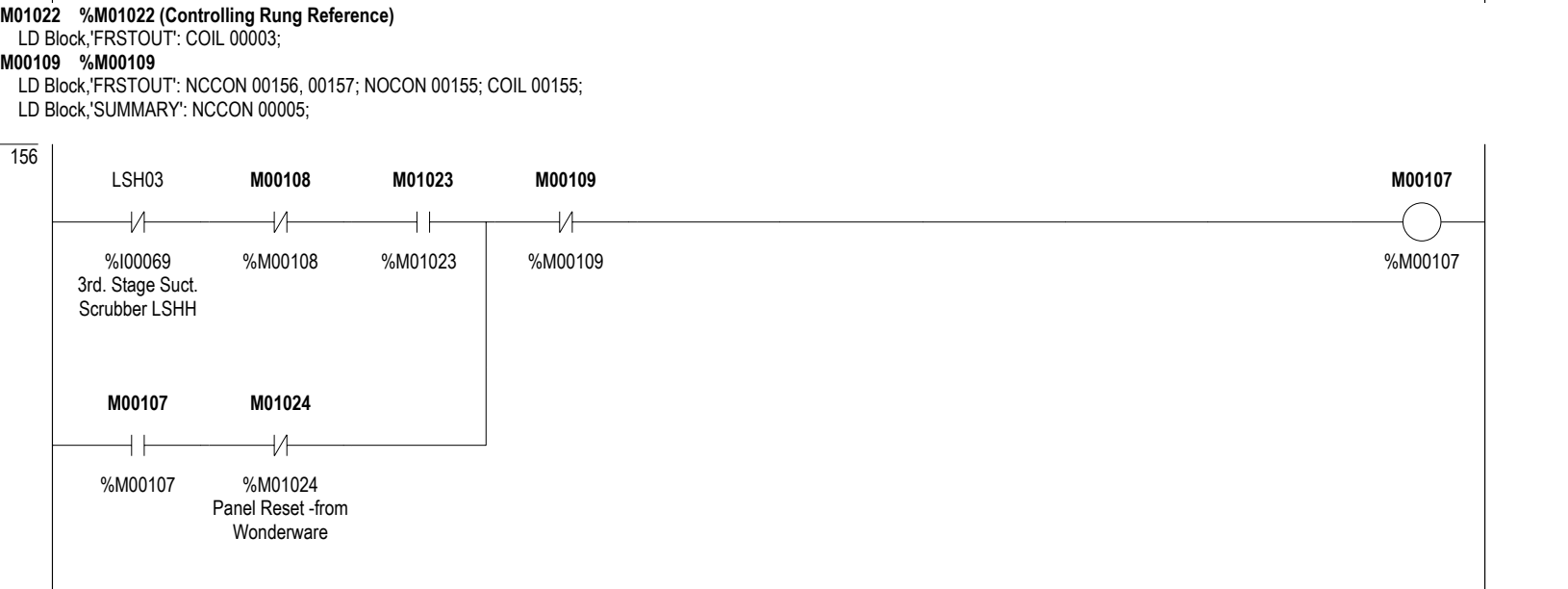
M01022 %M01022 (Controlling Rung Reference)

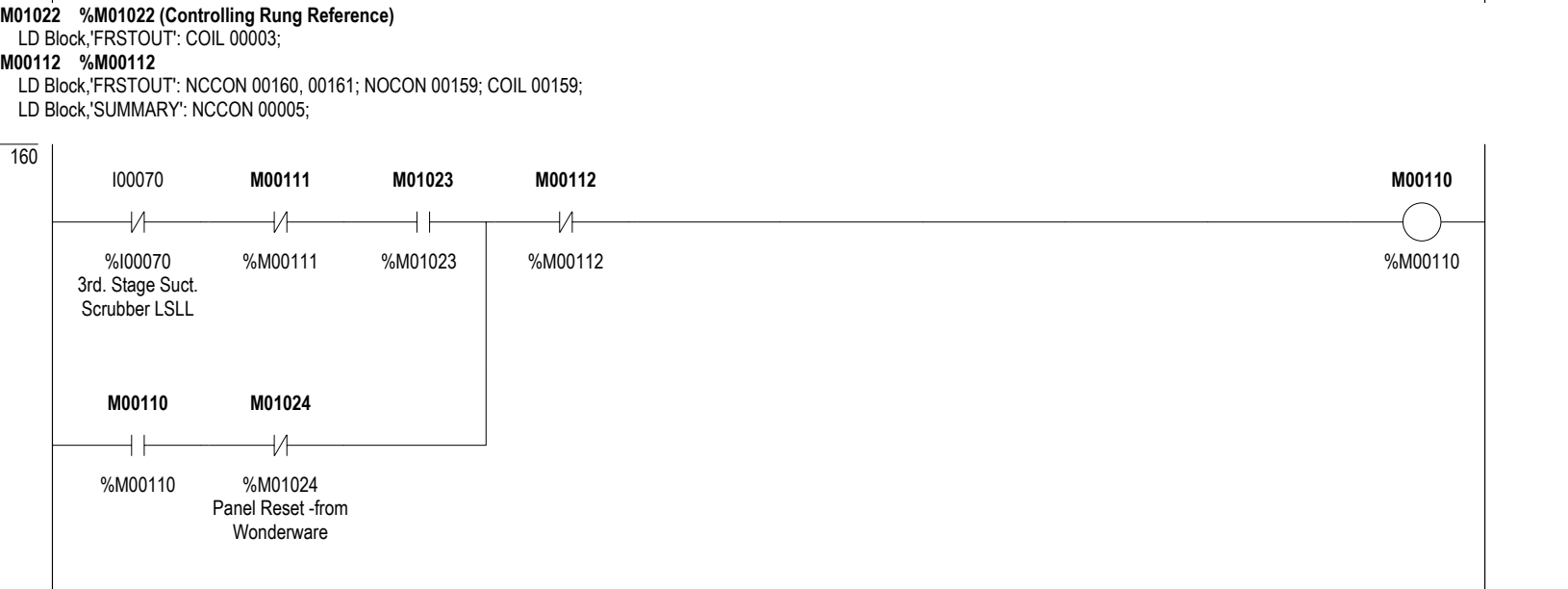
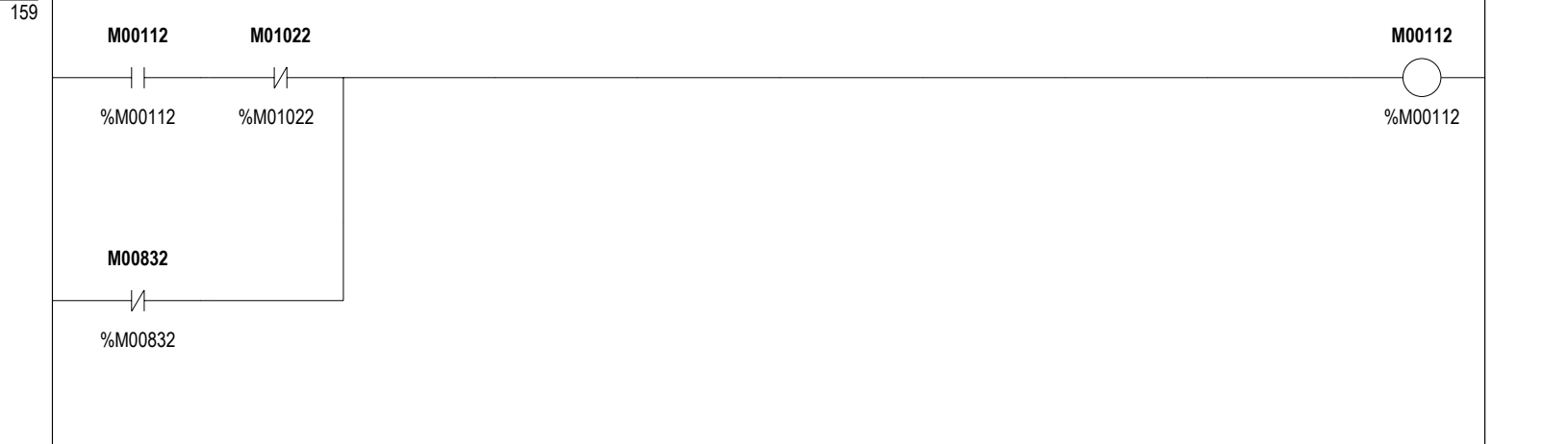
LD Block,'FRSTOUT': COIL 00003;

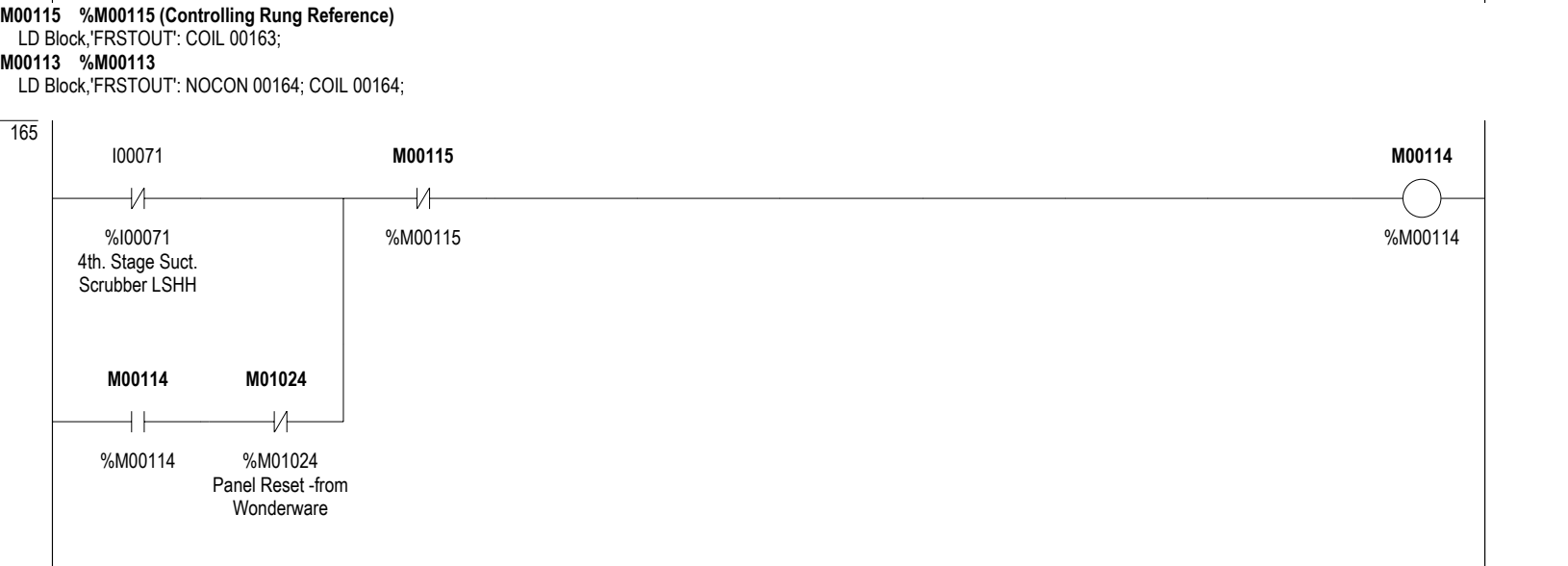
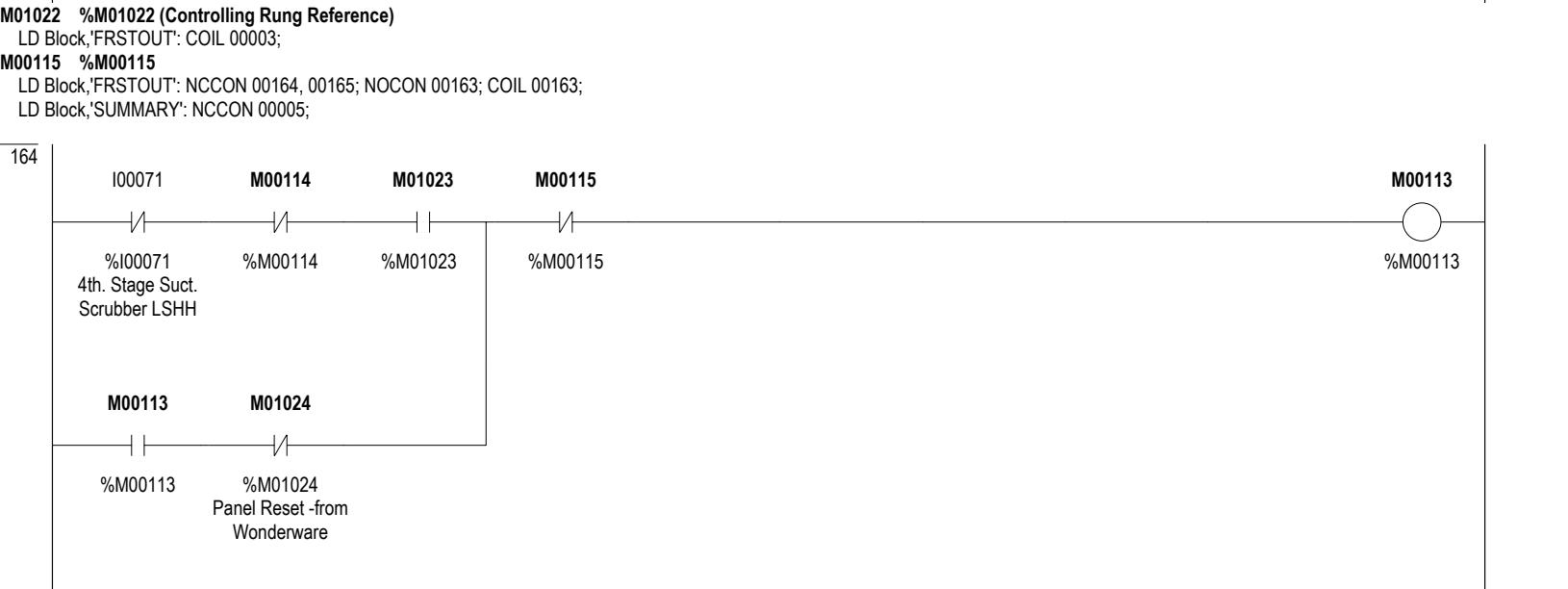
M00109 %M00109

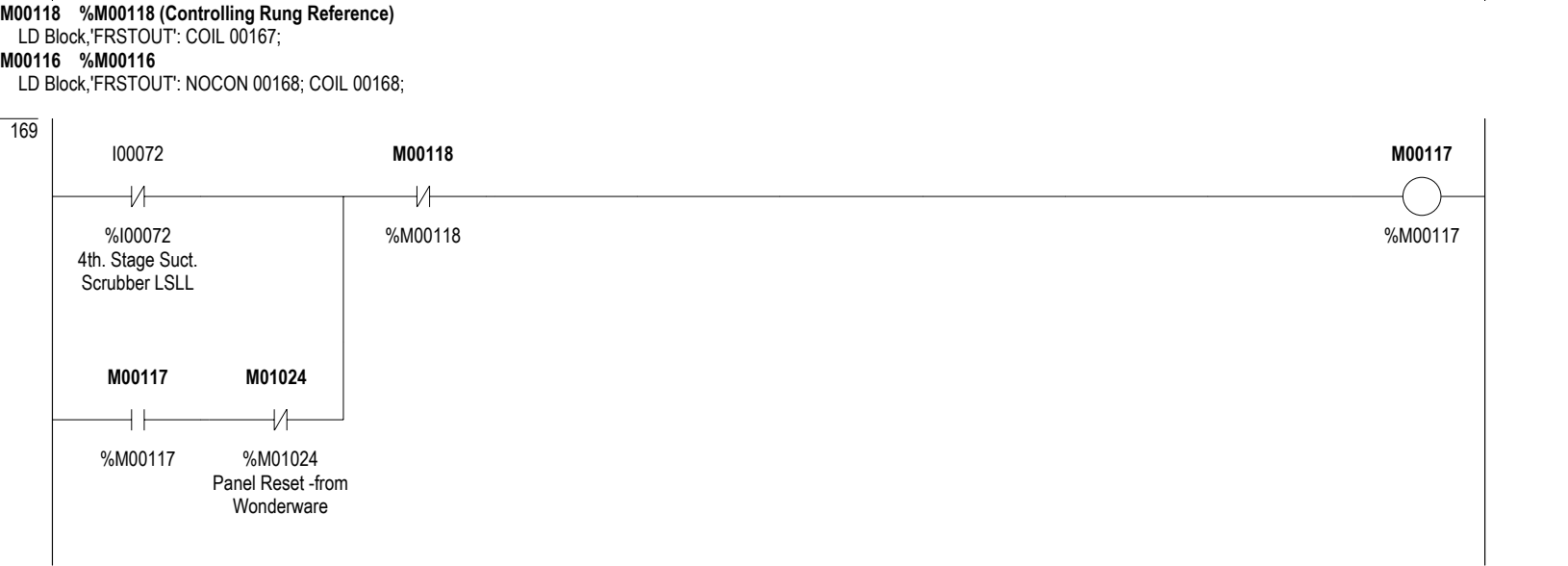
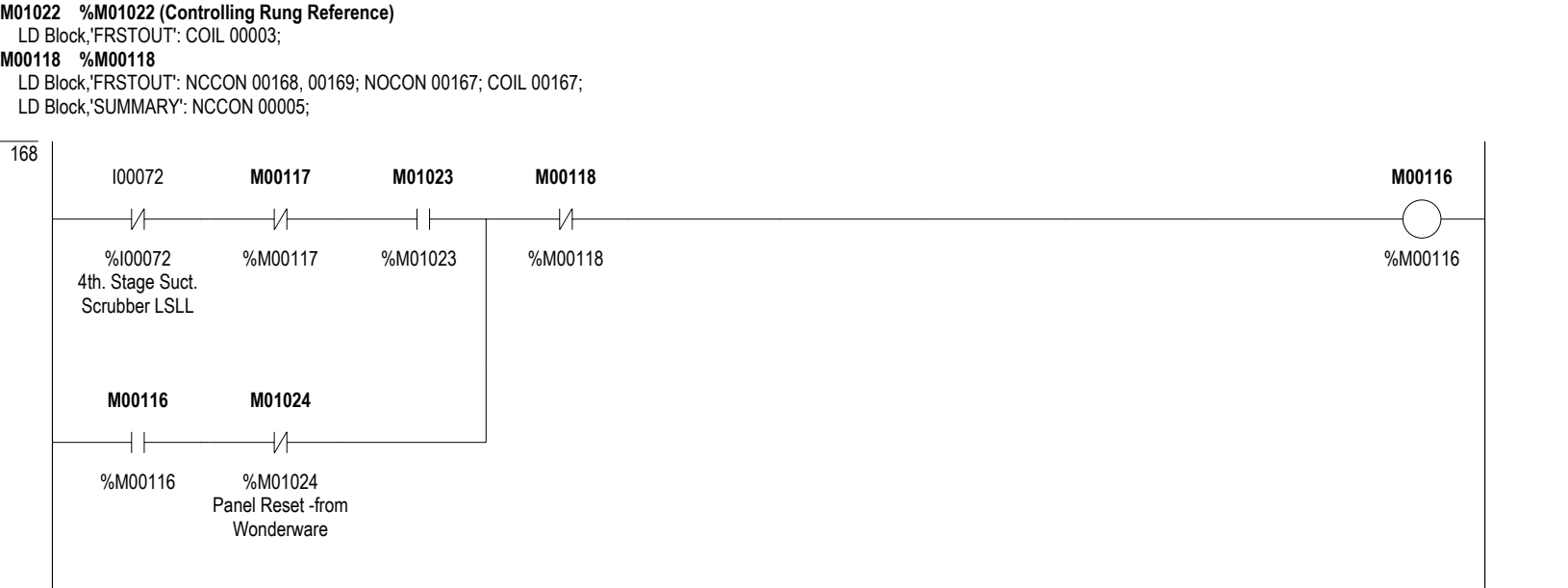
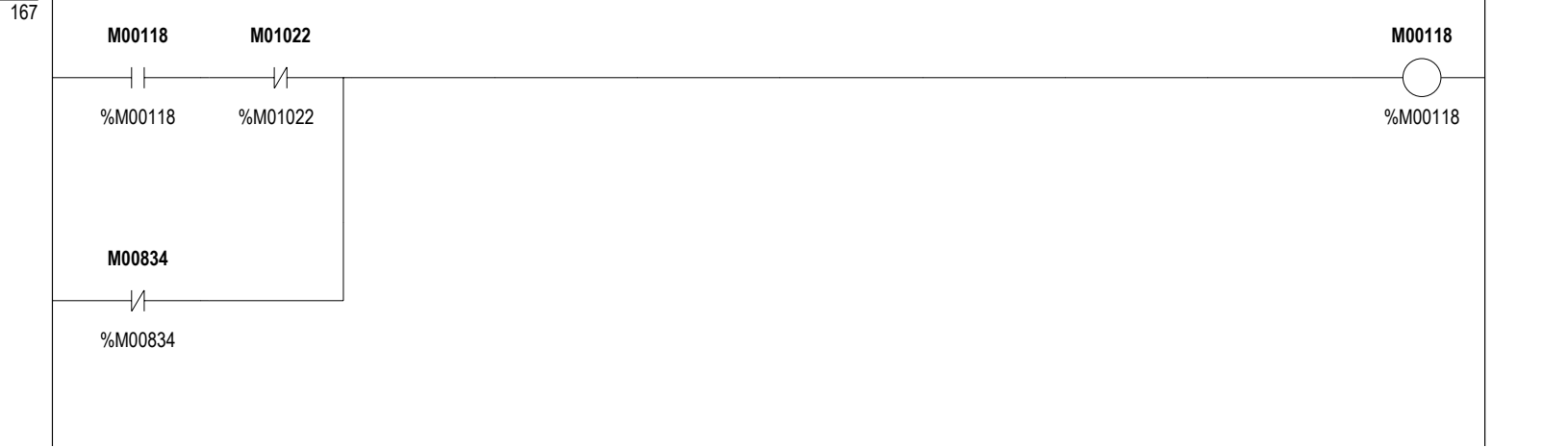
LD Block,'FRSTOUT': NCCON 00156, 00157; NOCON 00155; COIL 00155;

LD Block,'SUMMARY': NCCON 00005;



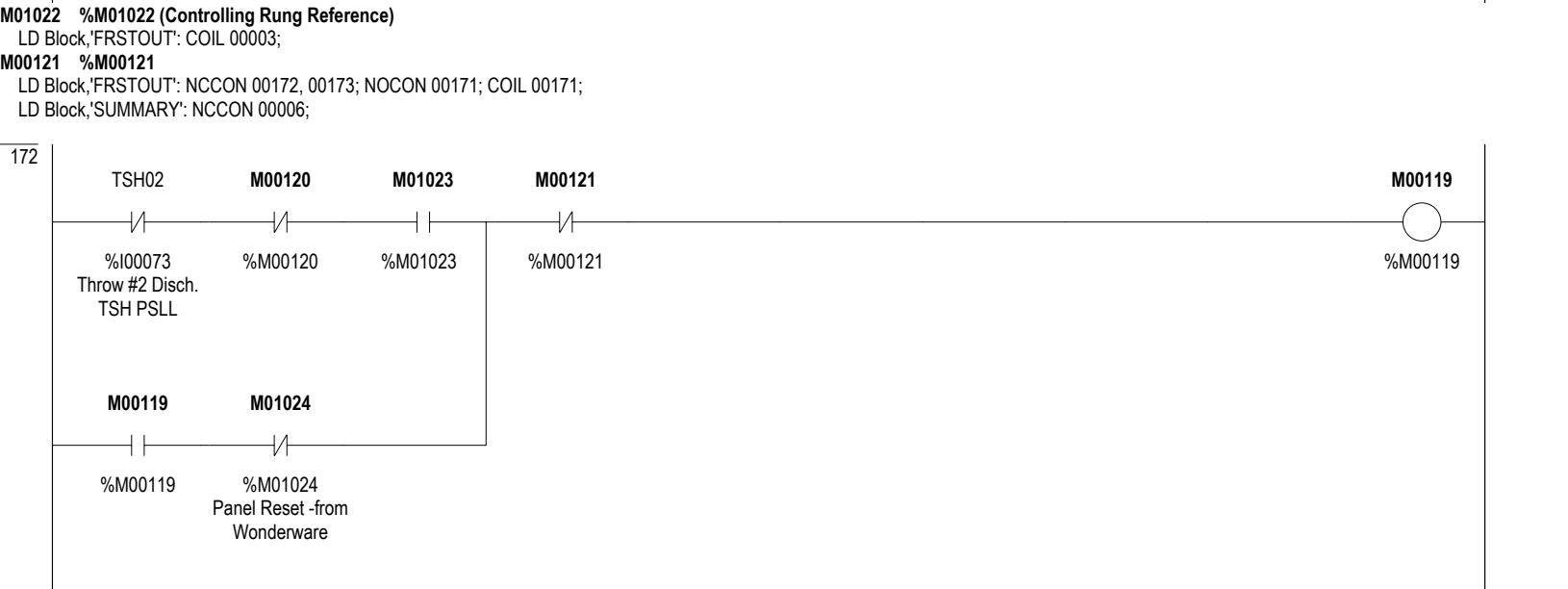
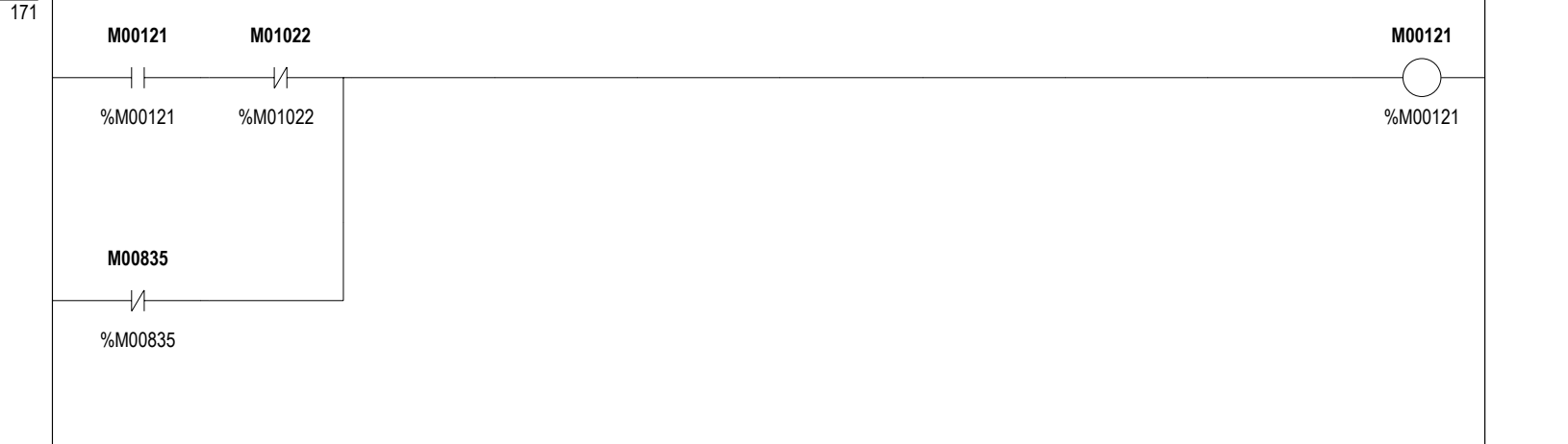


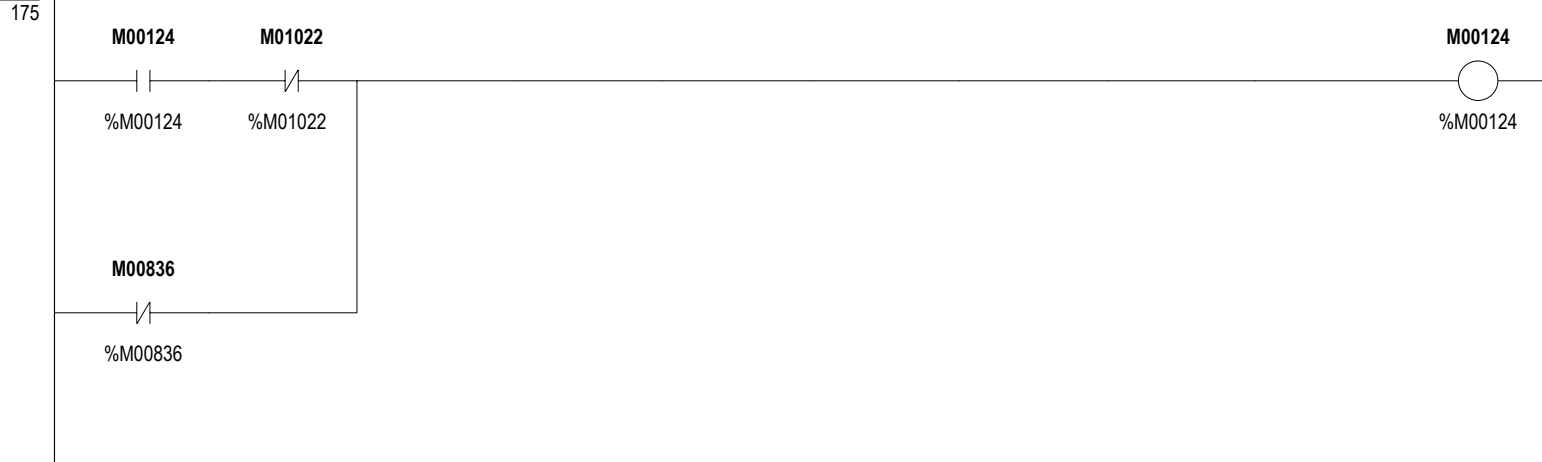




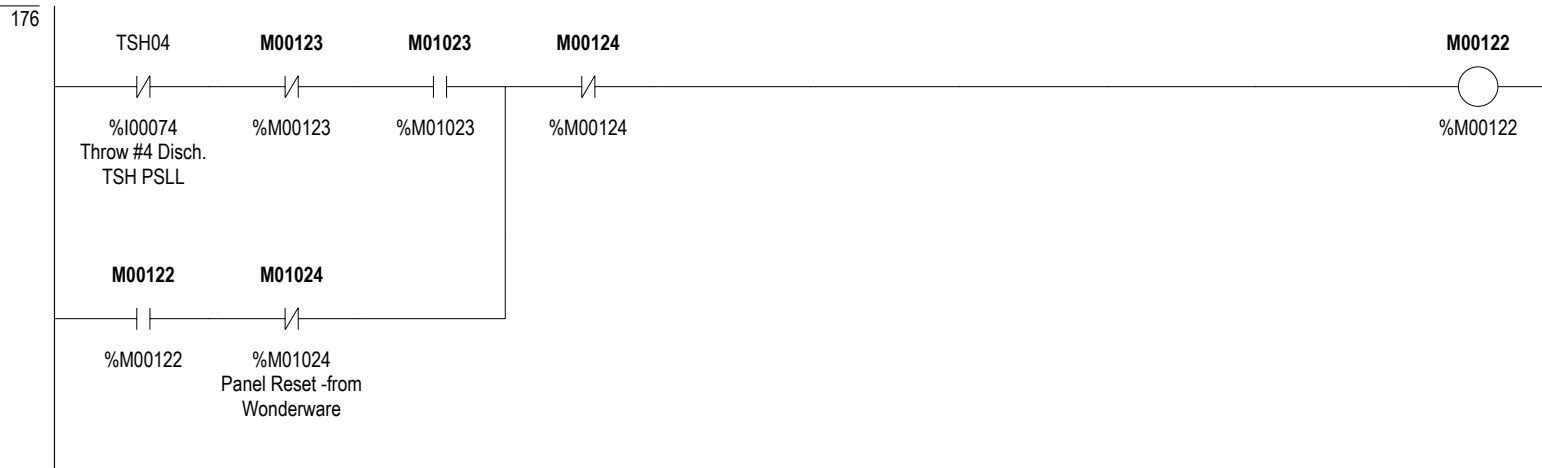
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M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00124 %M00124
LD Block,'FRSTOUT': NCCON 00176, 00177; NOCON 00175; COIL 00175;
LD Block,'SUMMARY': NCCON 00006;

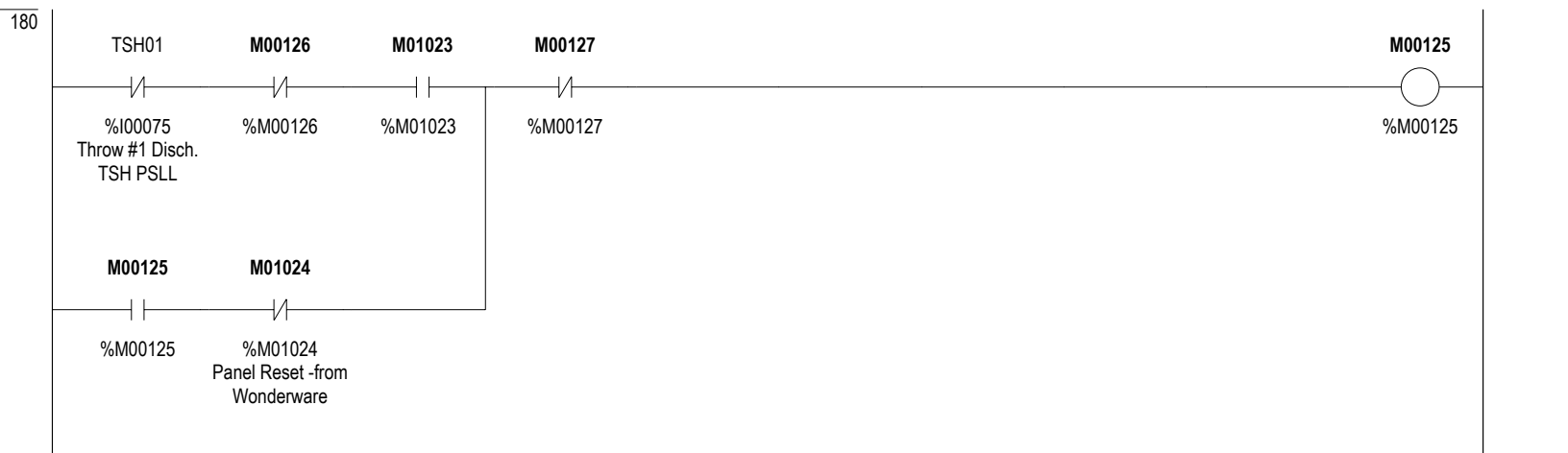
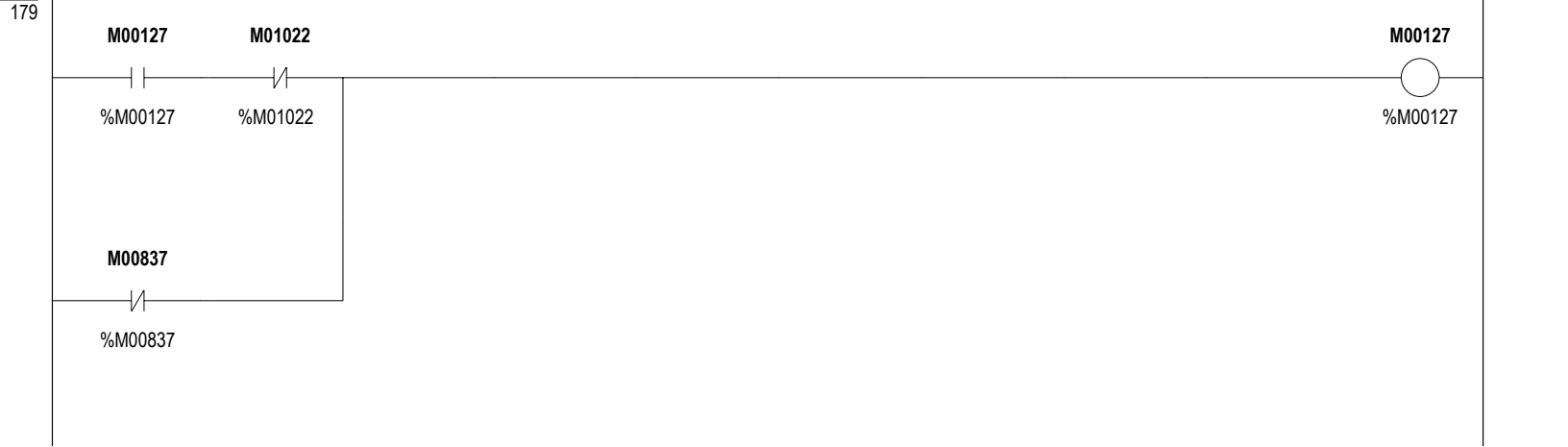


M00124 %M00124 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00175;
M00122 %M00122
LD Block,'FRSTOUT': NOCON 00176; COIL 00176;
LD Block,'SUMMARY': NCCON 00025;



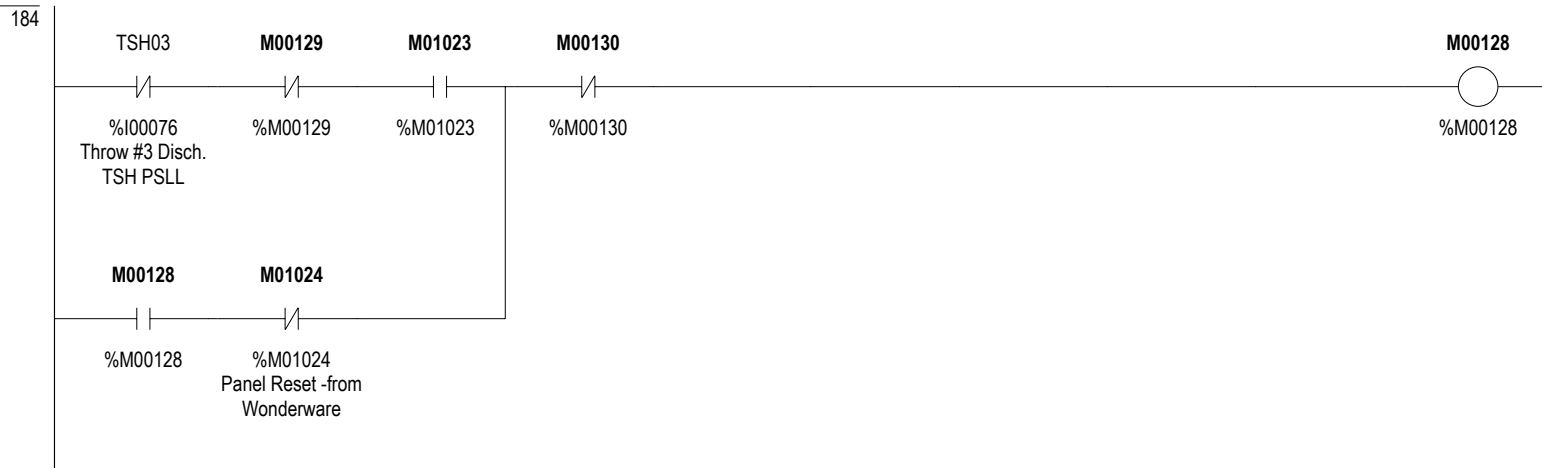
M00124 %M00124 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00175;
M00123 %M00123
LD Block,'FRSTOUT': NCCON 00176; NOCON 00177; COIL 00177;
LD Block,'SUMMARY': NOCON 00044;

178 | COMMENT8191





M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00130 %M00130
LD Block,'FRSTOUT': NCCON 00184, 00185; NOCON 00183; COIL 00183;
LD Block,'SUMMARY': NCCON 00006;

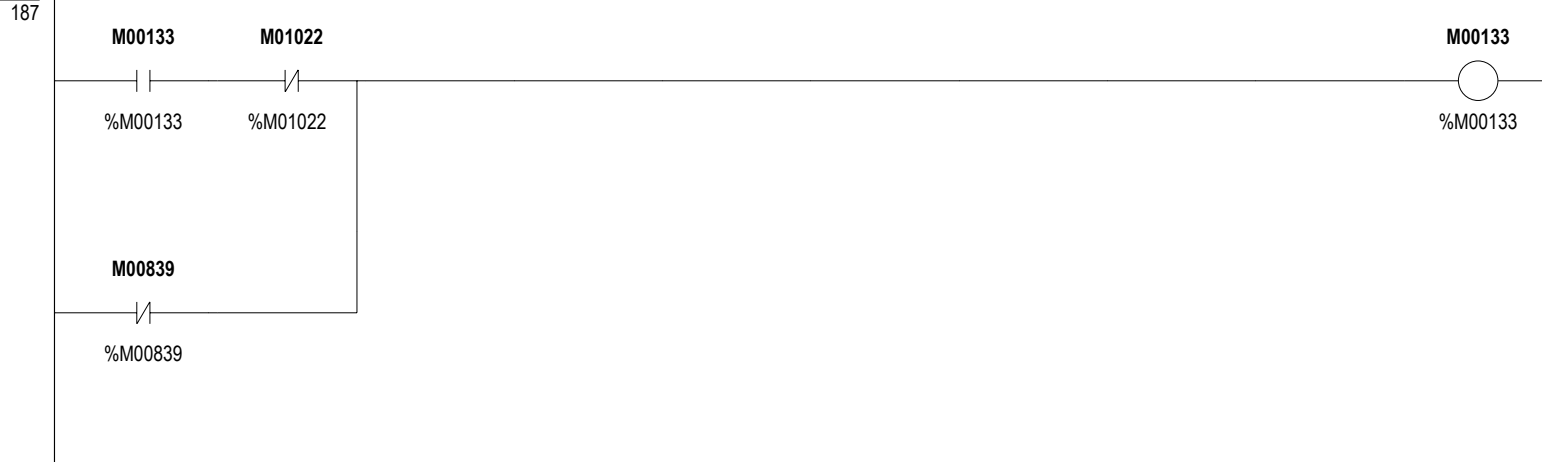


M00130 %M00130 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00183;
M00128 %M00128
LD Block,'FRSTOUT': NOCON 00184; COIL 00184;
LD Block,'SUMMARY': NCCON 00025;

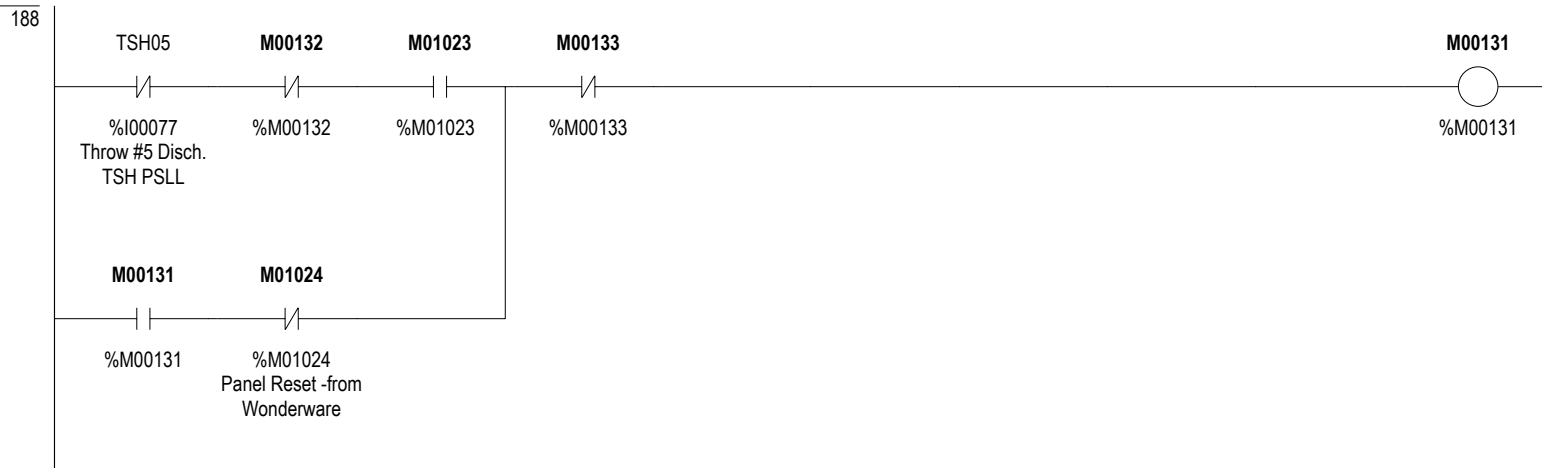


M00130 %M00130 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00183;
M00129 %M00129
LD Block,'FRSTOUT': NCCON 00184; NOCON 00185; COIL 00185;
LD Block,'SUMMARY': NOCON 00044;

186 | COMMENT8191



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00133 %M00133
LD Block,'FRSTOUT': NCCON 00188, 00189; NOCON 00187; COIL 00187;
LD Block,'SUMMARY': NCCON 00006;

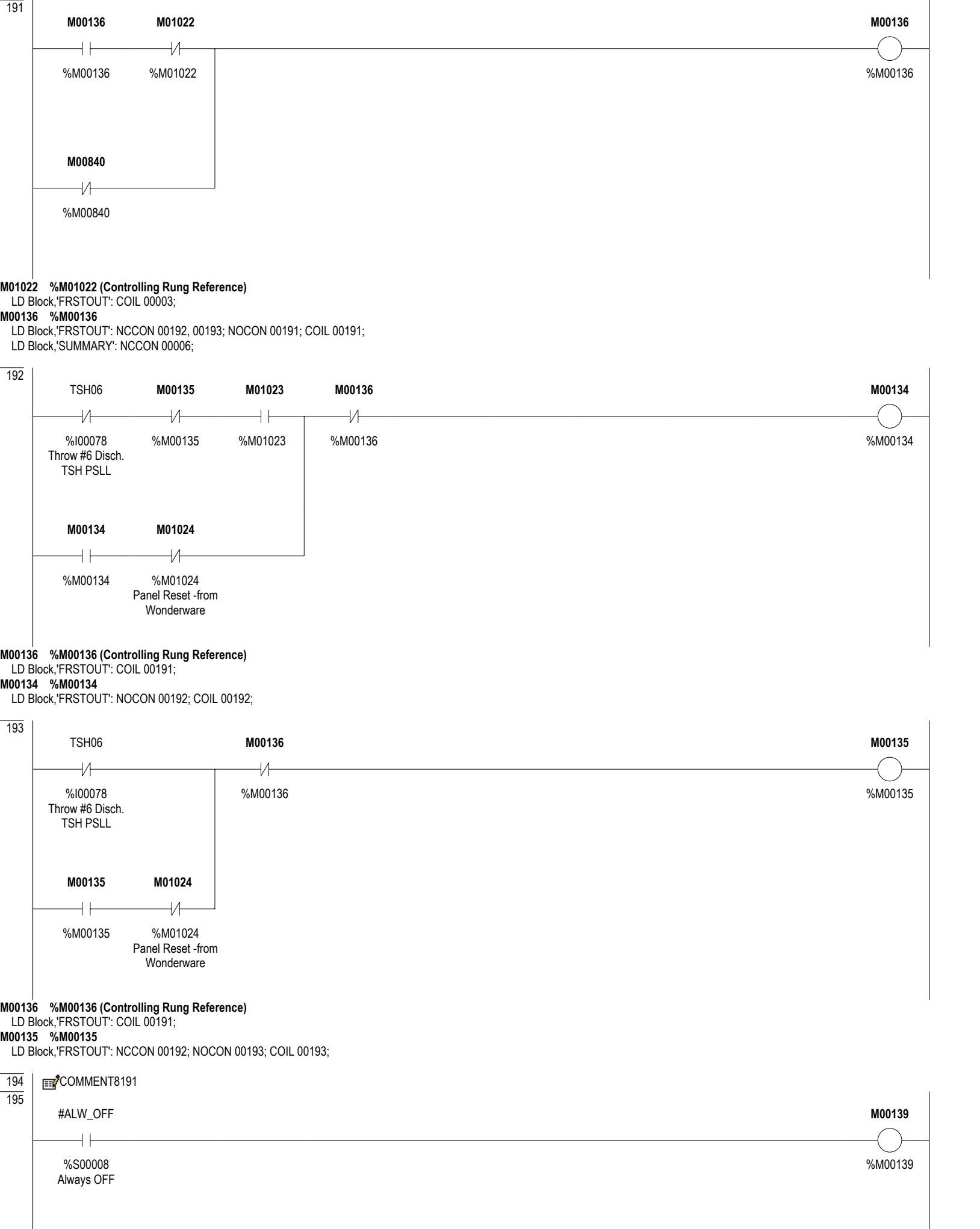


M00133 %M00133 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00187;
M00131 %M00131
LD Block,'FRSTOUT': NOCON 00188; COIL 00188;



M00133 %M00133 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00187;
M00132 %M00132
LD Block,'FRSTOUT': NCCON 00188; NOCON 00189; COIL 00189;

190 | COMMENT8191



196

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graph LR
    CFUNCT[CFUNCT] --- M00138[M00138]
    M00138 --- M01023[M01023]
    M01023 --- M00139((M00139))
    M00137[M00137] --- M01024[M01024]
    M01024 --- M00139
  
```

CFUNCT M00138 M01023 M00139 M00137

%I00079
"C" Function
Unload

%M00138

%M01023

%M00137

%M01024
Panel Reset -from
Wonderware

%M00139

%M00137

197

CFUNCT

M00139

M00138

%I00079
"C" Function
Unload

%M00139

M00138

M01024

%M00138

%M01024
Panel Reset -from
Wonderware

M00138

198	COMMENT8191	
199	#ALW_OFF	M00142

%S00008
 Always OFF

%M00142

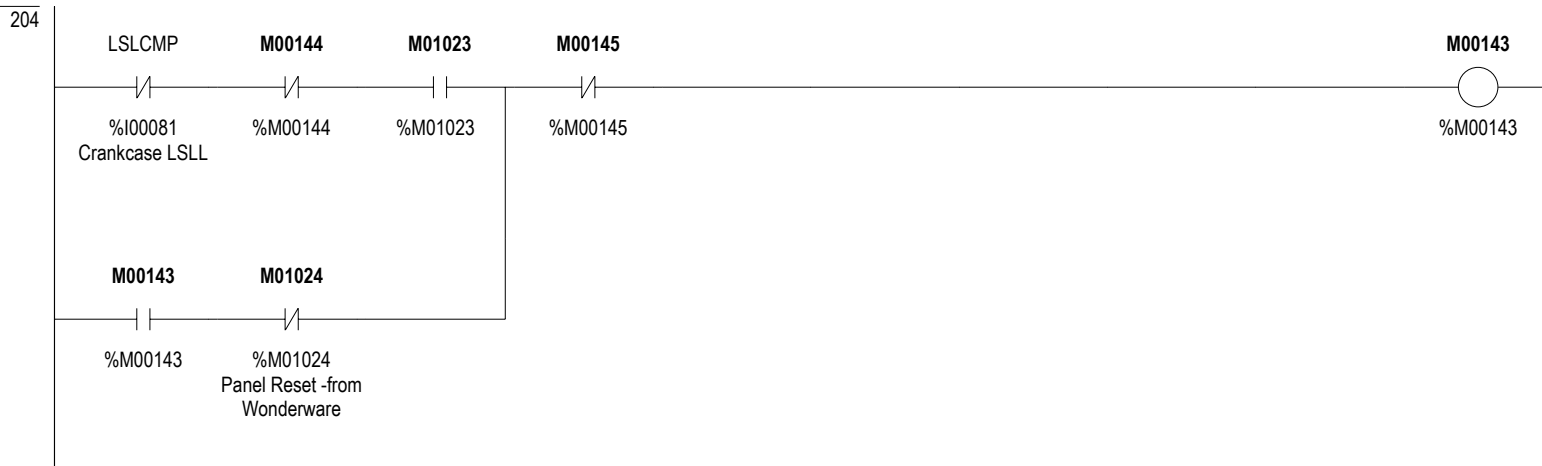
Page 85

200

M00141 %M00141
LD Block,'FRSTOUT': NCCON 00200; NOCON 00201; COIL 00201;
LD Block,'SUMMARY': NOCON 00044;
LD Block,'STATS': NCCON 00013;



M00145 %M00145
LD Block,'FRSTOUT': NCCON 00204, 00205; COIL 00203;
LD Block,'SUMMARY': NCCON 00007;

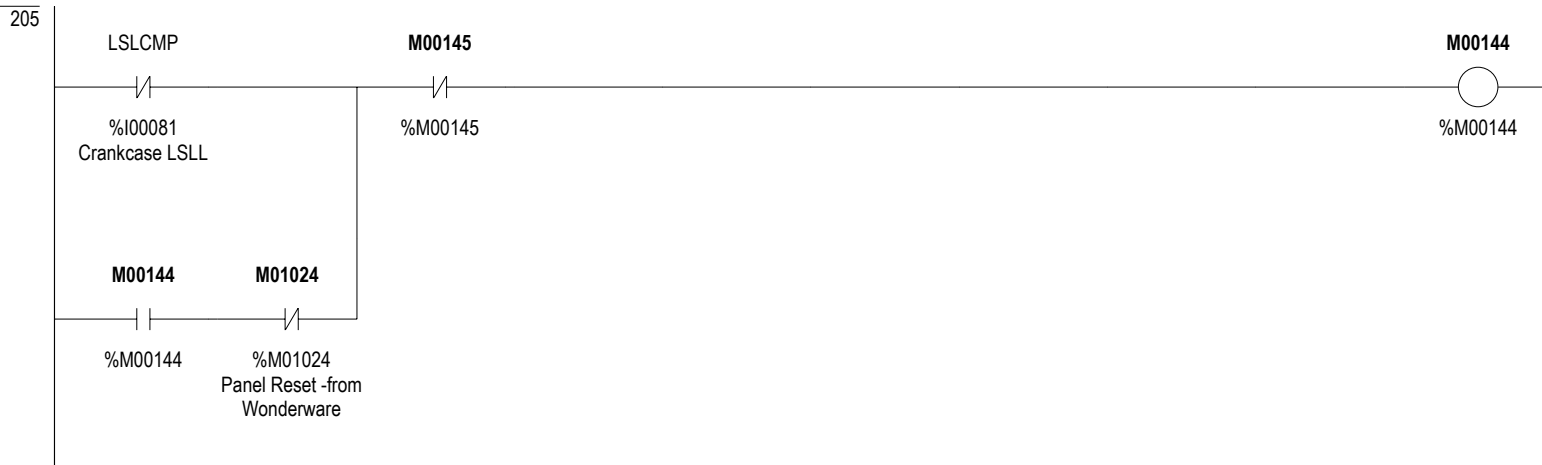


M00145 %M00145 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00203;

M00143 %M00143

LD Block,'FRSTOUT': NOCON 00204; COIL 00204;
LD Block,'SUMMARY': NCCON 00026;



M00145 %M00145 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00203;

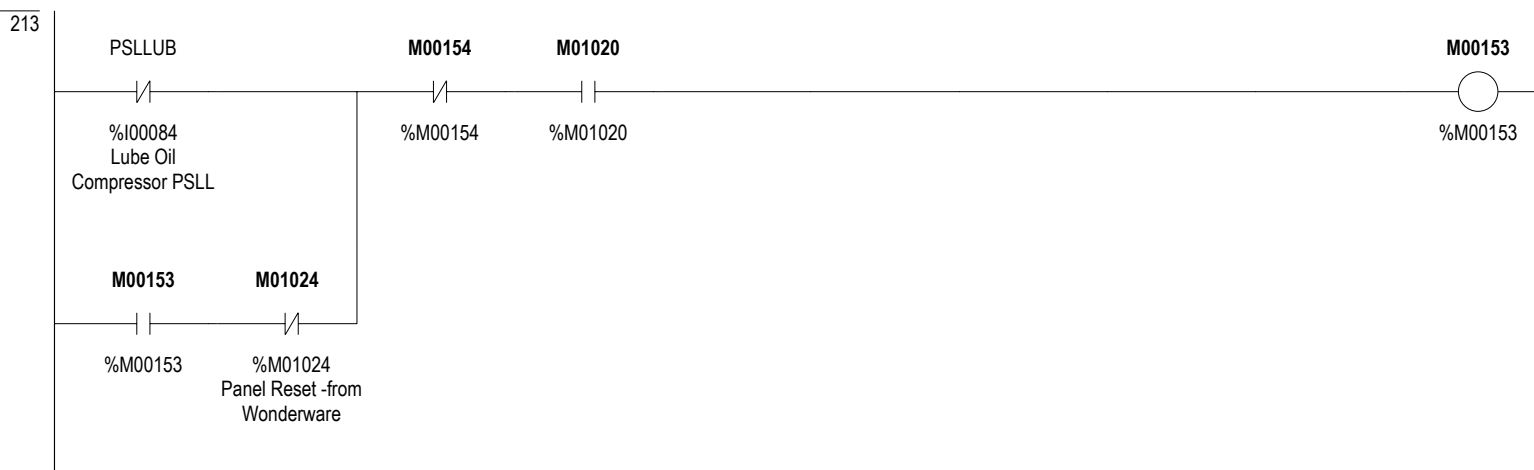
M00144 %M00144

LD Block,'FRSTOUT': NCCON 00204; NOCON 00205; COIL 00205;
LD Block,'SUMMARY': NOCON 00045;



M00148 %M00148
LD Block,'FRSTOUT': NCCON 00208, 00209; COIL 00207;
LD Block,'SUMMARY': NCCON 00007;

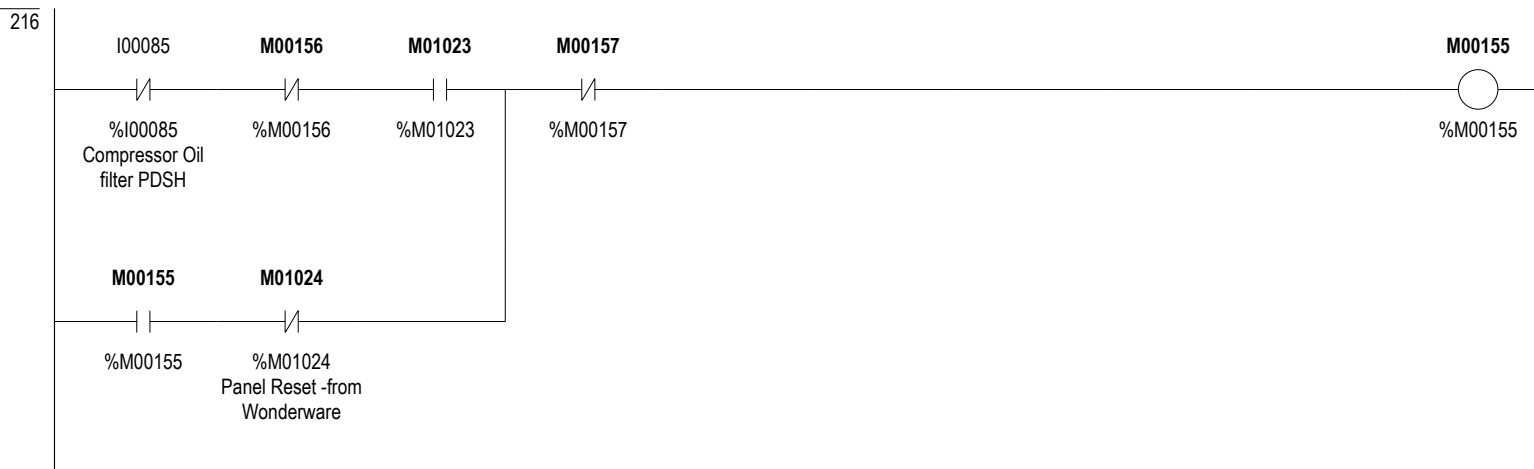
M01020 %M01020 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00001;
M00152 %M00152
LD Block,'FRSTOUT': NOCON 00212; COIL 00212;
LD Block,'SUMMARY': NCCON 00026;



M00154 %M00154 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00211;
M01020 %M01020 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00001;
M00153 %M00153
LD Block,'FRSTOUT': NCCON 00212; NOCON 00213; COIL 00213;
LD Block,'SUMMARY': NOCON 00045;



M00157 %M00157
LD Block,'FRSTOUT': NCCON 00216, 00217; COIL 00215;
LD Block,'SUMMARY': NCCON 00007;



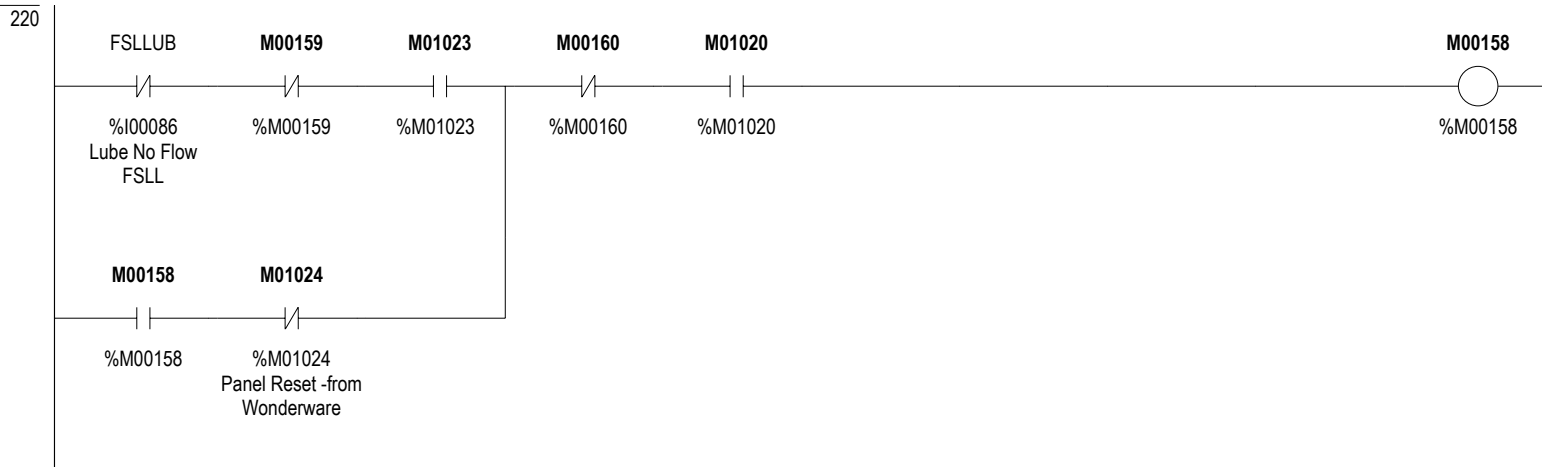
M00157 %M00157 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00215;
M00155 %M00155
LD Block,'FRSTOUT': NOCON 00216; COIL 00216;



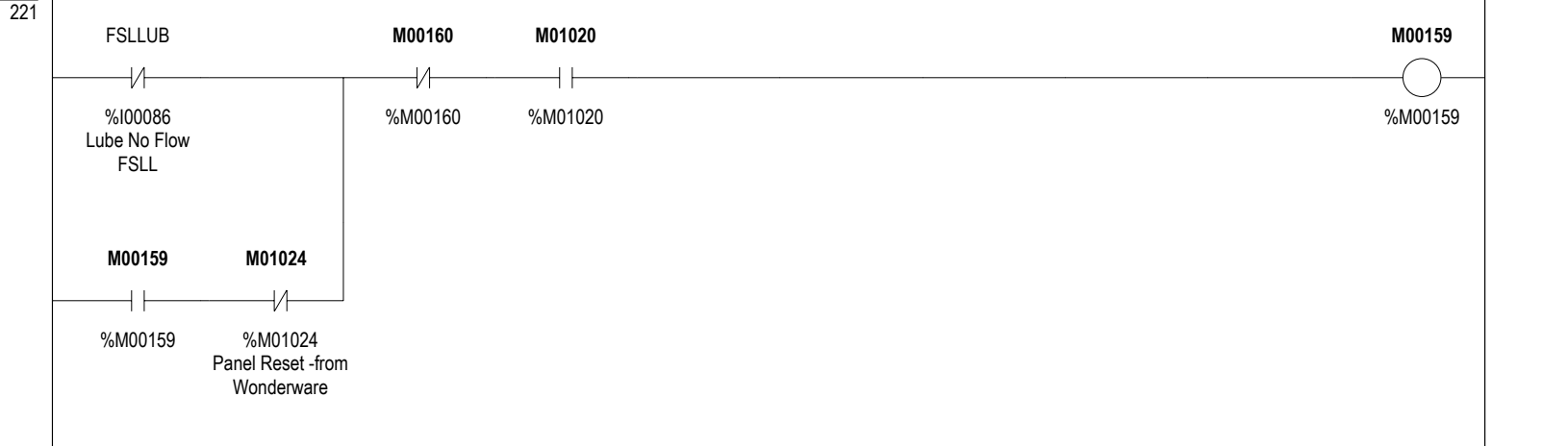
M00157 %M00157 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00215;
M00156 %M00156
LD Block,'FRSTOUT': NCCON 00216; NOCON 00217; COIL 00217;



M00160 %M00160
LD Block,'FRSTOUT': NCCON 00220, 00221; COIL 00219;
LD Block,'SUMMARY': NCCON 00007;



M00160 %M00160 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00219;
M01020 %M01020 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00001;
M00158 %M00158
LD Block,'FRSTOUT': NOCON 00220; COIL 00220;
LD Block,'SUMMARY': NCCON 00026;



M00160 %M00160 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00219;

M01020 %M01020 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00001;

M00159 %M00159

LD Block,'FRSTOUT': NCCON 00220; NOCON 00221; COIL 00221;

LD Block,'SUMMARY': NOCON 00045;

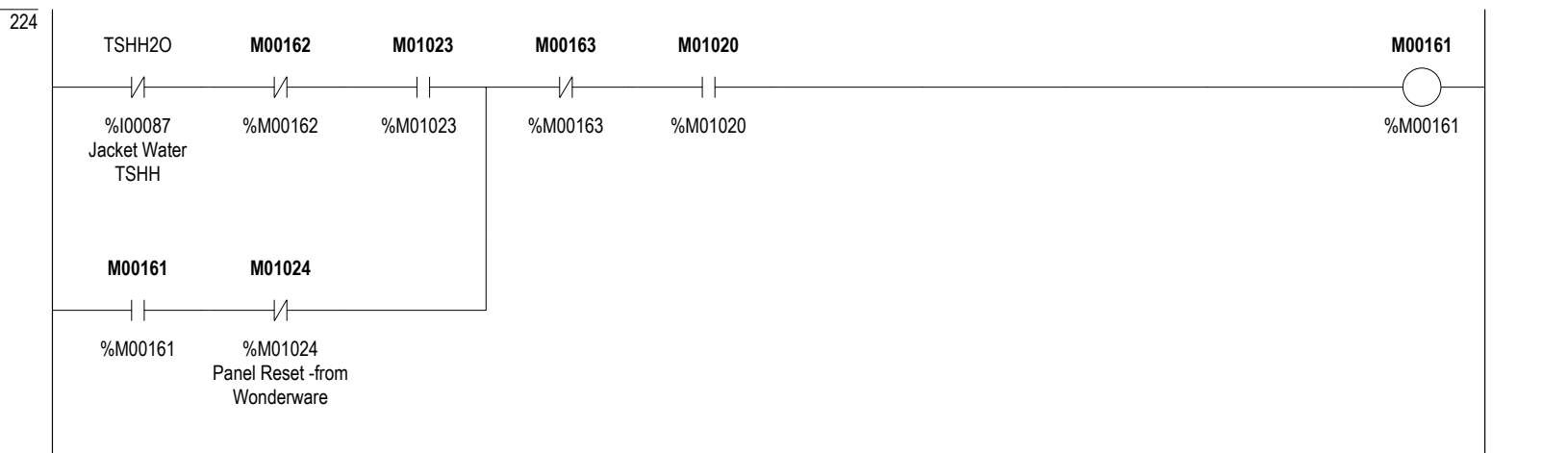
222 COMMENT8191



M00163 %M00163

LD Block,'FRSTOUT': NCCON 00224, 00225; COIL 00223;

LD Block,'SUMMARY': NCCON 00007;



M00163 %M00163 (Controlling Rung Reference)

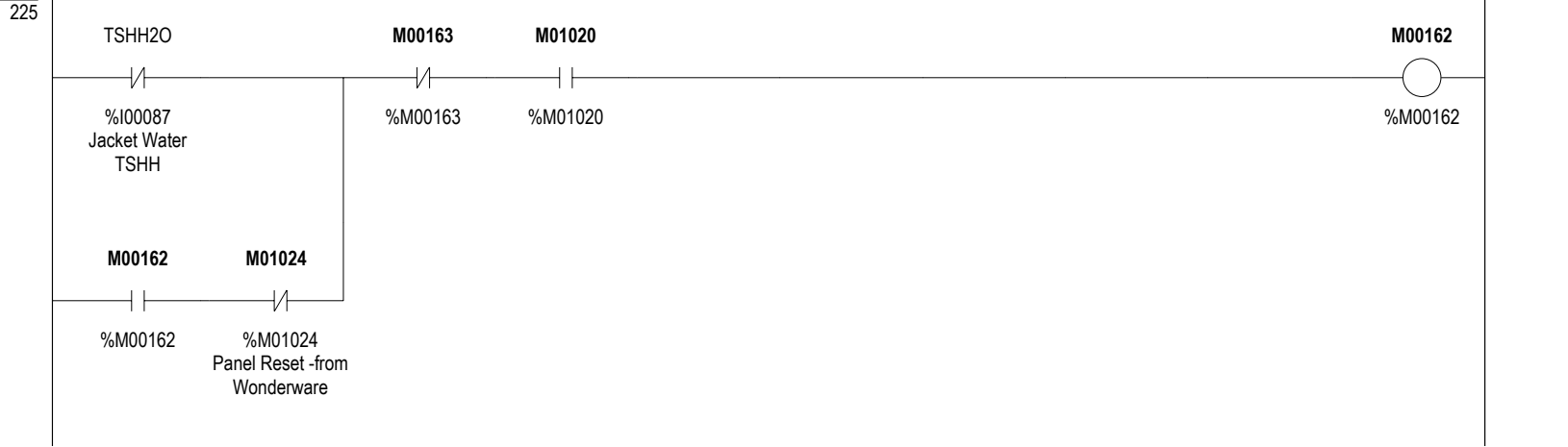
LD Block,'FRSTOUT': COIL 00223;

M01020 %M01020 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00001;

M00161 %M00161

LD Block,'FRSTOUT': NOCON 00224; COIL 00224;



M00163 %M00163 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00223;

M01020 %M01020 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00001;

M00162 %M00162

LD Block,'FRSTOUT': NCCON 00224; NOCON 00225; COIL 00225;

226 COMMENT0

227

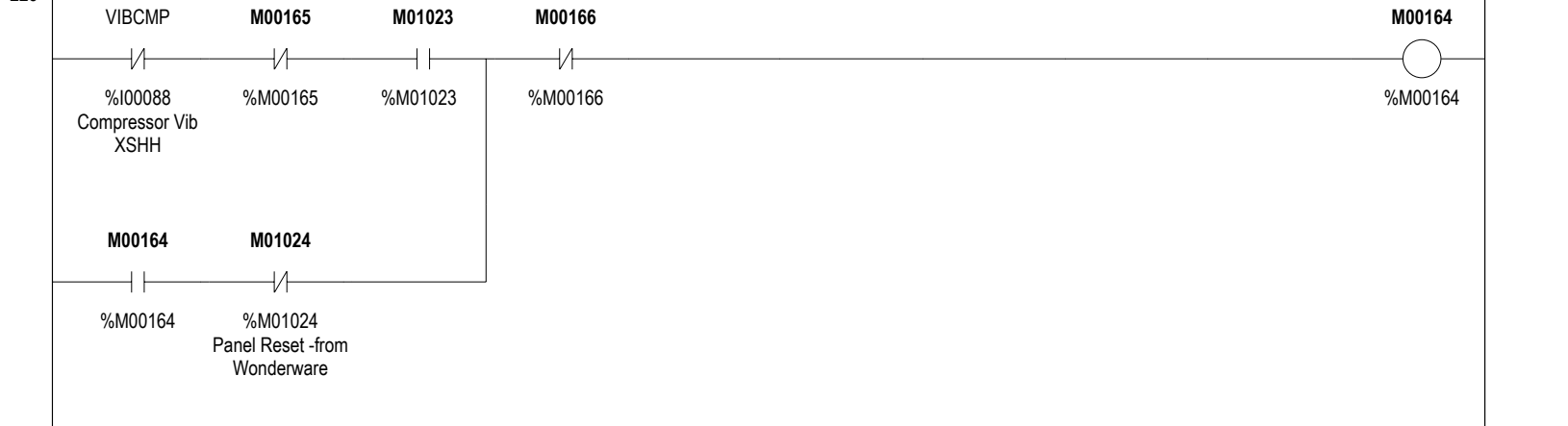


M00166 %M00166

LD Block,'FRSTOUT': NCCON 00228, 00229; COIL 00227;

LD Block,'SUMMARY': NCCON 00007;

228



M00166 %M00166 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00227;

M00164 %M00164

LD Block,'FRSTOUT': NOCON 00228; COIL 00228;

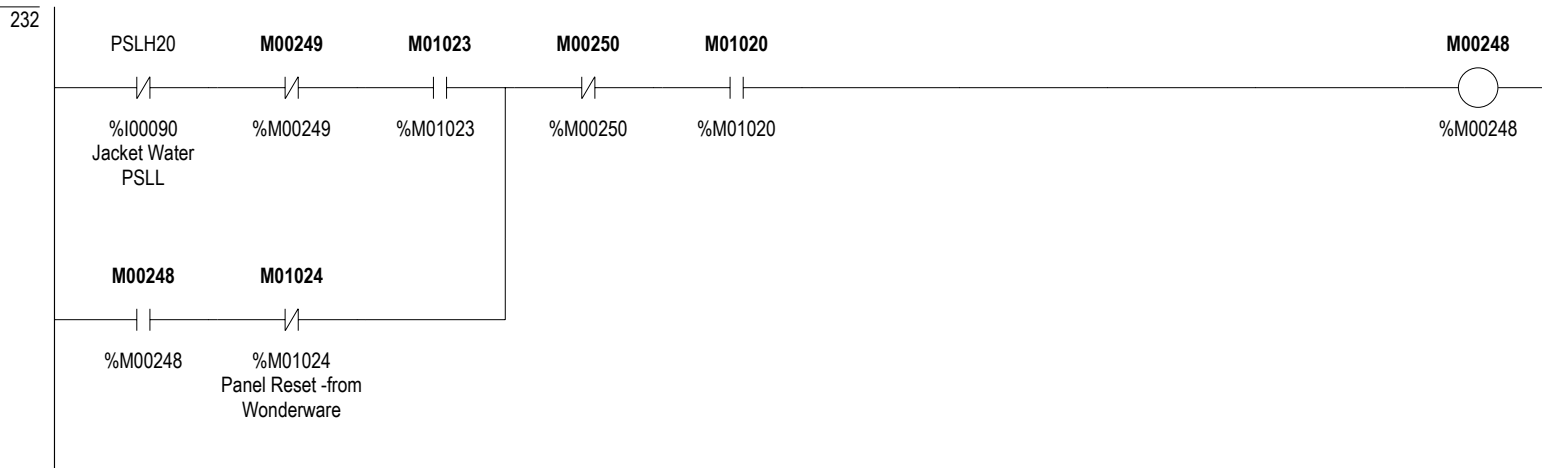
LD Block,'SUMMARY': NCCON 00026;



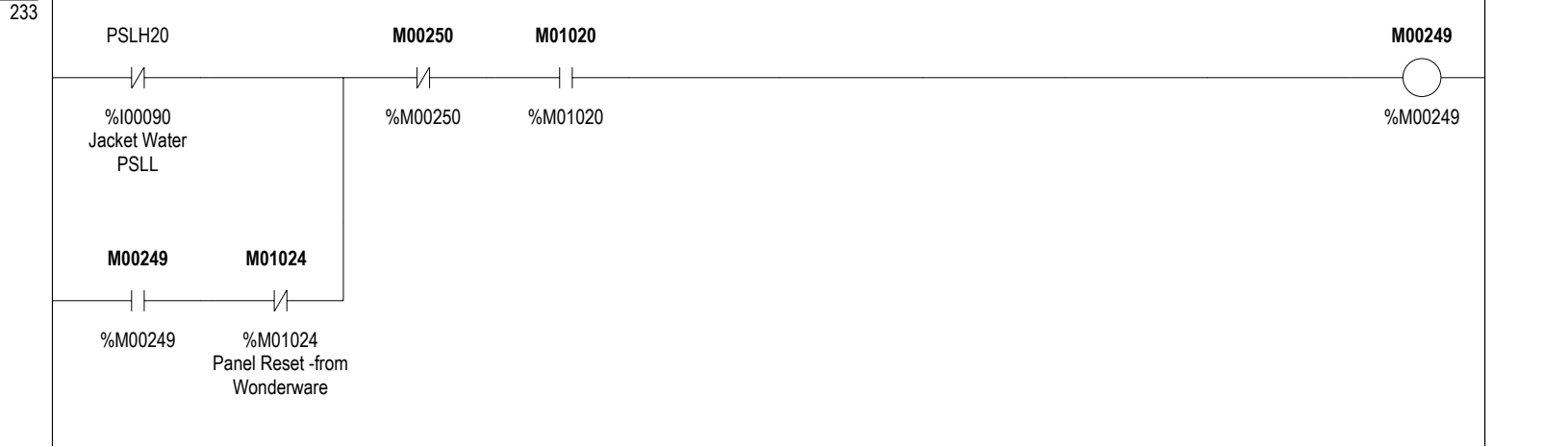
M00166 %M00166 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00227;
M00165 %M00165
LD Block,'FRSTOUT': NCCON 00228; NOCON 00229; COIL 00229;
LD Block,'SUMMARY': NOCON 00046;



M00250 %M00250
LD Block,'FRSTOUT': NCCON 00232, 00233; COIL 00231;
LD Block,'SUMMARY': NCCON 00008;



M00250 %M00250 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00231;
M01020 %M01020 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00001;
M00248 %M00248
LD Block,'FRSTOUT': NOCON 00232; COIL 00232;
LD Block,'SUMMARY': NCCON 00029;



M00250 %M00250 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00231;

M01020 %M01020 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00001;

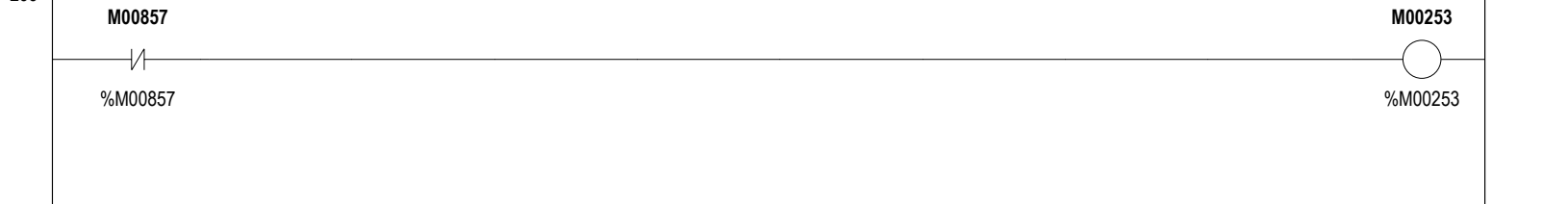
M00249 %M00249

LD Block,'FRSTOUT': NCCON 00232; NOCON 00233; COIL 00233;

LD Block,'SUMMARY': NOCON 00049;

234 COMMENT11

235

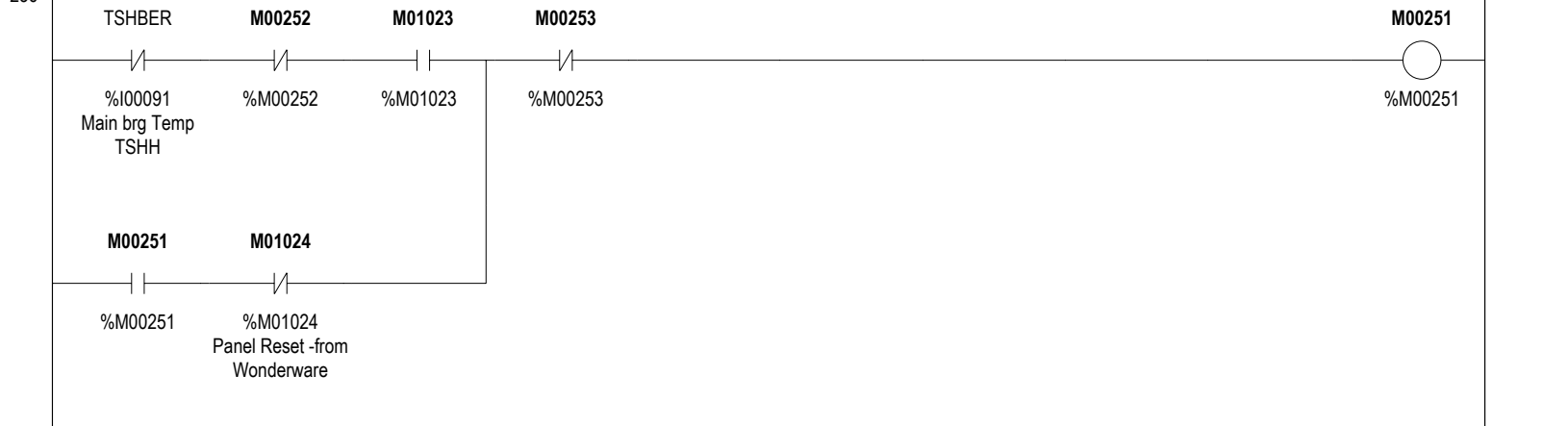


M00253 %M00253

LD Block,'FRSTOUT': NCCON 00236, 00237; COIL 00235;

LD Block,'SUMMARY': NCCON 00008;

236

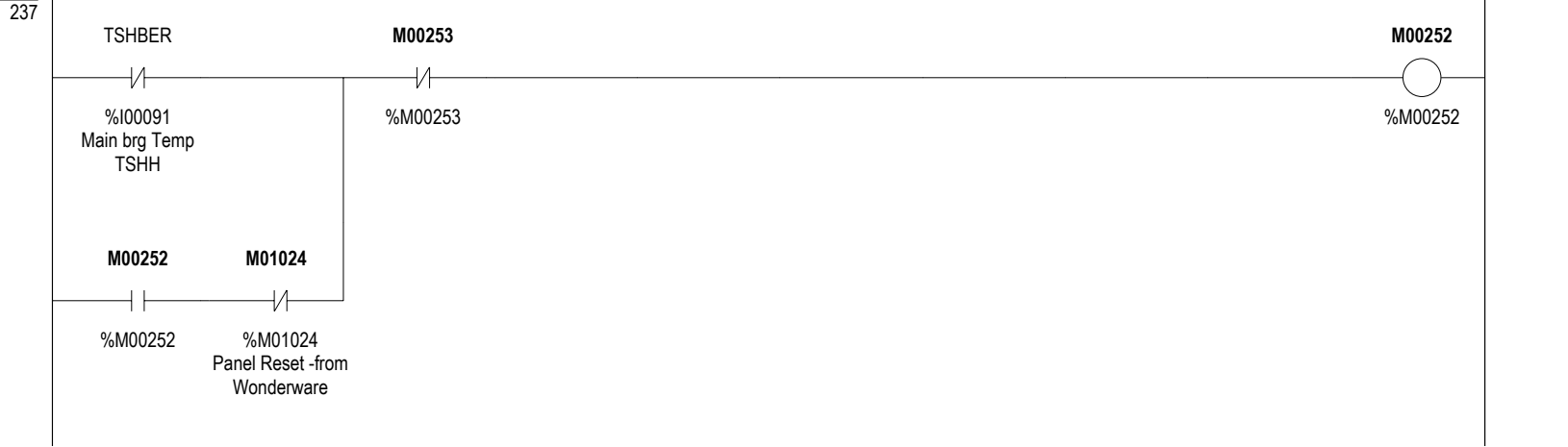


M00253 %M00253 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00235;

M00251 %M00251

LD Block,'FRSTOUT': NOCON 00236; COIL 00236;



M00253 %M00253 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00235;

M00252 %M00252

LD Block,'FRSTOUT': NCCON 00236; NOCON 00237; COIL 00237;



M00218 %M00218

LD Block,'FRSTOUT': NCCON 00241, 00242; COIL 00239;

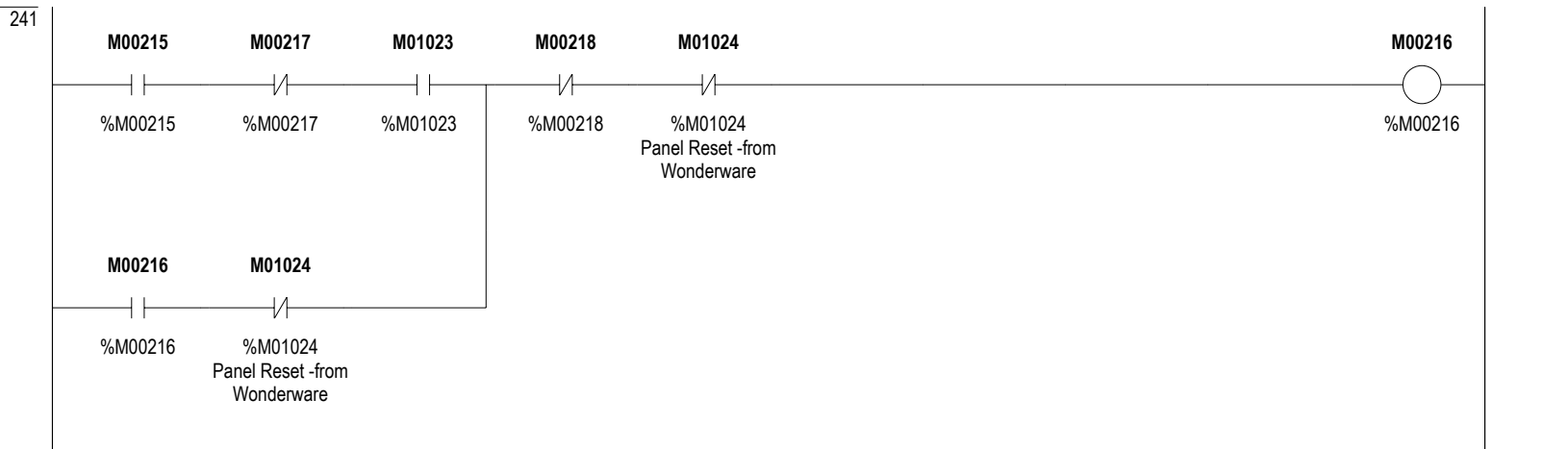
LD Block,'SUMMARY': NCCON 00019;



M00215 %M00215

LD Block,'FRSTOUT': NEGCOIL 00240; NOCON 00241, 00242;

LD Block,'PREPURG': NOCON 00027; COIL 00026;



M00215 %M00215 (Controlling Rung Reference)

LD Block,'FRSTOUT': NEGCOIL 00240;

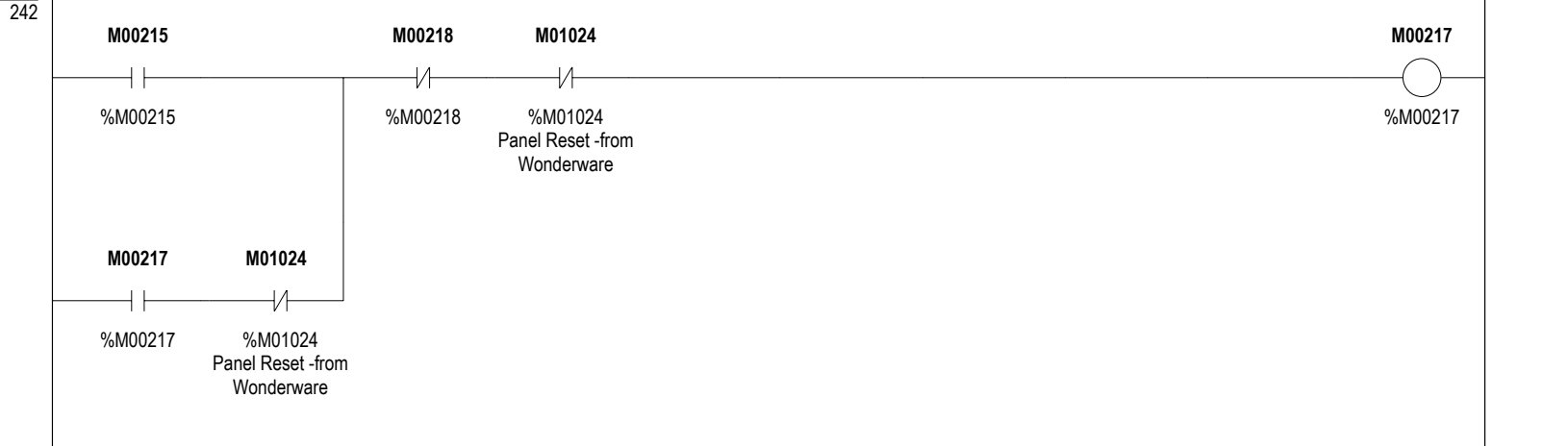
M00218 %M00218 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00239;

M00216 %M00216

LD Block,'FRSTOUT': NOCON 00241; COIL 00241;

LD Block,'SUMMARY': NCCON 00038;



M00215 %M00215 (Controlling Rung Reference)

LD Block,'FRSTOUT': NEGCOIL 00240;

M00218 %M00218 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00239;

M00217 %M00217

LD Block,'FRSTOUT': NCCON 00241; NOCON 00242; COIL 00242;

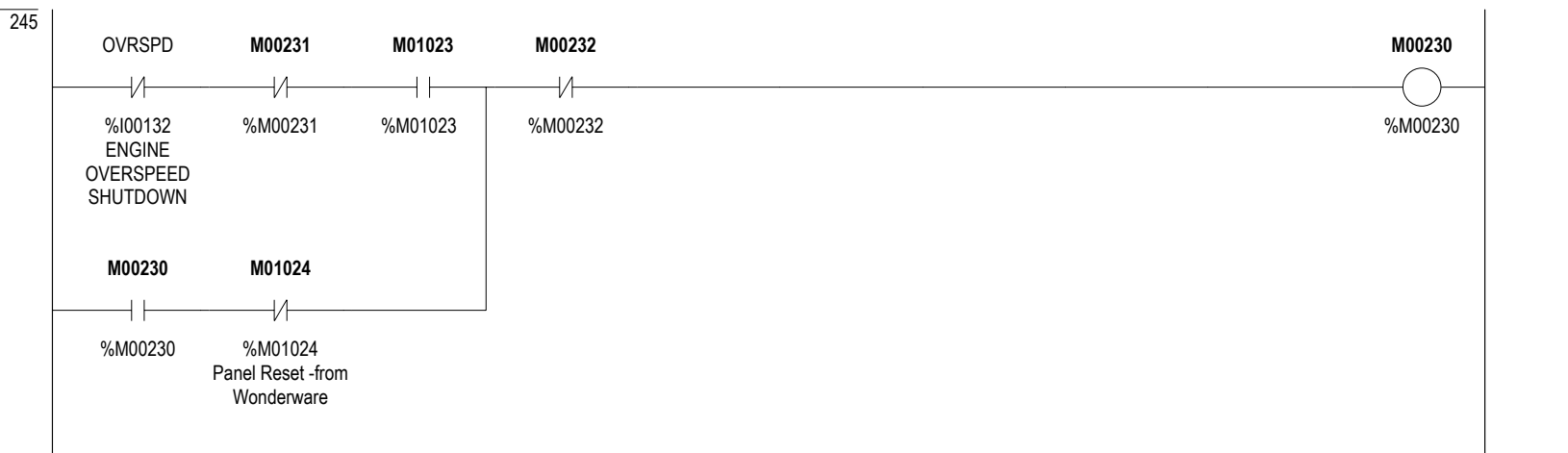
LD Block,'SUMMARY': NOCON 00048;



M00232 %M00232

LD Block,'FRSTOUT': NCCON 00245, 00246; COIL 00244;

LD Block,'SUMMARY': NCCON 00019;



M00232 %M00232 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00244;

M00230 %M00230

LD Block,'FRSTOUT': NOCON 00245; COIL 00245;



M00232 %M00232 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00244;

M00231 %M00231

LD Block,'FRSTOUT': NCCON 00245; NOCON 00246; COIL 00246;

LD Block,'SUMMARY': NOCON 00048;

247 COMMENT14

248

#ALW_OFF

M00256

%S00008
Always OFF

%M00256

M00256 %M00256

LD Block,'FRSTOUT': NCCON 00249, 00250; COIL 00248;

LD Block,'SUMMARY': NCCON 00008;

249

TRB1OS

M00255

M01023

M00256

M00254

M00256 %M00256 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00248;

M00254 %M00254

LD Block,'FRSTOUT': NOCON 00249; COIL 00249;

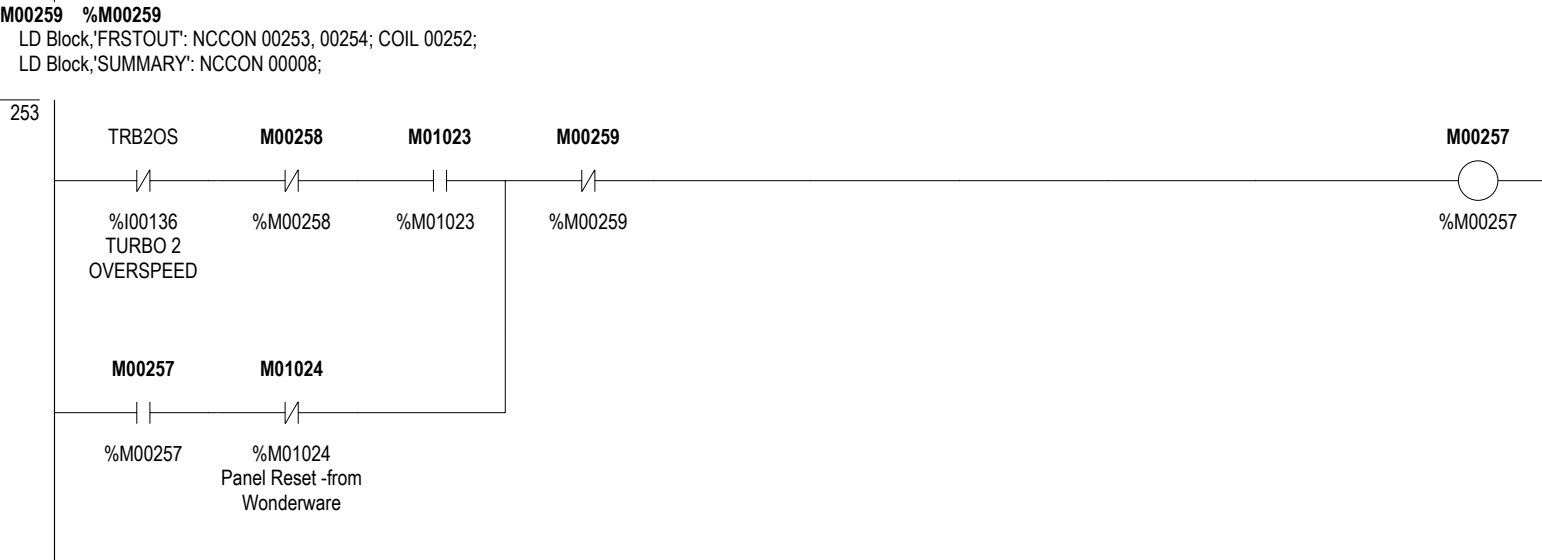
LD Block,'SUMMARY': NCCON 00029;

250

TRB1OS

M00256

M00255





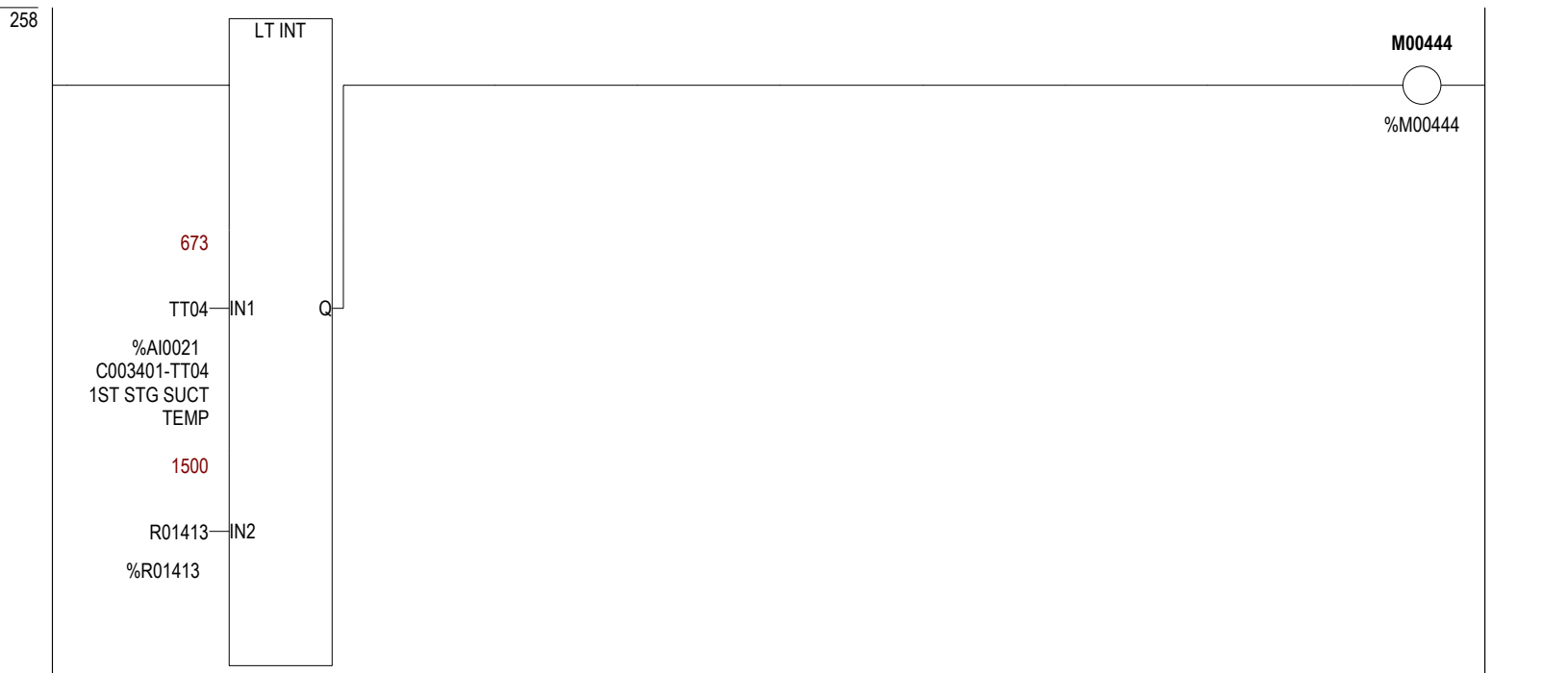
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00294 %M00294

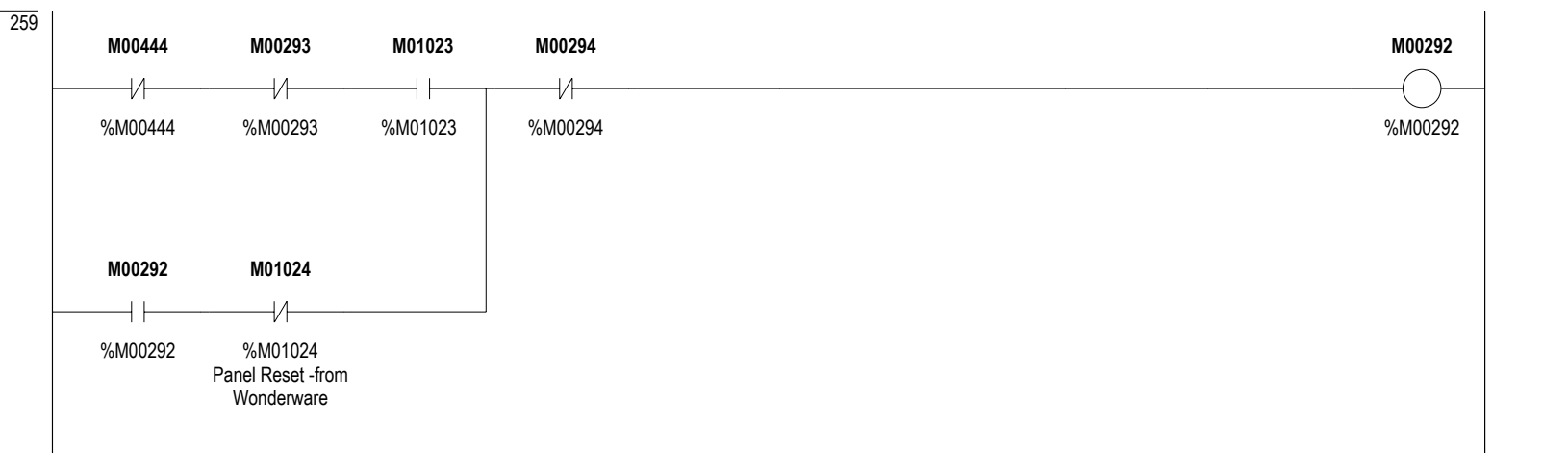
LD Block,'FRSTOUT': NCCON 00259, 00260; NOCON 00257; COIL 00257;

LD Block,'SUMMARY': NCCON 00011;



M00444 %M00444

LD Block,'FRSTOUT': NCCON 00259, 00260; COIL 00258;



M00444 %M00444 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00258;

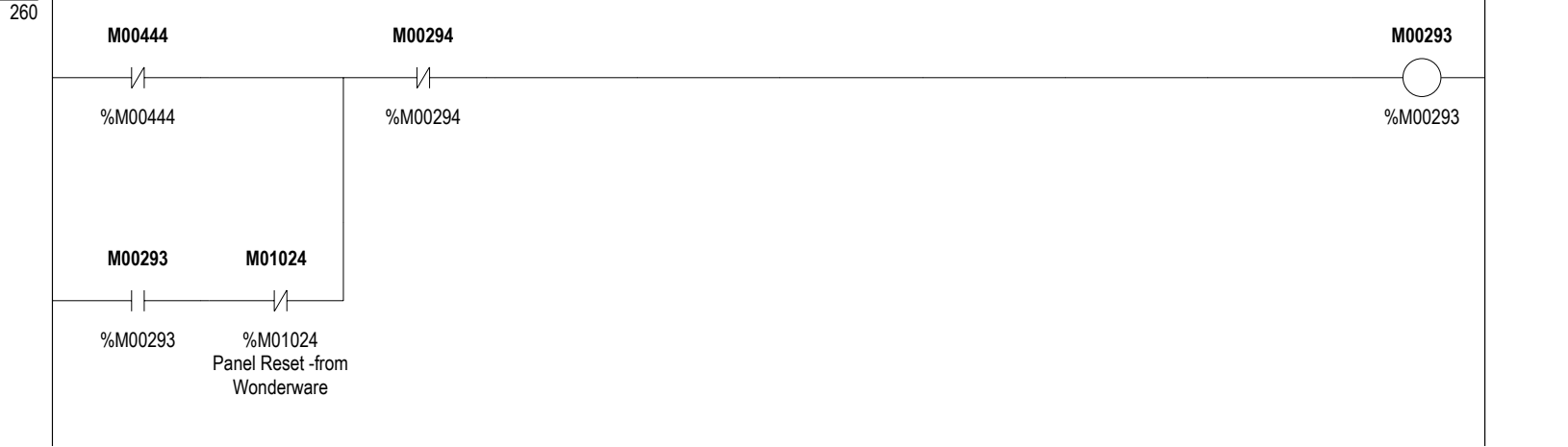
M00294 %M00294 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00257;

M00292 %M00292

LD Block,'FRSTOUT': NOCON 00259; COIL 00259;

LD Block,'SUMMARY': NCCON 00031;



M00444 %M00444 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00258;

M00294 %M00294 (Controlling Rung Reference)

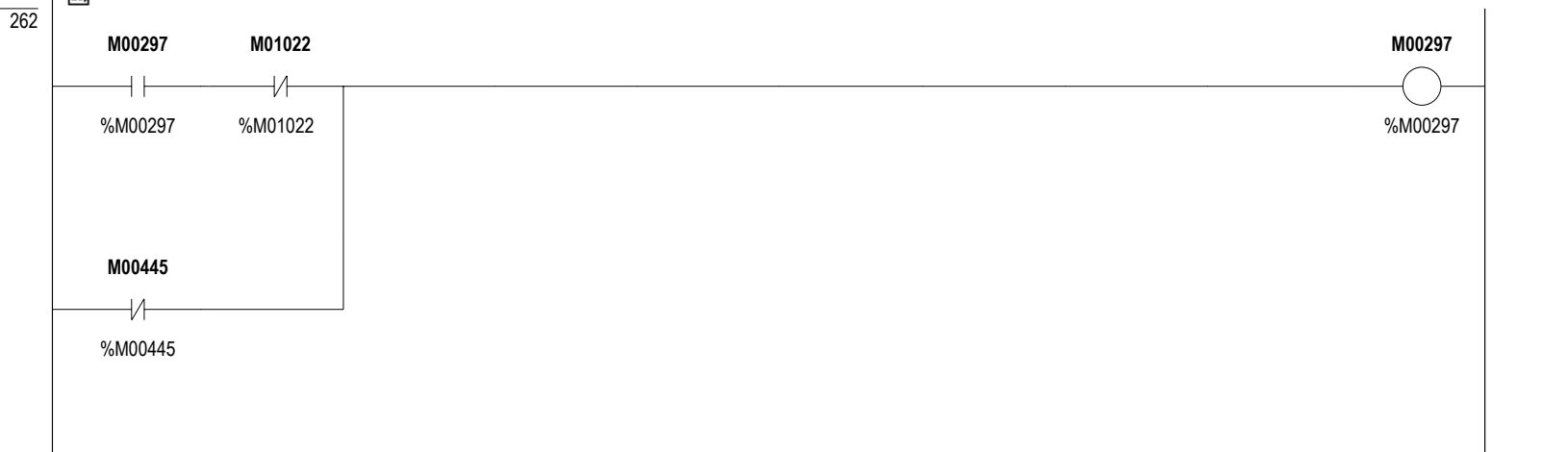
LD Block,'FRSTOUT': COIL 00257;

M00293 %M00293

LD Block,'FRSTOUT': NCCON 00259; NOCON 00260; COIL 00260;

LD Block,'SUMMARY': NOCON 00052;

261 COMMENT29



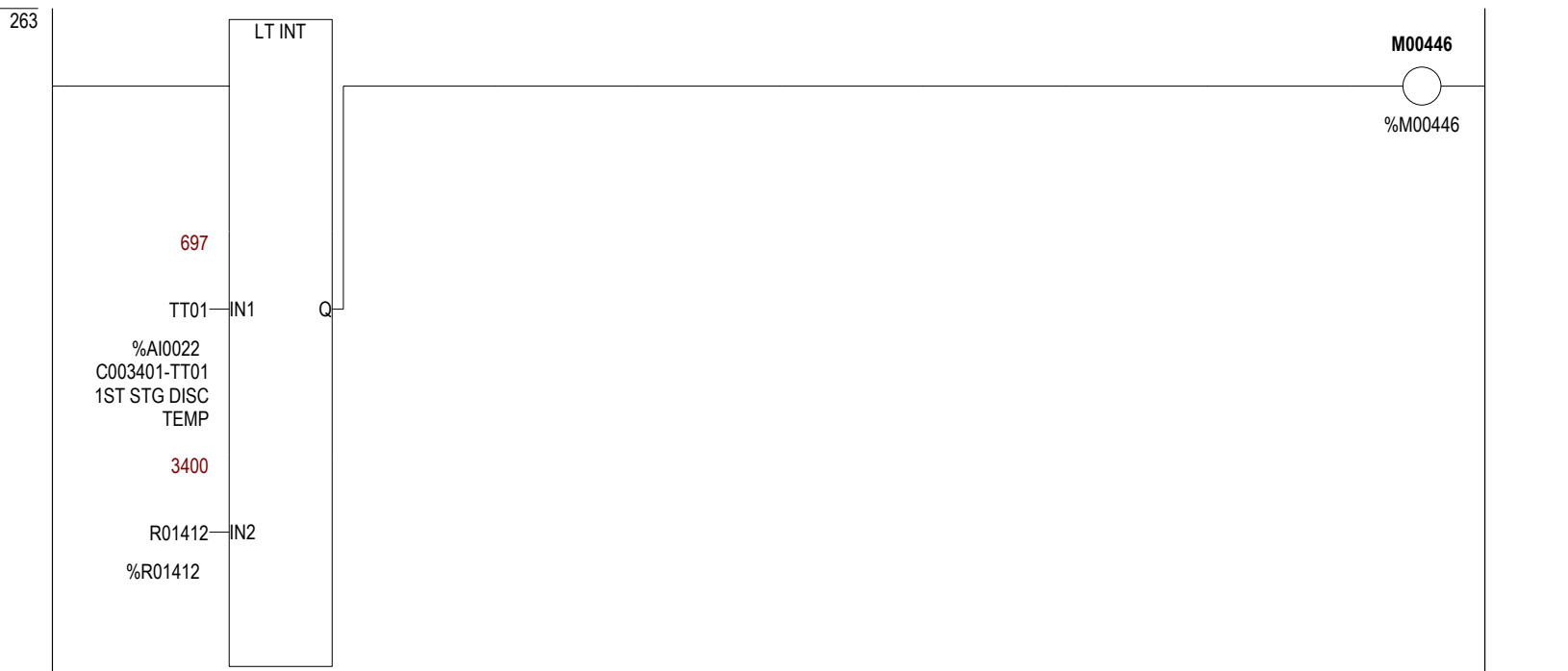
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00297 %M00297

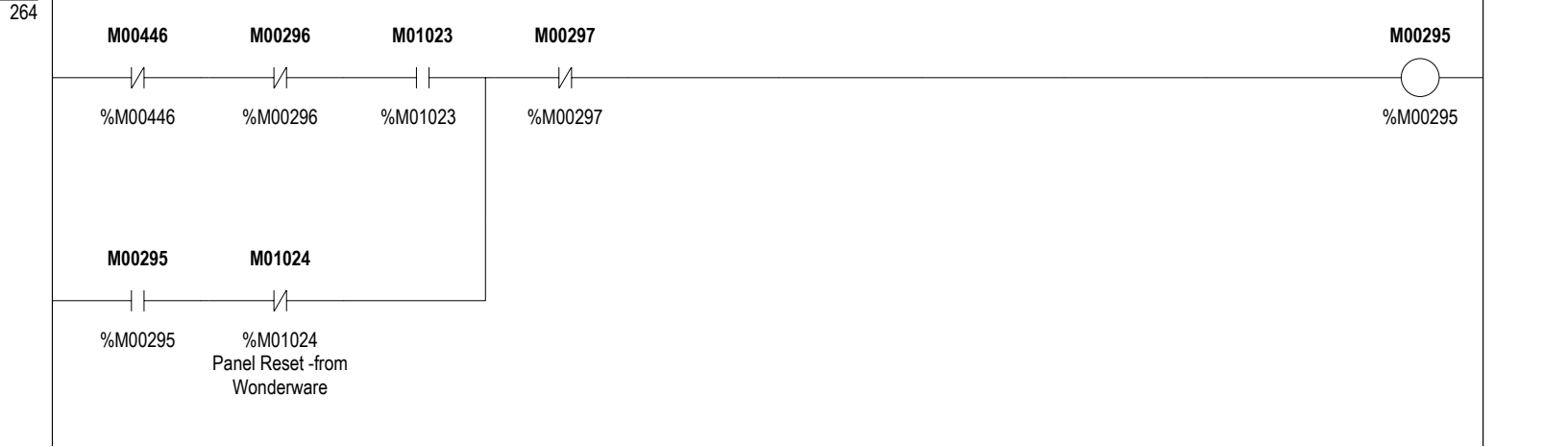
LD Block,'FRSTOUT': NCCON 00264, 00265; NOCON 00262; COIL 00262;

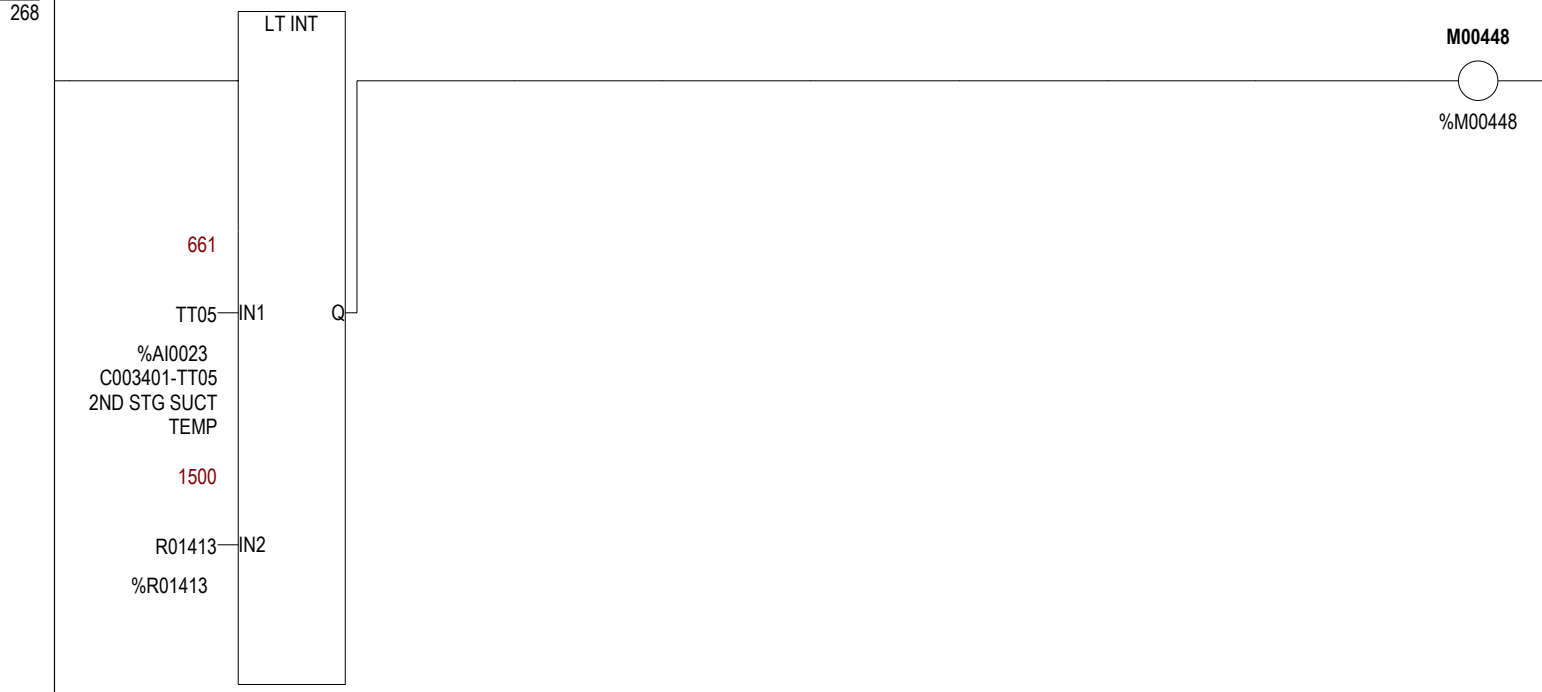
LD Block,'SUMMARY': NCCON 00011;



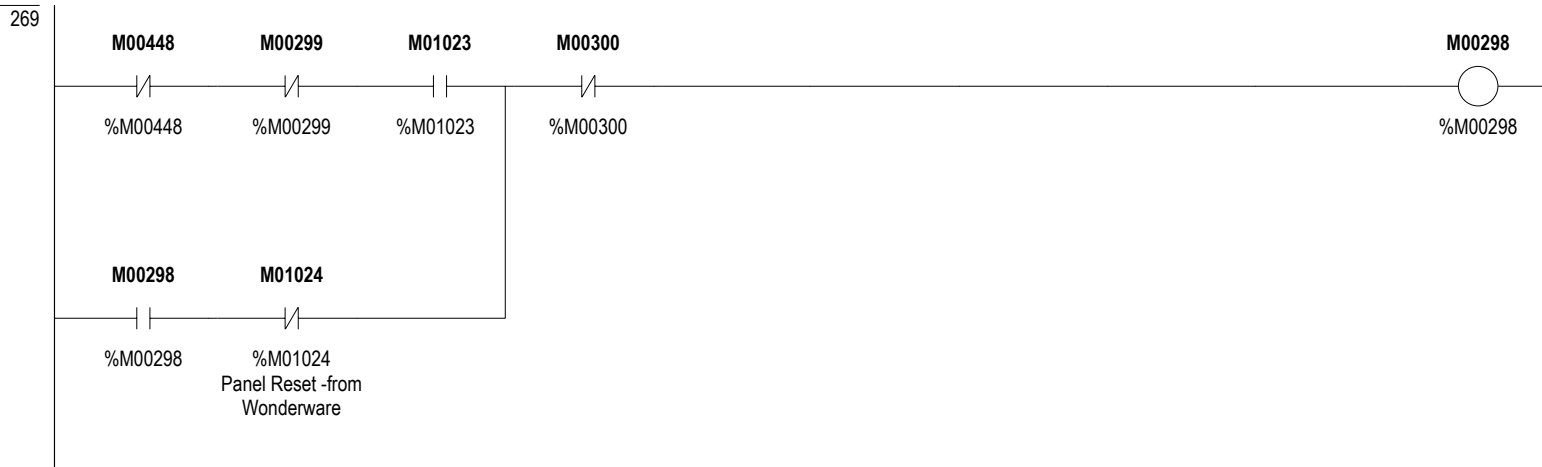
M00446 %M00446

LD Block,'FRSTOUT': NCCON 00264, 00265; COIL 00263;





M00448 %M00448
LD Block,'FRSTOUT': NCCON 00269, 00270; COIL 00268;



M00448 %M00448 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00268;

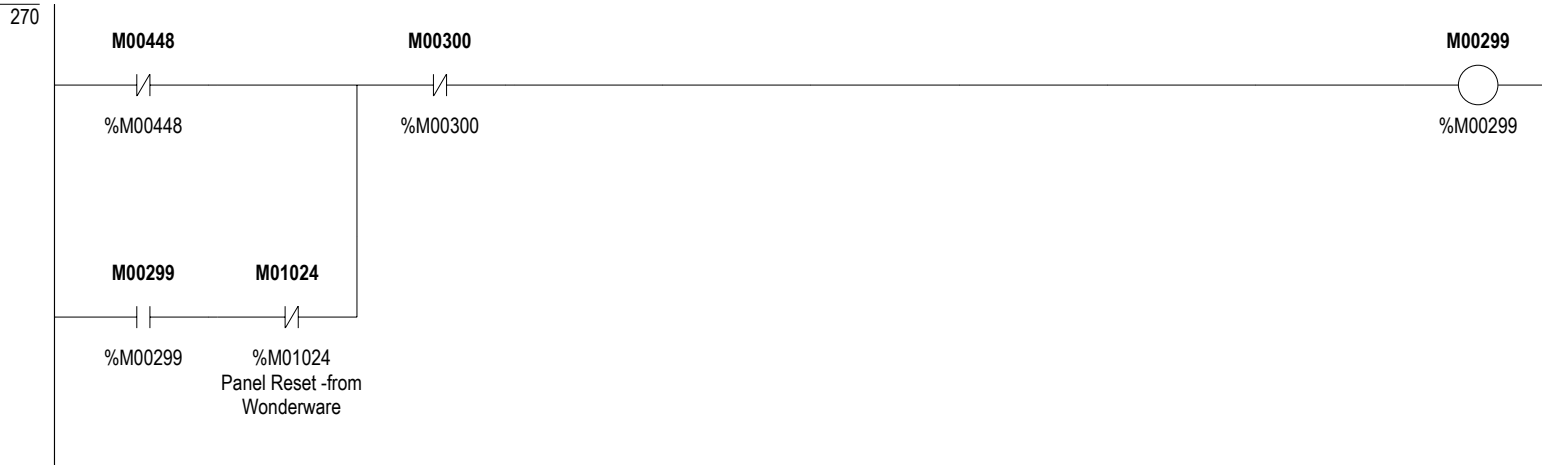
M00300 %M00300 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00267;

M00298 %M00298

LD Block,'FRSTOUT': NOCON 00269; COIL 00269;

LD Block,'SUMMARY': NCCON 00031;



M00448 %M00448 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00268;

M00300 %M00300 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00267;

M00299 %M00299

LD Block,'FRSTOUT': NCCON 00269; NOCON 00270; COIL 00270;

271
272

COMMENT31



M01022 %M01022 (Controlling Rung Reference)

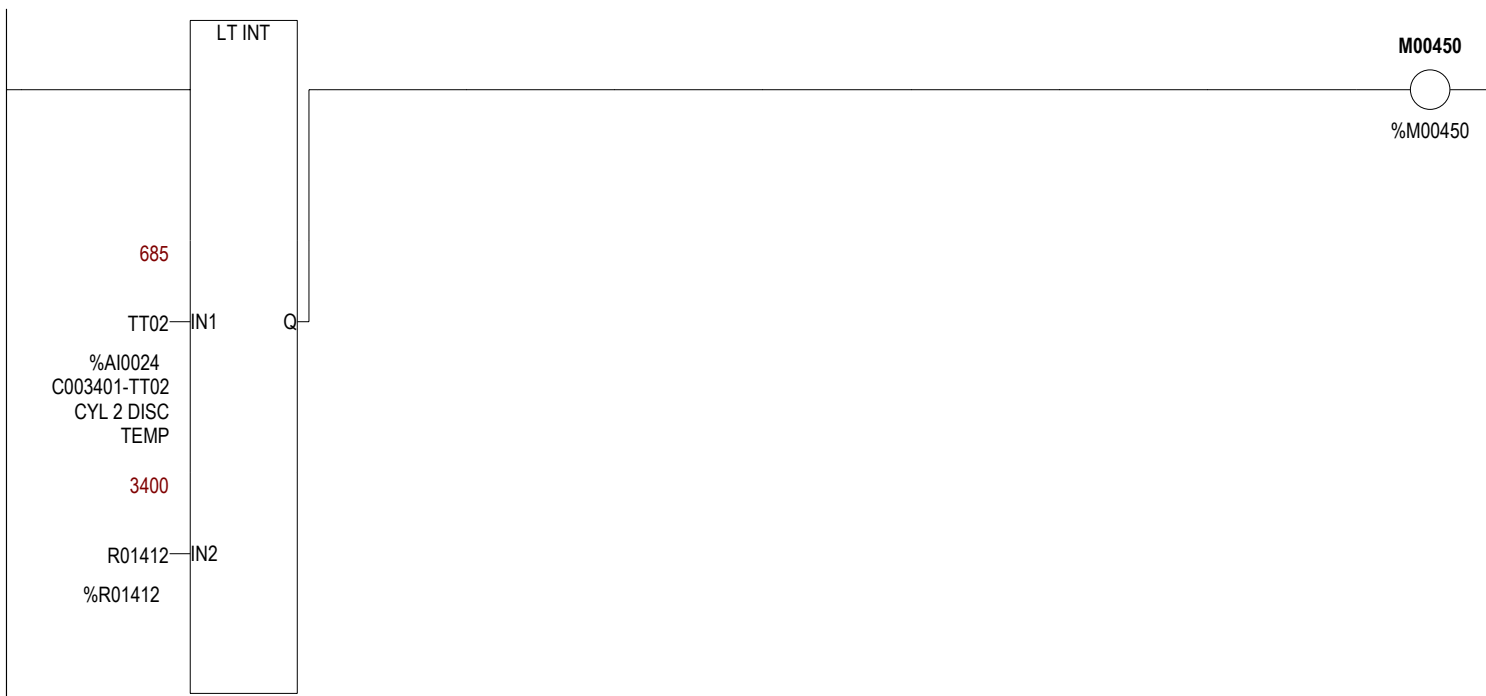
LD Block,'FRSTOUT': COIL 00003;

M00303 %M00303

LD Block,'FRSTOUT': NCCON 00274, 00275; NOCON 00272; COIL 00272;

LD Block,'SUMMARY': NCCON 00011;

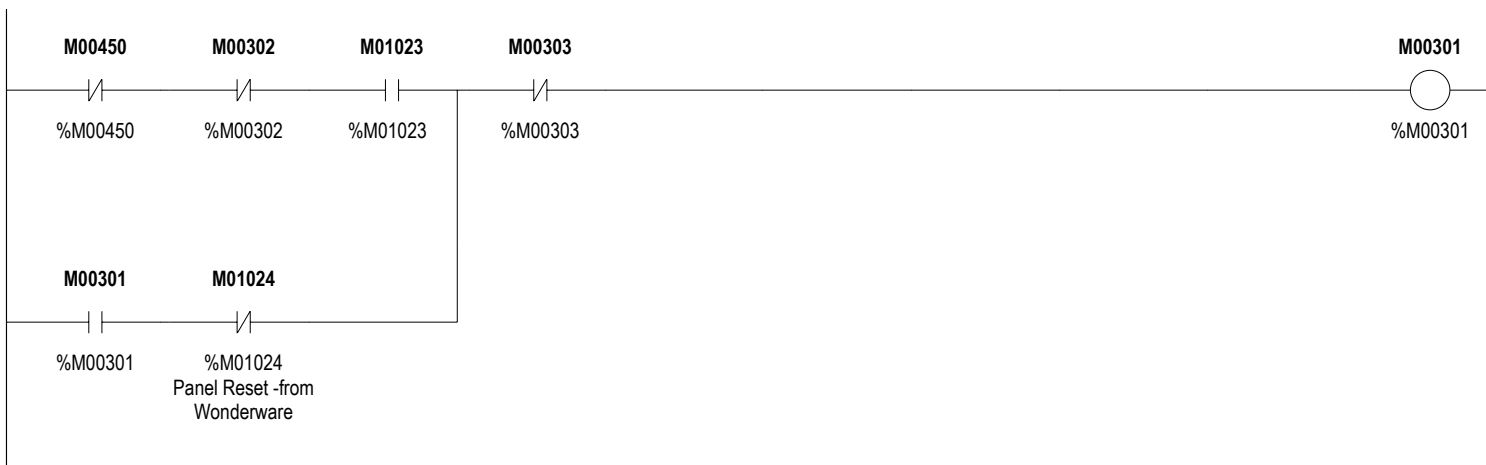
273



M00450 %M00450

LD Block,'FRSTOUT': NCCON 00274, 00275; COIL 00273;

274



M00450 %M00450 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00273;

M00303 %M00303 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00272;

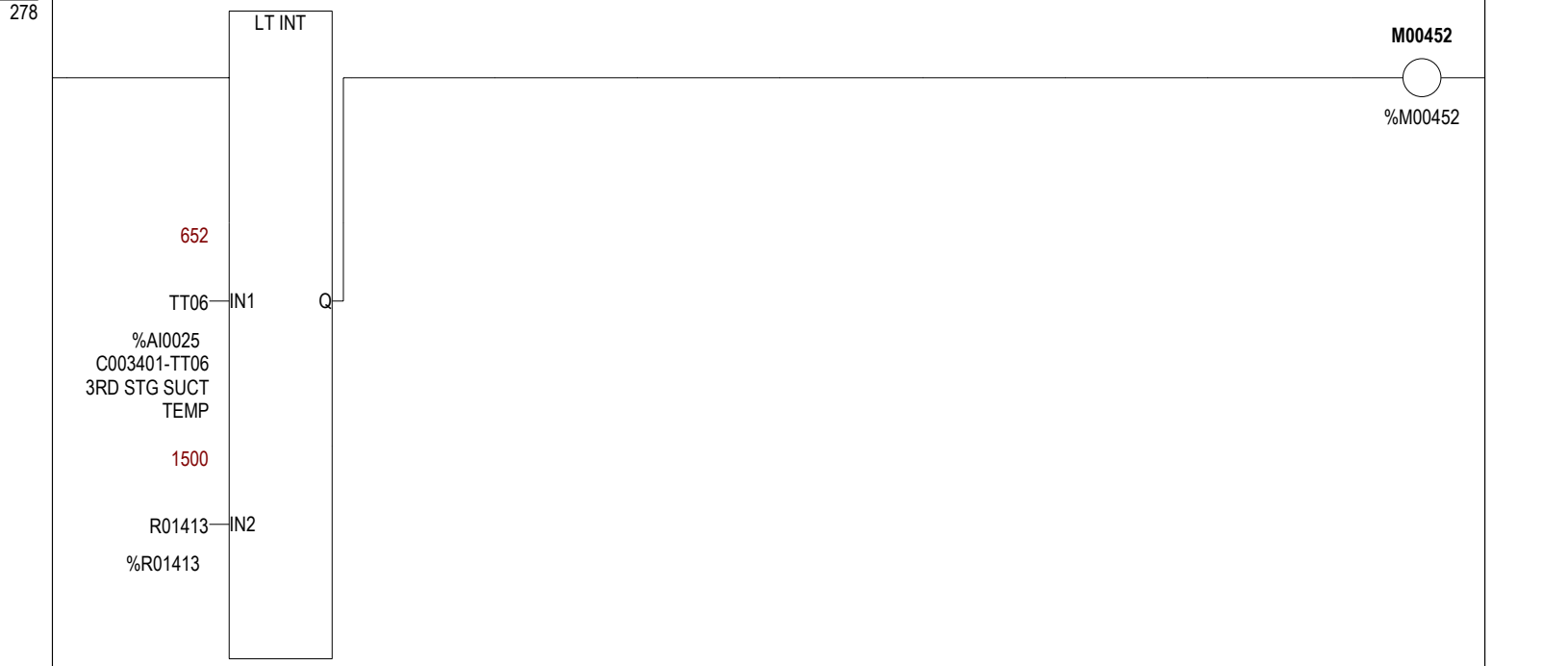
M00301 %M00301



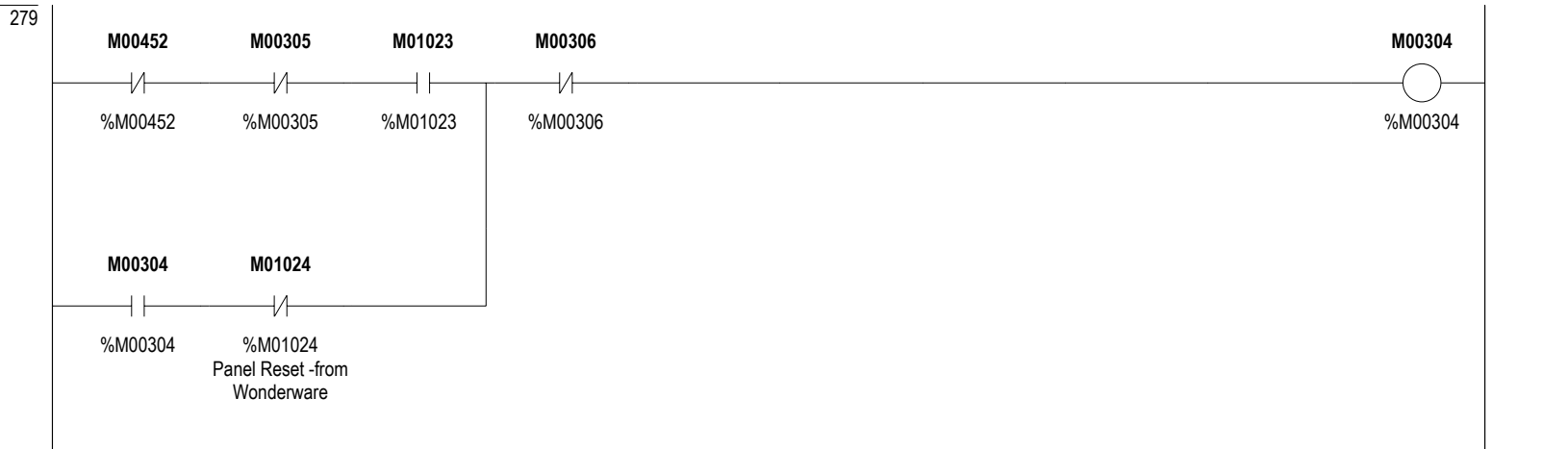
M00450 %M00450 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00273;
M00303 %M00303 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00272;
M00302 %M00302
LD Block,'FRSTOUT': NCCON 00274; NOCON 00275; COIL 00275;
LD Block,'SUMMARY': NOCON 00052;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00306 %M00306
LD Block,'FRSTOUT': NCCON 00279, 00280; NOCON 00277; COIL 00277;
LD Block,'SUMMARY': NCCON 00011;



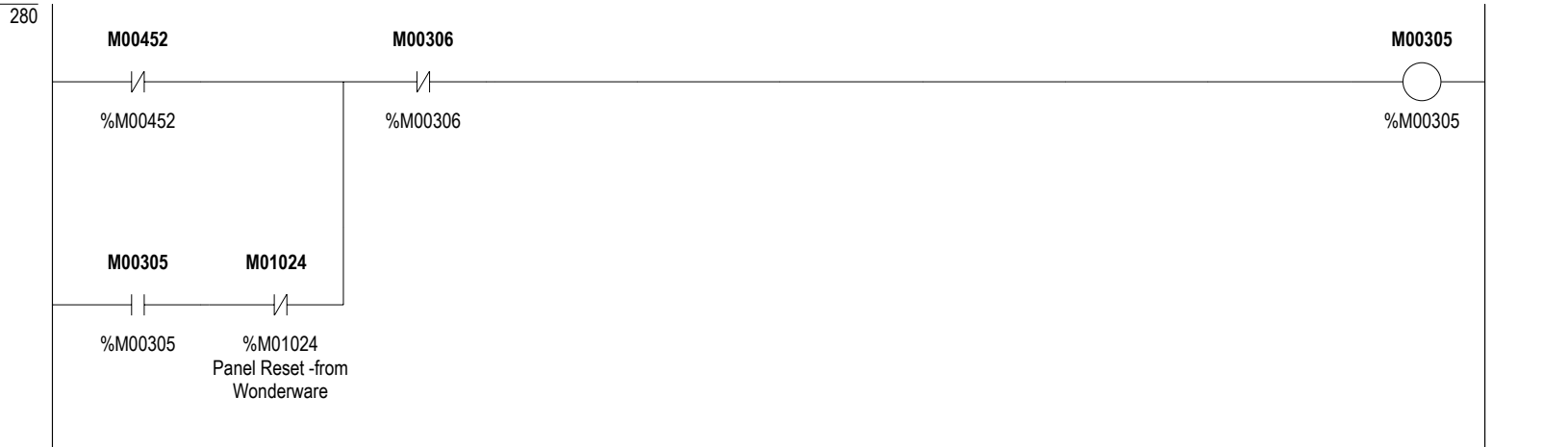
M00452 %M00452
LD Block,'FRSTOUT': NCCON 00279, 00280; COIL 00278;



M00452 %M00452 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00278;

M00306 %M00306 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00277;

M00304 %M00304
LD Block,'FRSTOUT': NOCON 00279; COIL 00279;
LD Block,'SUMMARY': NCCON 00031;



M00452 %M00452 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00278;

M00306 %M00306 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00277;

M00305 %M00305
LD Block,'FRSTOUT': NCCON 00279; NOCON 00280; COIL 00280;

281
282

COMMENT257

M00309

M01022

M00309

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%M00309

||
%M01022

○
%M00309

M00453

||
%M00453

M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00309 %M00309

LD Block,'FRSTOUT': NCCON 00284, 00285; NOCON 00282; COIL 00282;

LD Block,'SUMMARY': NCCON 00011;

283

LT INT

M00454

○
%M00454

660

TT03-IN1

Q

%AI0026
C003401-TT03
3RD STG DISC
TEMP

3400

R01412-IN2

%R01412

M00454 %M00454

LD Block,'FRSTOUT': NCCON 00284, 00285; COIL 00283;

284

M00454

M00308

M01023

M00309

M00307

||
%M00454

||
%M00308

||
%M01023

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%M00309

○
%M00307

M00307

M01024

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%M00307

||
%M01024
Panel Reset -from
Wonderware

M00454 %M00454 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00283;

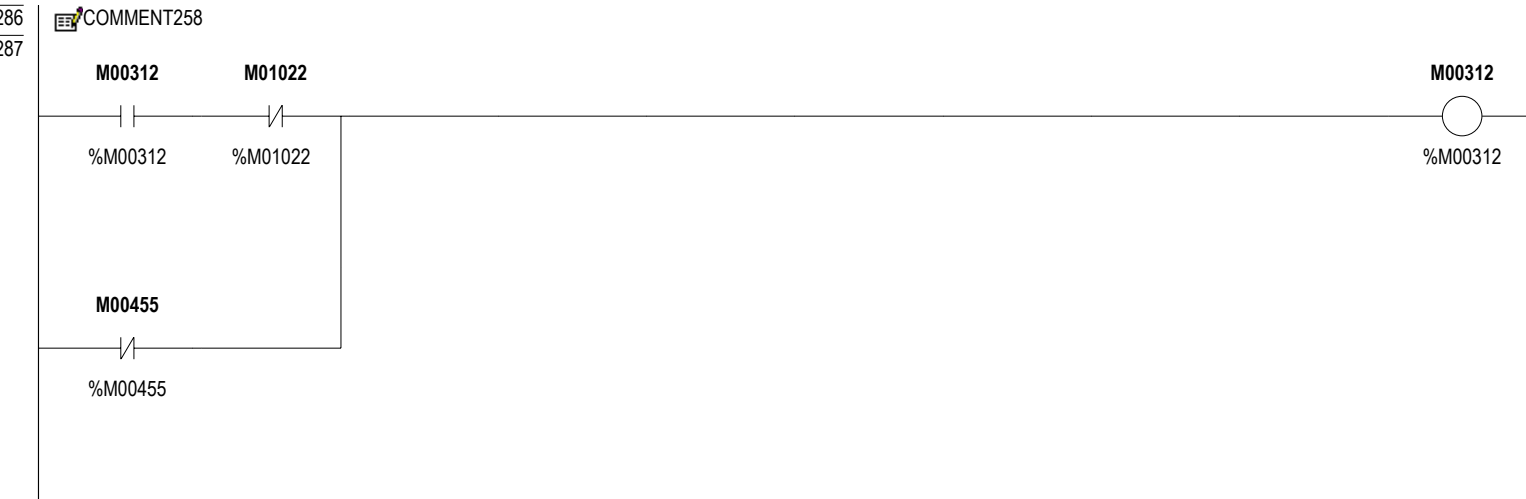
M00309 %M00309 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00282;

M00307 %M00307



M00454 %M00454 (Controlling Rung Reference)
LD Block, 'FRSTOUT': COIL 00283;
M00309 %M00309 (Controlling Rung Reference)
LD Block, 'FRSTOUT': COIL 00282;
M00308 %M00308
LD Block, 'FRSTOUT': NCCON 00284; NOCON 00285; COIL 00285;
LD Block, 'SUMMARY': NOCON 00052;



M01022 %M01022 (Controlling Rung Reference)
LD Block, 'FRSTOUT': COIL 00003;
M00312 %M00312
LD Block, 'FRSTOUT': NCCON 00289, 00290; NOCON 00287; COIL 00287;
LD Block, 'SUMMARY': NCCON 00012;

291

292

COMMENT259

M00315

M01022

M00315

||
%M00315

||
%M01022

%M00315

M00457

%M00457

M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00315 %M00315

LD Block,'FRSTOUT': NCCON 00294, 00295; NOCON 00292; COIL 00292;

LD Block,'SUMMARY': NCCON 00012;

293

LT INT

M00458

%M00458

669

TT38—IN1

Q

%AI0029
C003401-TT38
TURBO1 TEMP

9000

R01411—IN2

%R01411

M00458 %M00458

LD Block,'FRSTOUT': NCCON 00294, 00295; COIL 00293;

294

M00458

M00314

M01023

M00315

M00313

||
%M00458

||
%M00314

||
%M01023

||
%M00315

%M00313

M00313

M01024

||
%M00313

||
%M01024
Panel Reset -from
Wonderware

M00458 %M00458 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00293;

M00315 %M00315 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00292;

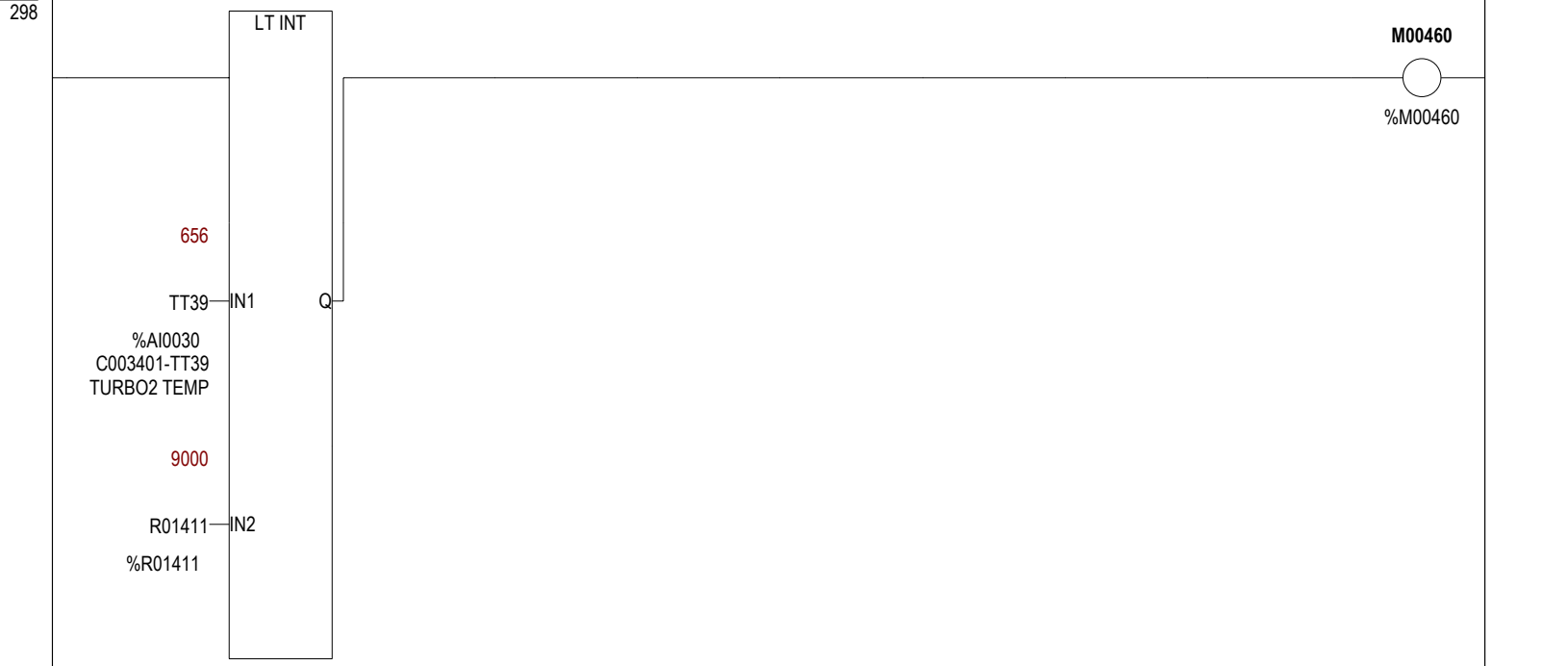
M00313 %M00313



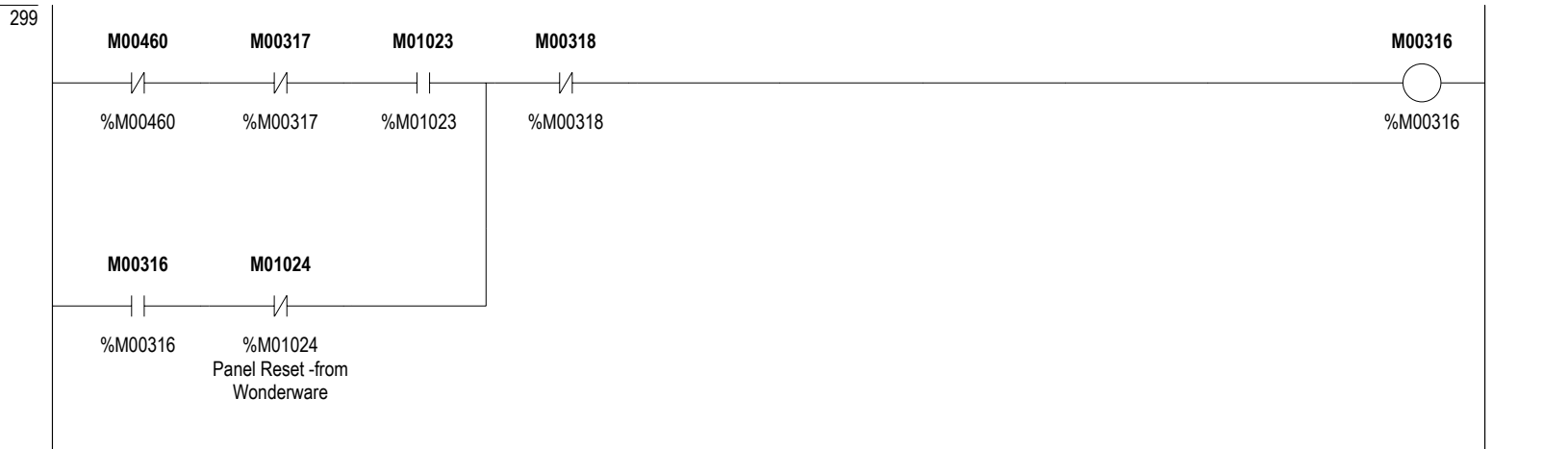
M00458 %M00458 (Controlling Rung Reference)
LD Block, 'FRSTOUT': COIL 00293;
M00315 %M00315 (Controlling Rung Reference)
LD Block, 'FRSTOUT': COIL 00292;
M00314 %M00314
LD Block, 'FRSTOUT': NCCON 00294; NOCON 00295; COIL 00295;
LD Block, 'SUMMARY': NOCON 00053;



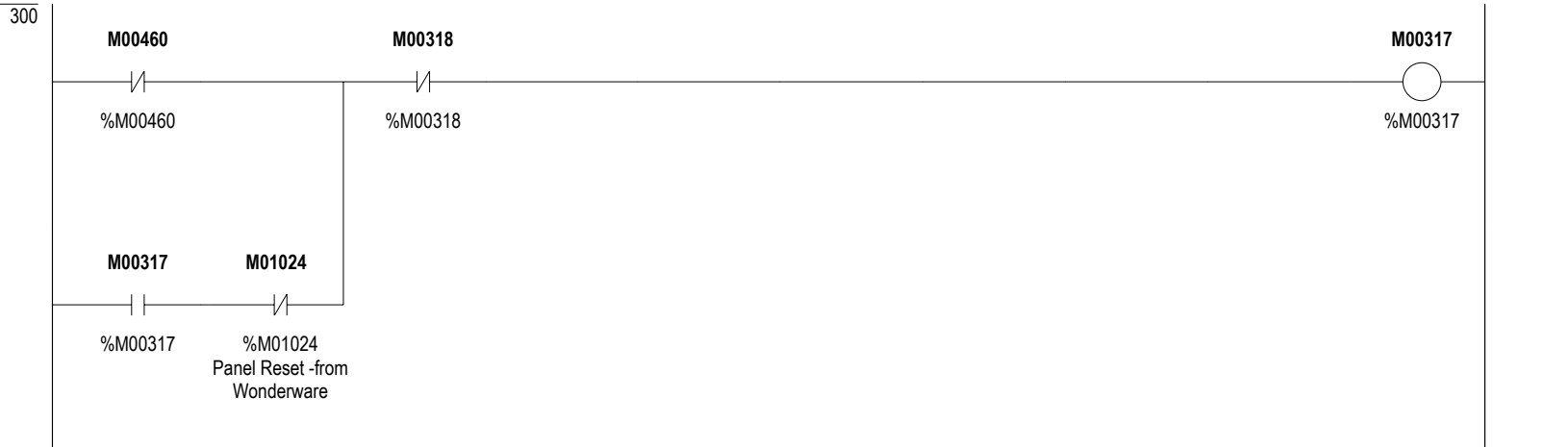
M01022 %M01022 (Controlling Rung Reference)
LD Block, 'FRSTOUT': COIL 00003;
M00318 %M00318
LD Block, 'FRSTOUT': NCCON 00299, 00300; NOCON 00297; COIL 00297;
LD Block, 'SUMMARY': NCCON 00012;



M00460 %M00460
LD Block,'FRSTOUT': NCCON 00299, 00300; COIL 00298;



M00460 %M00460 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00298;
M00318 %M00318 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00297;
M00316 %M00316
LD Block,'FRSTOUT': NOCON 00299; COIL 00299;
LD Block,'SUMMARY': NCCON 00032;



M00460 %M00460 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00298;
M00318 %M00318 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00297;
M00317 %M00317
LD Block,'FRSTOUT': NCCON 00299; NOCON 00300; COIL 00300;

301
302

COMMENT261

M00321

M01022

M00321

|| %M00321
|/| %M01022

○
%M00321

M00461

|/|
%M00461

M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00321 %M00321

LD Block,'FRSTOUT': NCCON 00304, 00305; NOCON 00302; COIL 00302;

LD Block,'SUMMARY': NCCON 00012;

303

LT INT

M00462

657

TT41-IN1

Q

%AI0031
C003401-TT41
H2O INTO COOL
TEMP

2100

R01415-IN2

%R01415

○
%M00462

M00462 %M00462

LD Block,'FRSTOUT': NCCON 00304, 00305; COIL 00303;

304

M00462

M00320

M01023

M00321

M00319

|/| %M00462
|/| %M00320
|| %M01023
|/| %M00321

○
%M00319

M00319

M01024

|| %M00319
|/| %M01024
Panel Reset -from
Wonderware

M00462 %M00462 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00303;

M00321 %M00321 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00302;

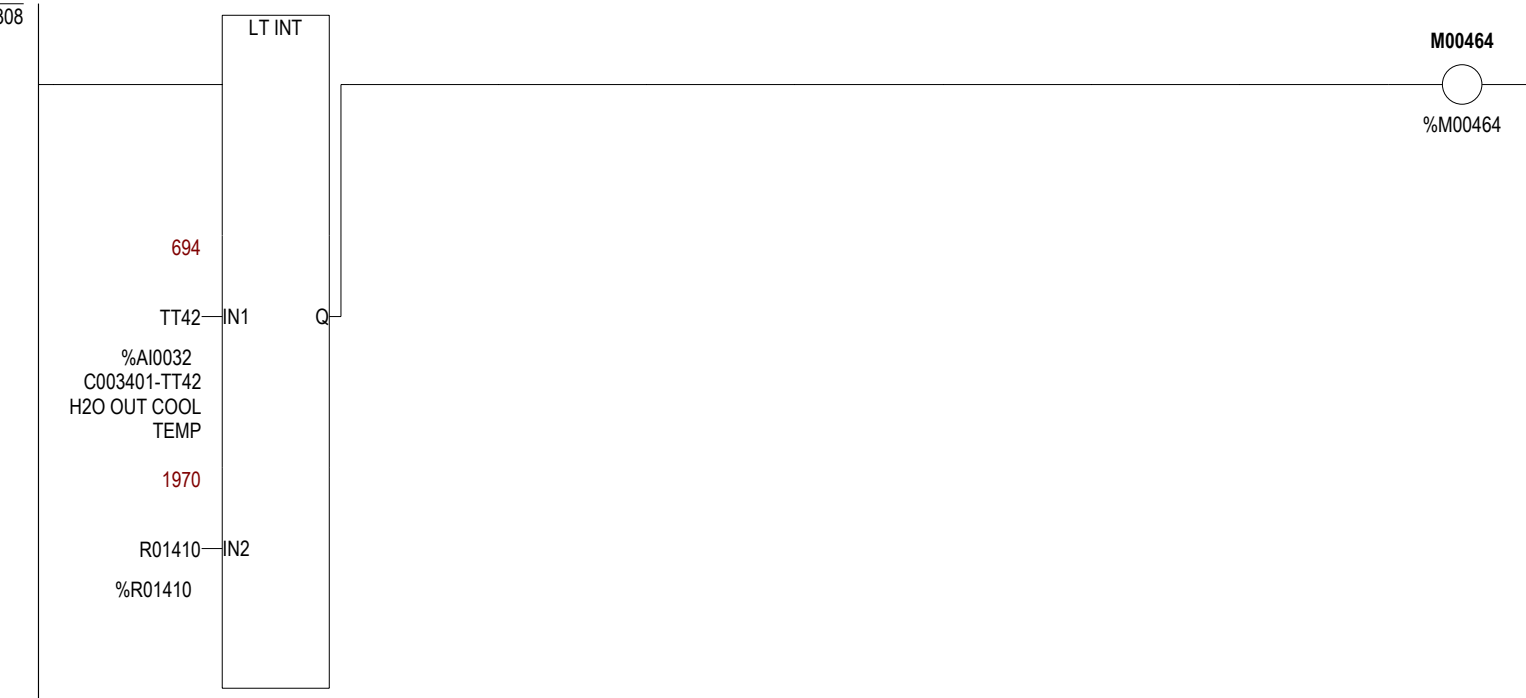
M00319 %M00319



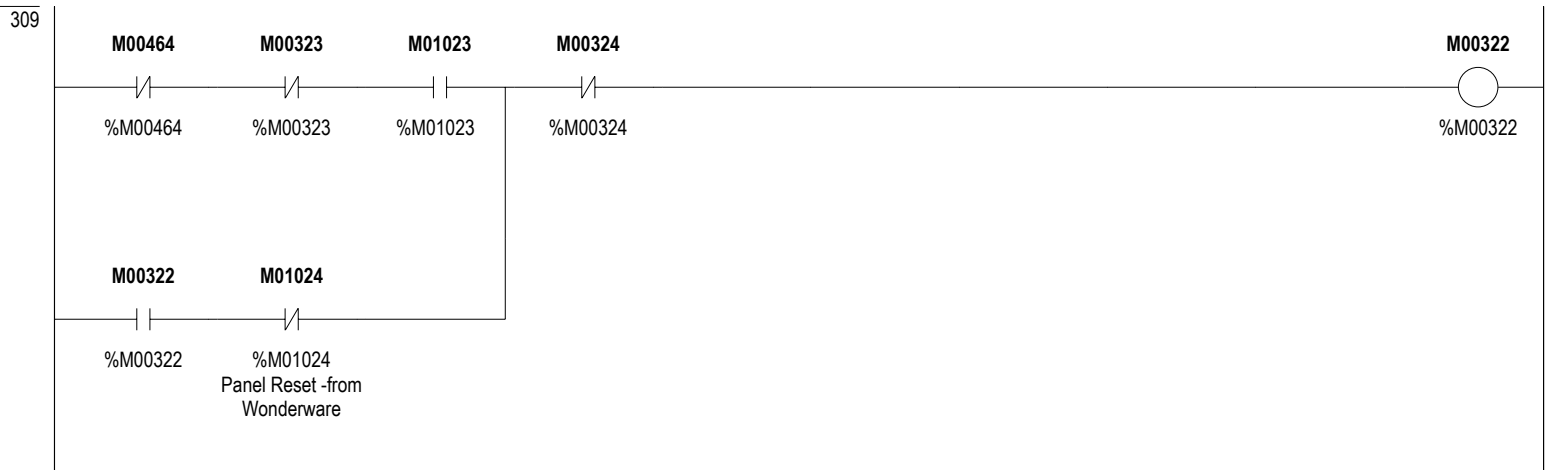
M00462 %M00462 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00303;
M00321 %M00321 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00302;
M00320 %M00320
LD Block,'FRSTOUT': NCCON 00304; NOCON 00305; COIL 00305;
LD Block,'SUMMARY': NOCON 00053;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00324 %M00324
LD Block,'FRSTOUT': NCCON 00309, 00310; NOCON 00307; COIL 00307;



M00464 %M00464
LD Block,'FRSTOUT': NCCON 00309, 00310; COIL 00308;



M00464 %M00464 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00308;

M00324 %M00324 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00307;

M00322 %M00322

LD Block,'FRSTOUT': NOCON 00309; COIL 00309;



M00464 %M00464 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00308;

M00324 %M00324 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00307;

M00323 %M00323

LD Block,'FRSTOUT': NCCON 00309; NOCON 00310; COIL 00310;



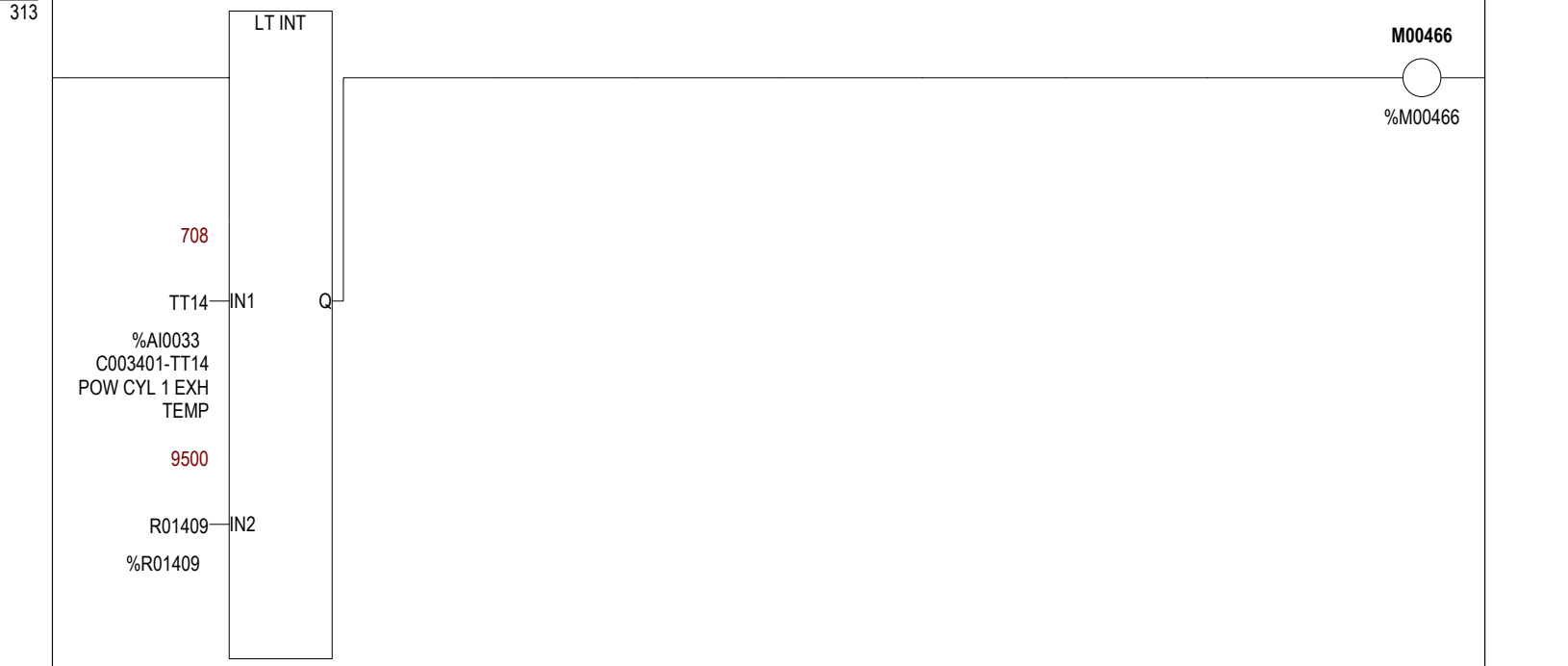
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

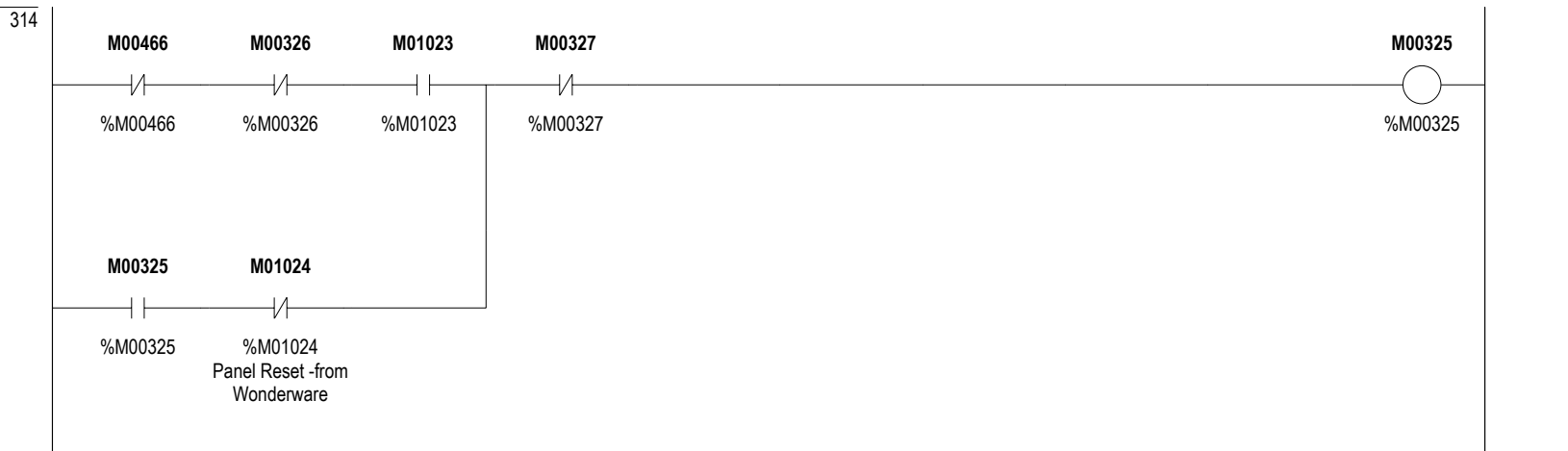
M00327 %M00327

LD Block,'FRSTOUT': NCCON 00314, 00315; NOCON 00312; COIL 00312;

LD Block,'SUMMARY': NCCON 00012;



M00466 %M00466
LD Block,'FRSTOUT': NCCON 00314, 00315; COIL 00313;



M00466 %M00466 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00313;

M00327 %M00327 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00312;

M00325 %M00325
LD Block,'FRSTOUT': NOCON 00314; COIL 00314;
LD Block,'SUMMARY': NCCON 00032;



M00466 %M00466 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00313;

M00327 %M00327 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00312;

M00326 %M00326
LD Block,'FRSTOUT': NCCON 00314; NOCON 00315; COIL 00315;

316
317

COMMENT264

M00330

M01022

M00330

||
%M00330

||
%M01022

○
%M00330

M00467

||
%M00467

M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00330 %M00330

LD Block,'FRSTOUT': NCCON 00319, 00320; NOCON 00317; COIL 00317;

LD Block,'SUMMARY': NCCON 00013;

318

LT INT

M00468

○
%M00468

697

TT15-IN1

Q

%AI0034
C003401-TT15
POW CYL 2 EXH
TEMP

9500

R01409-IN2

%R01409

M00468 %M00468

LD Block,'FRSTOUT': NCCON 00319, 00320; COIL 00318;

319

M00468

M00329

M01023

M00330

M00328

||
%M00468

||
%M00329

||
%M01023

||
%M00330

○
%M00328

M00328

M01024

||
%M00328

||
%M01024
Panel Reset -from
Wonderware

M00468 %M00468 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00318;

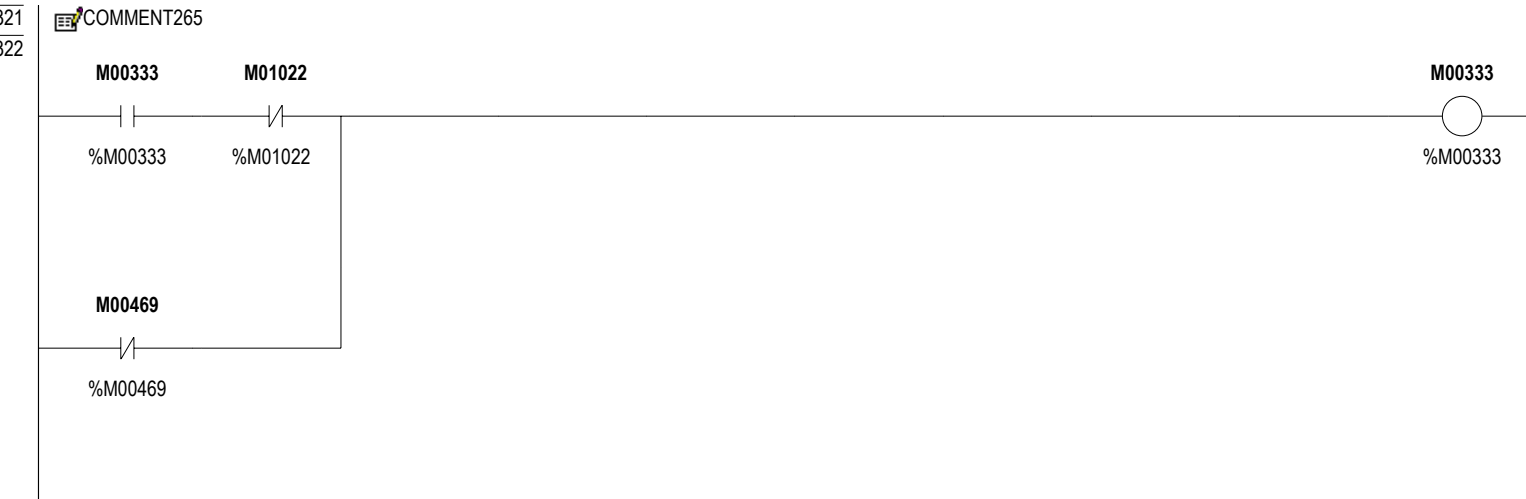
M00330 %M00330 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00317;

M00328 %M00328



M00468 %M00468 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00318;
M00330 %M00330 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00317;
M00329 %M00329
LD Block,'FRSTOUT': NCCON 00319; NOCON 00320; COIL 00320;
LD Block,'SUMMARY': NOCON 00054;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00333 %M00333
LD Block,'FRSTOUT': NCCON 00324, 00325; NOCON 00322; COIL 00322;
LD Block,'SUMMARY': NCCON 00013;

326
327

COMMENT266

M00336

M01022

M00336

||
%M00336

||
%M01022

○
%M00336

M00471

||
%M00471

M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00336 %M00336

LD Block,'FRSTOUT': NCCON 00329, 00330; NOCON 00327; COIL 00327;

LD Block,'SUMMARY': NCCON 00013;

328

LT INT

M00472

○
%M00472

673

TT17—IN1

%AI0036
C003401-TT17
POW CYL 4 EXH
TEMP

9500

R01409—IN2

%R01409

M00472 %M00472

LD Block,'FRSTOUT': NCCON 00329, 00330; COIL 00328;

329

M00472

M00335

M01023

M00336

M00334

||
%M00472

||
%M00335

||
%M01023

||
%M00336

○
%M00334

M00334

M01024

||
%M00334

||
%M01024
Panel Reset -from
Wonderware

M00472 %M00472 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00328;

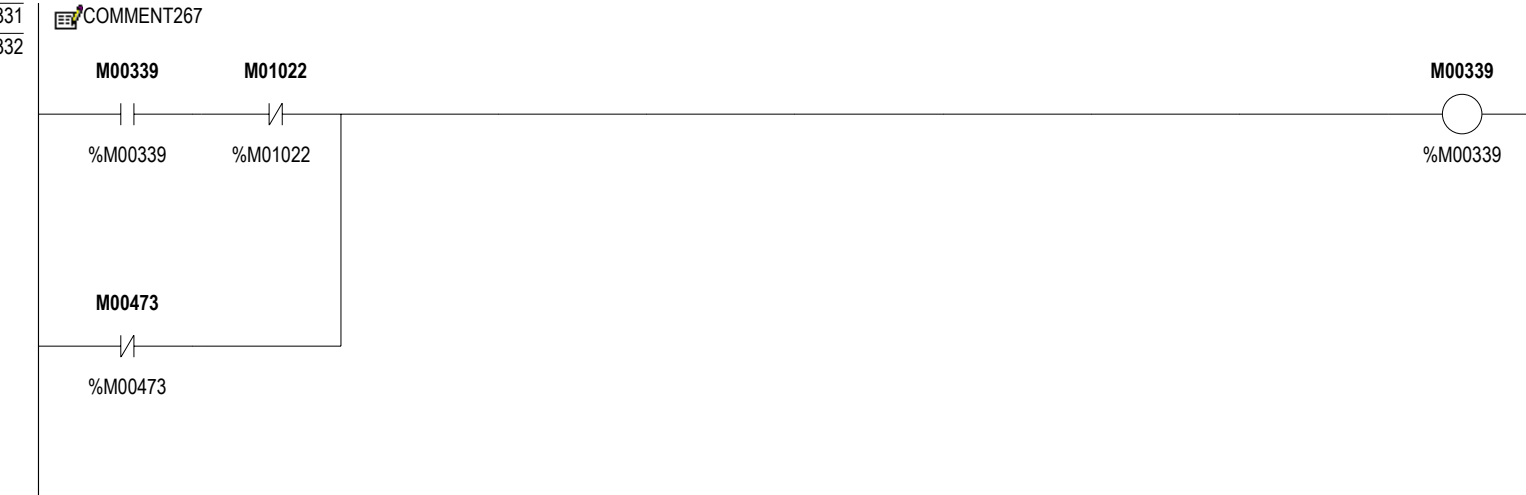
M00336 %M00336 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00327;

M00334 %M00334



M00472 %M00472 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00328;
M00336 %M00336 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00327;
M00335 %M00335
LD Block,'FRSTOUT': NCCON 00329; NOCON 00330; COIL 00330;
LD Block,'SUMMARY': NOCON 00054;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00339 %M00339
LD Block,'FRSTOUT': NCCON 00334, 00335; NOCON 00332; COIL 00332;
LD Block,'SUMMARY': NCCON 00013;

336
337

COMMENT268

M00342

M01022

M00342

||
%M00342

||
%M01022

○
%M00342

M00475

||
%M00475

M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00342 %M00342

LD Block,'FRSTOUT': NCCON 00339, 00340; NOCON 00337; COIL 00337;

LD Block,'SUMMARY': NCCON 00013;

338

LT INT

M00476

○
%M00476

666

TT19-IN1

Q

%AI0038
C003401-TT19
POW CYL 6 EXH
TEMP

9500

R01409-IN2

%R01409

M00476 %M00476

LD Block,'FRSTOUT': NCCON 00339, 00340; COIL 00338;

339

M00476

M00341

M01023

M00342

M00340

||
%M00476

||
%M00341

||
%M01023

||
%M00342

○
%M00340

M00340

M01024

||
%M00340

||
%M01024
Panel Reset -from
Wonderware

M00476 %M00476 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00338;

M00342 %M00342 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00337;

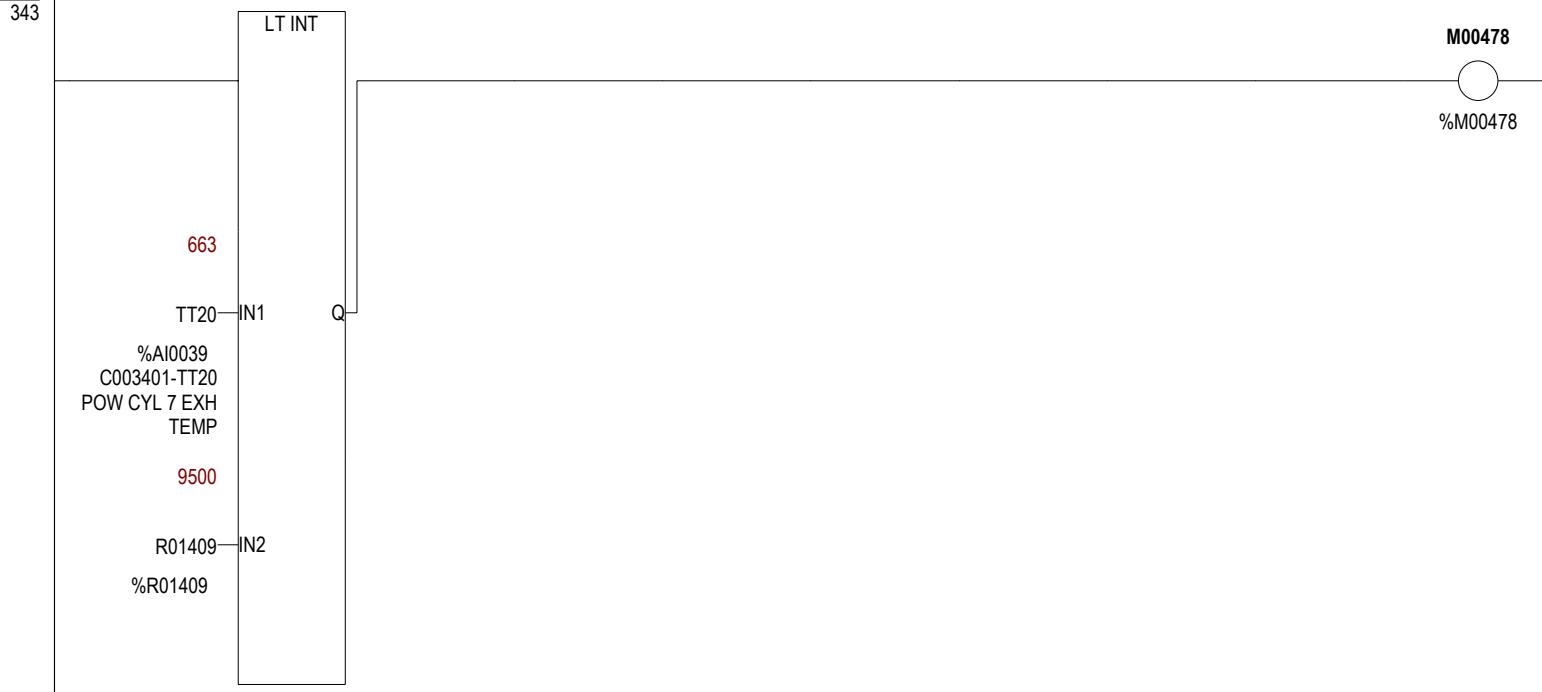
M00340 %M00340



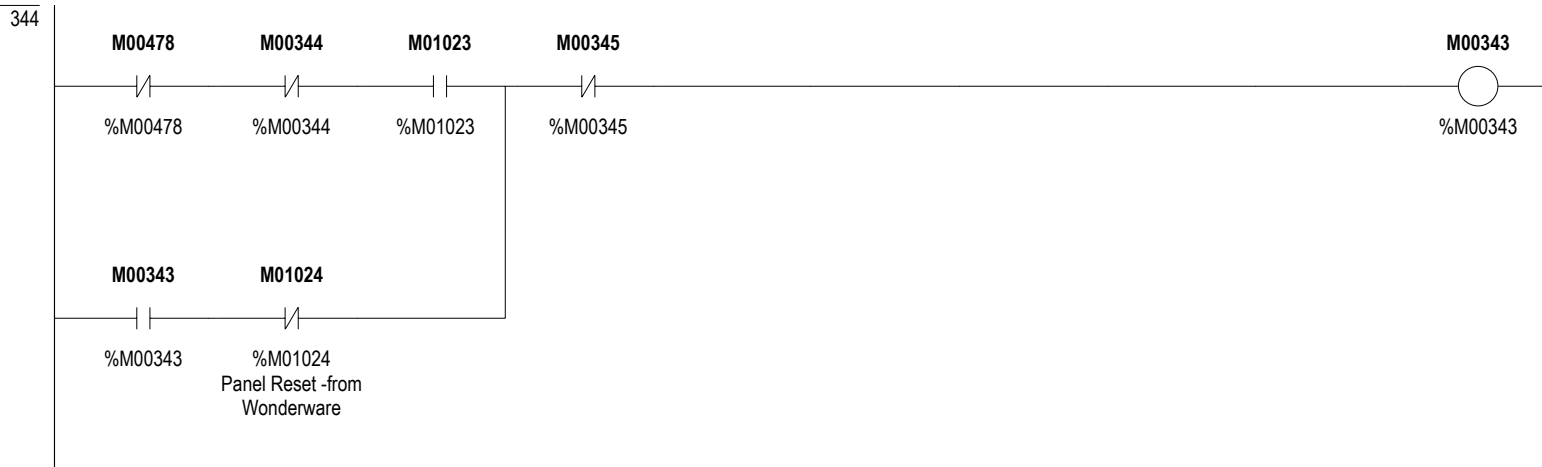
M00476 %M00476 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00338;
M00342 %M00342 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00337;
M00341 %M00341
LD Block,'FRSTOUT': NCCON 00339; NOCON 00340; COIL 00340;
LD Block,'SUMMARY': NOCON 00054;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00345 %M00345
LD Block,'FRSTOUT': NCCON 00344, 00345; NOCON 00342; COIL 00342;
LD Block,'SUMMARY': NCCON 00013;



M00478 %M00478
LD Block,'FRSTOUT': NCCON 00344, 00345; COIL 00343;



M00478 %M00478 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00343;

M00345 %M00345 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00342;

M00343 %M00343

LD Block,'FRSTOUT': NOCON 00344; COIL 00344;

LD Block,'SUMMARY': NCCON 00033;



M00478 %M00478 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00343;

M00345 %M00345 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00342;

M00344 %M00344

LD Block,'FRSTOUT': NCCON 00344; NOCON 00345; COIL 00345;

346
347

COMMENT270

M00348

M01022

M00348

||
%M00348

||
%M01022

○
%M00348

M00479

||
%M00479

M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00348 %M00348

LD Block,'FRSTOUT': NCCON 00349, 00350; NOCON 00347; COIL 00347;

LD Block,'SUMMARY': NCCON 00014;

348

LT INT

M00480

665

TT21-IN1

Q

%AI0040
C003401-TT21
POW CYL 8 EXH
TEMP

9500

R01409-IN2

%R01409

○
%M00480

M00480 %M00480

LD Block,'FRSTOUT': NCCON 00349, 00350; COIL 00348;

349

M00480

M00347

M01023

M00348

M00346

||
%M00480

||
%M00347

||
%M01023

||
%M00348

○
%M00346

M00346

M01024

||
%M00346

||
%M01024
Panel Reset -from
Wonderware

M00480 %M00480 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00348;

M00348 %M00348 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00347;

M00346 %M00346



M00480 %M00480 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00348;
M00348 %M00348 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00347;
M00347 %M00347
LD Block,'FRSTOUT': NCCON 00349; NOCON 00350; COIL 00350;
LD Block,'SUMMARY': NOCON 00055;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00351 %M00351
LD Block,'FRSTOUT': NCCON 00354, 00355; NOCON 00352; COIL 00352;
LD Block,'SUMMARY': NCCON 00014;

356
357

COMMENT272

M00354

M01022

M00354

||
%M00354

||
%M01022

○
%M00354

M00483

||
%M00483

M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00354 %M00354

LD Block,'FRSTOUT': NCCON 00359, 00360; NOCON 00357; COIL 00357;

LD Block,'SUMMARY': NCCON 00014;

358

LT INT

M00484

○
%M00484

659

TT23-IN1

Q

%AI0042
C003401-TT23
POW CYL 10 EXH
TEMP

9500

R01409-IN2

%R01409

M00484 %M00484

LD Block,'FRSTOUT': NCCON 00359, 00360; COIL 00358;

359

M00484

M00353

M01023

M00354

M00352

||
%M00484

||
%M00353

||
%M01023

||
%M00354

○
%M00352

M00352

M01024

||
%M00352

||
%M01024
Panel Reset -from
Wonderware

M00484 %M00484 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00358;

M00354 %M00354 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00357;

M00352 %M00352



M00354

%M00354

M00353

%M00353

M00353

%M00353

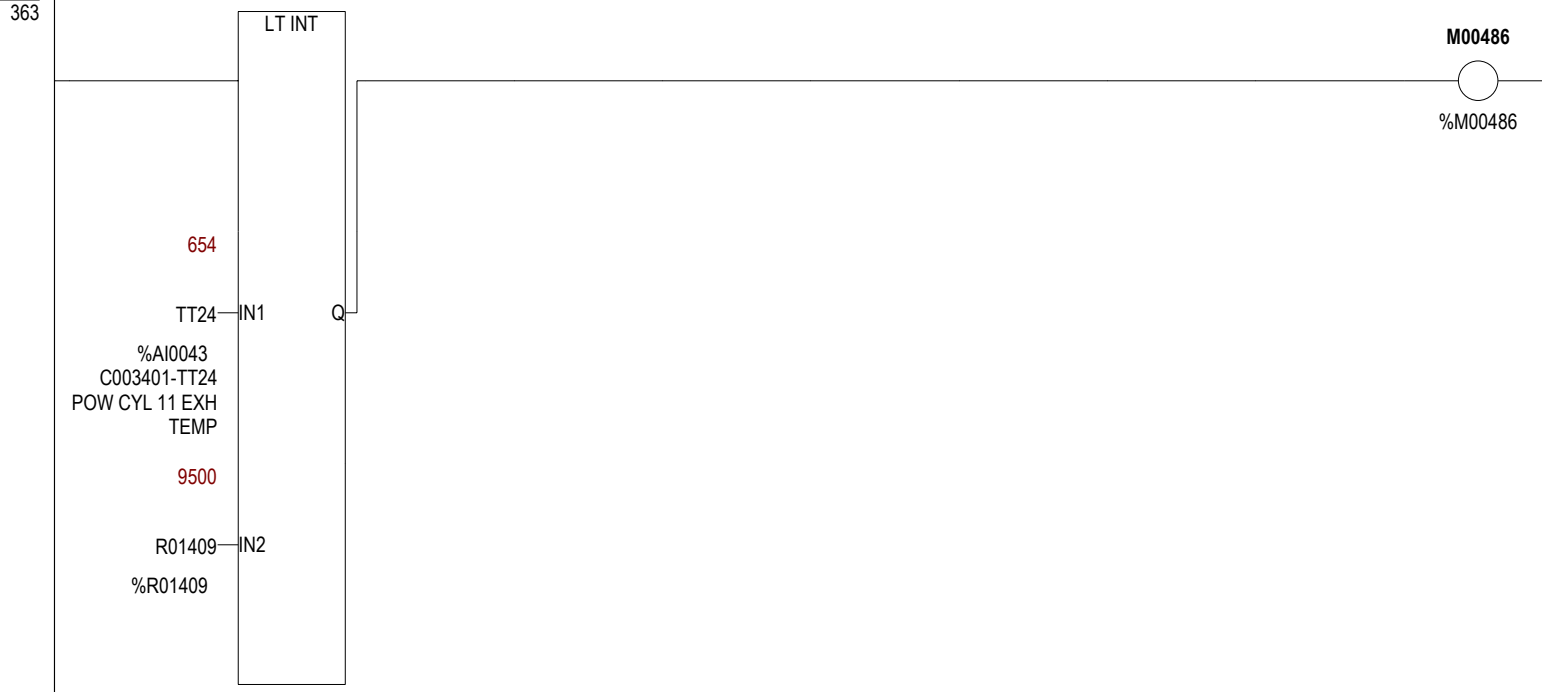
M00484 %M00484 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00358;
M00354 %M00354 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00357;
M00353 %M00353
LD Block,'FRSTOUT': NCCON 00359; NOCON 00360; COIL 00360;
LD Block,'SUMMARY': NOCON 00055;



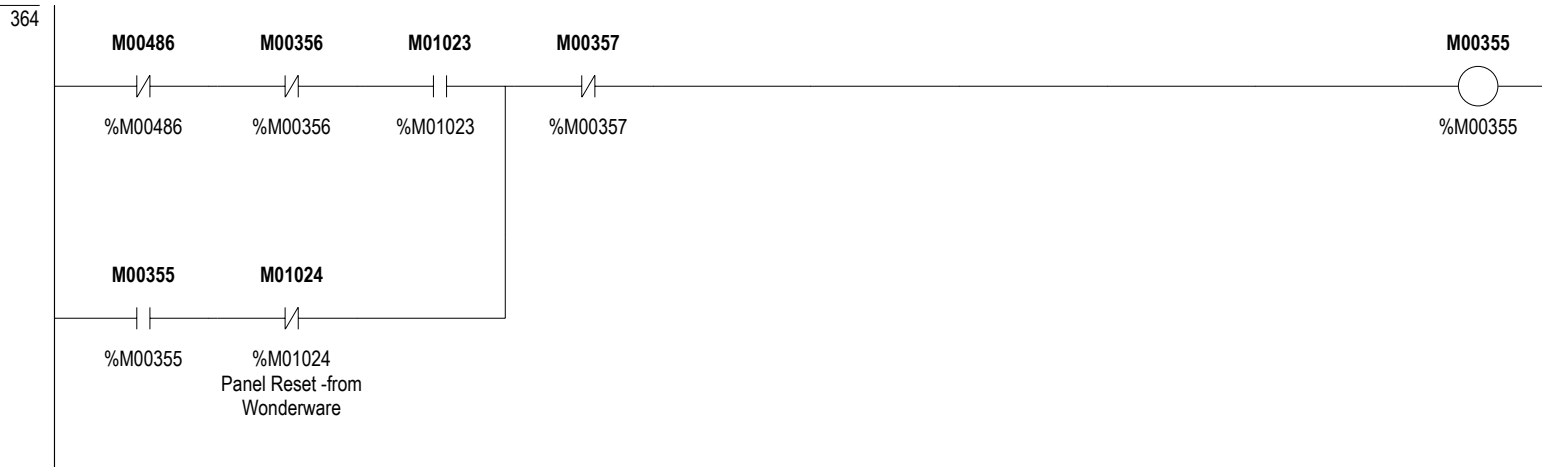
M00357

%M00357

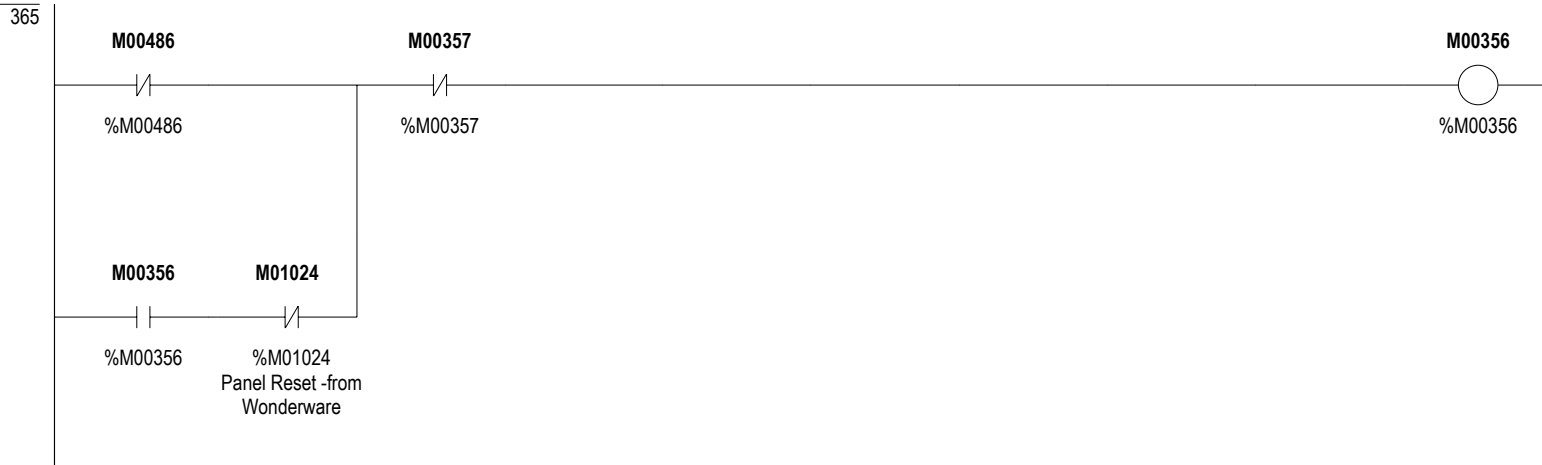
M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00357 %M00357
LD Block,'FRSTOUT': NCCON 00364, 00365; NOCON 00362; COIL 00362;
LD Block,'SUMMARY': NCCON 00014;



M00486 %M00486
LD Block,'FRSTOUT': NCCON 00364, 00365; COIL 00363;



M00486 %M00486 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00363;
M00357 %M00357 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00362;
M00355 %M00355
LD Block,'FRSTOUT': NOCON 00364; COIL 00364;
LD Block,'SUMMARY': NCCON 00034;



M00486 %M00486 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00363;
M00357 %M00357 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00362;
M00356 %M00356
LD Block,'FRSTOUT': NCCON 00364; NOCON 00365; COIL 00365;

366
367

COMMENT274

M00360

M01022

M00360

%M00360 %M01022

%M00360

M00487

%M00487

M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00360 %M00360

LD Block,'FRSTOUT': NCCON 00369, 00370; NOCON 00367; COIL 00367;

LD Block,'SUMMARY': NCCON 00014;

368

LT INT

M00488

%M00488

655

TT25-IN1

Q

%AI0044

C003401-TT25

POW CYL 12 EXH

TEMP

9500

R01409-IN2

%R01409

M00488 %M00488

LD Block,'FRSTOUT': NCCON 00369, 00370; COIL 00368;

369

M00488

M00359

M01023

M00360

M00358

%M00488 %M00359 %M01023 %M00360

%M00358

M00358

M01024

%M00358 %M01024
Panel Reset -from
Wonderware

M00488 %M00488 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00368;

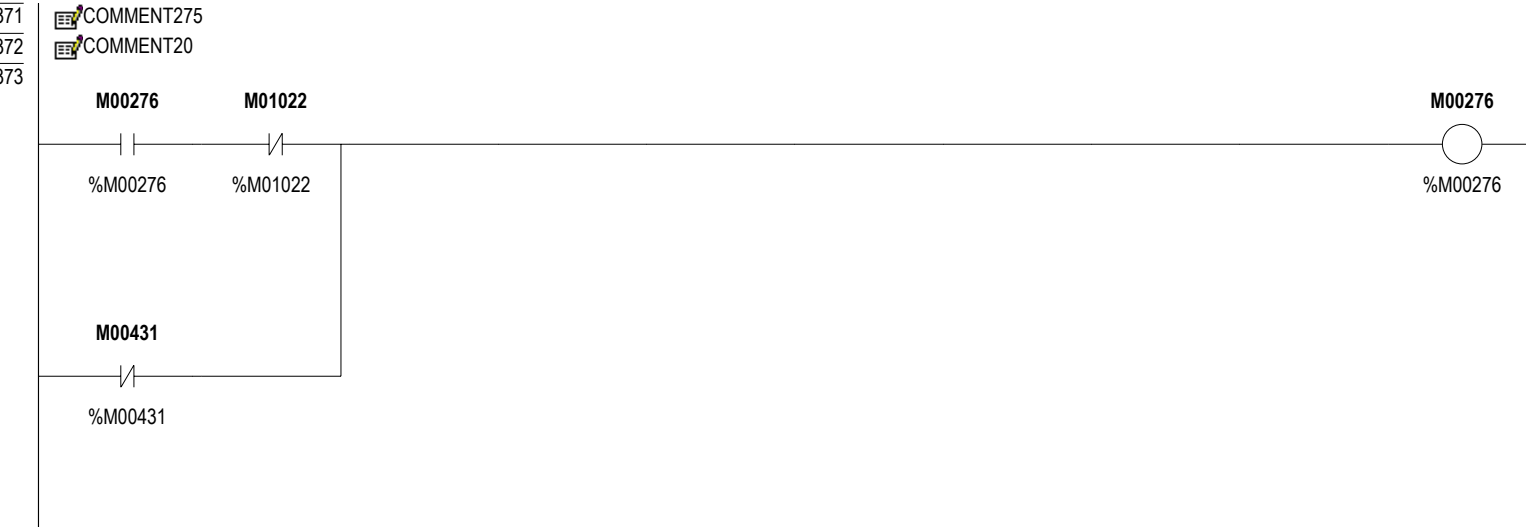
M00360 %M00360 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00367;

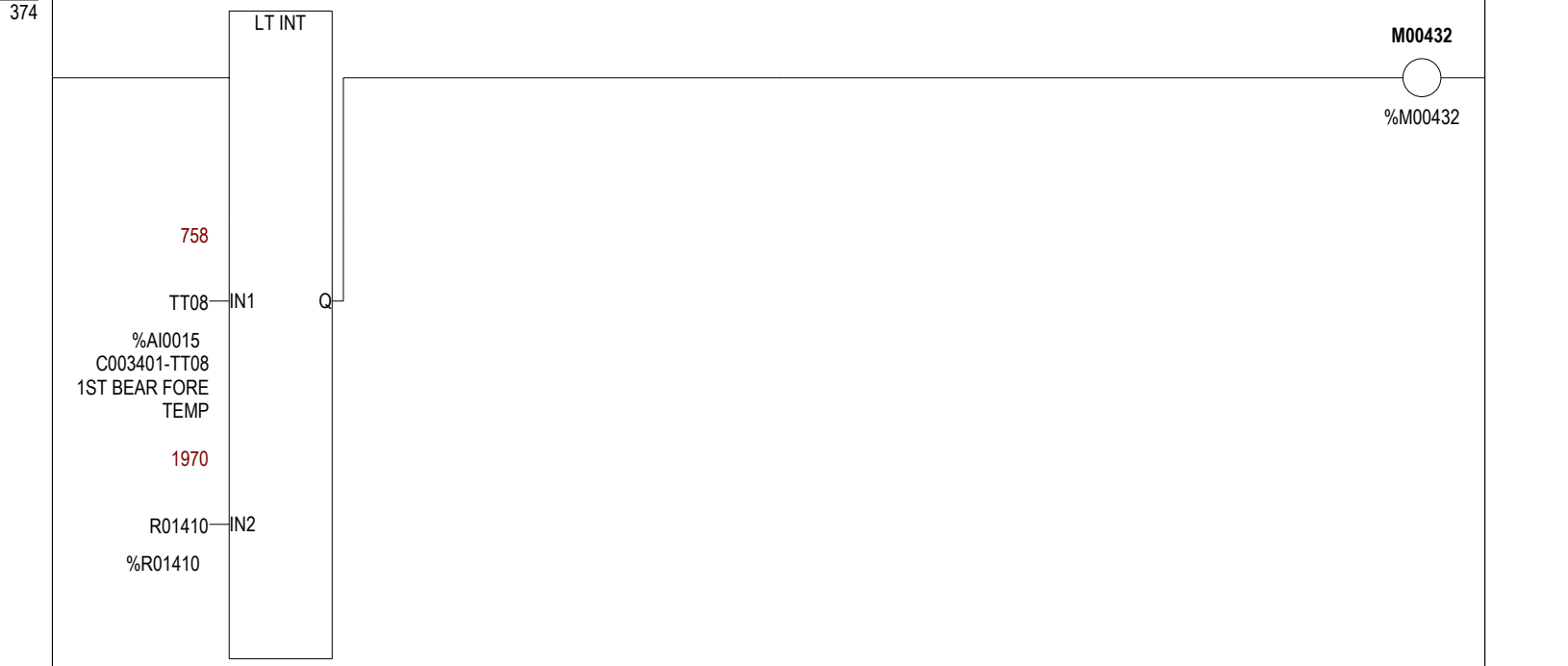
M00358 %M00358



M00488 %M00488 (Controlling Rung Reference)
LD Block, 'FRSTOUT': COIL 00368;
M00360 %M00360 (Controlling Rung Reference)
LD Block, 'FRSTOUT': COIL 00367;
M00359 %M00359
LD Block, 'FRSTOUT': NCCON 00369; NOCON 00370; COIL 00370;
LD Block, 'SUMMARY': NOCON 00055;



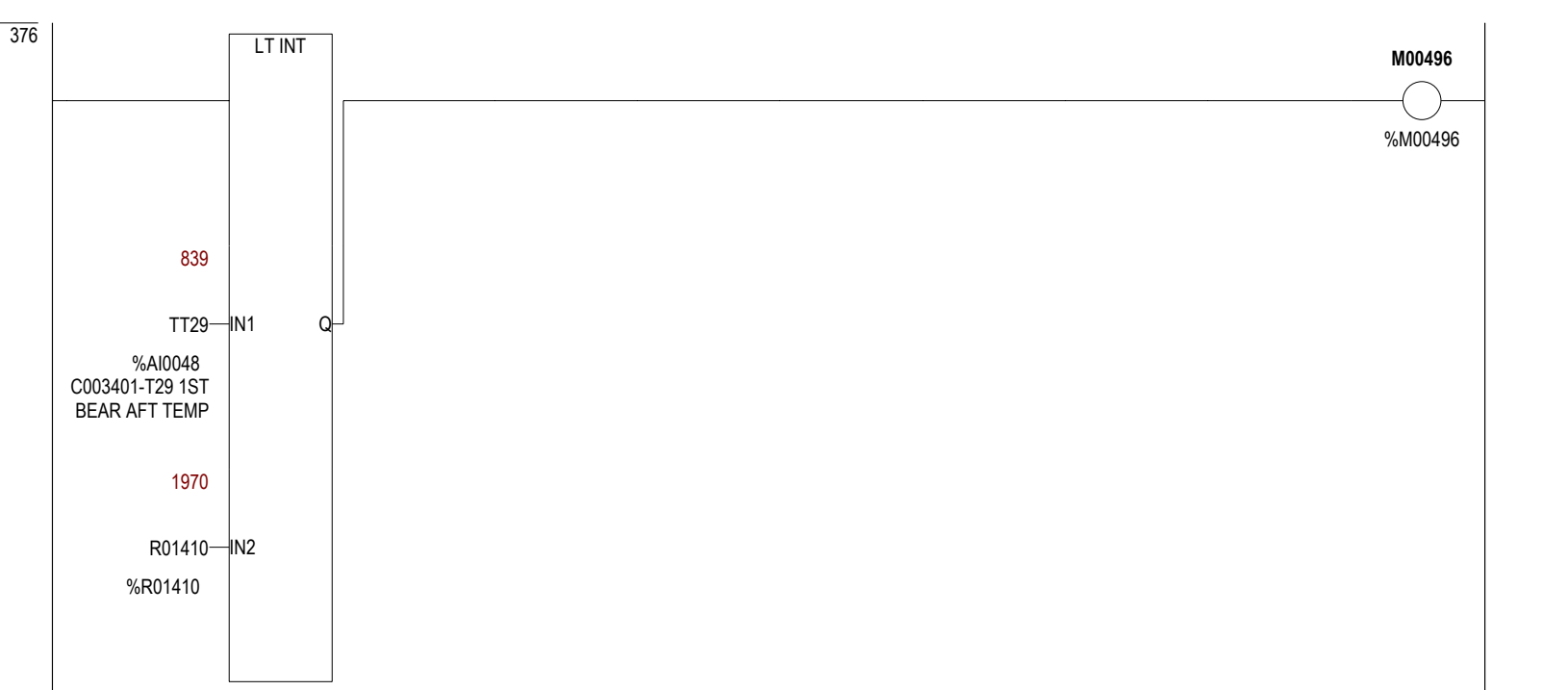
M01022 %M01022 (Controlling Rung Reference)
LD Block, 'FRSTOUT': COIL 00003;
M00276 %M00276
LD Block, 'FRSTOUT': NCCON 00377, 00378; NOCON 00373; COIL 00373;
LD Block, 'SUMMARY': NCCON 00010;



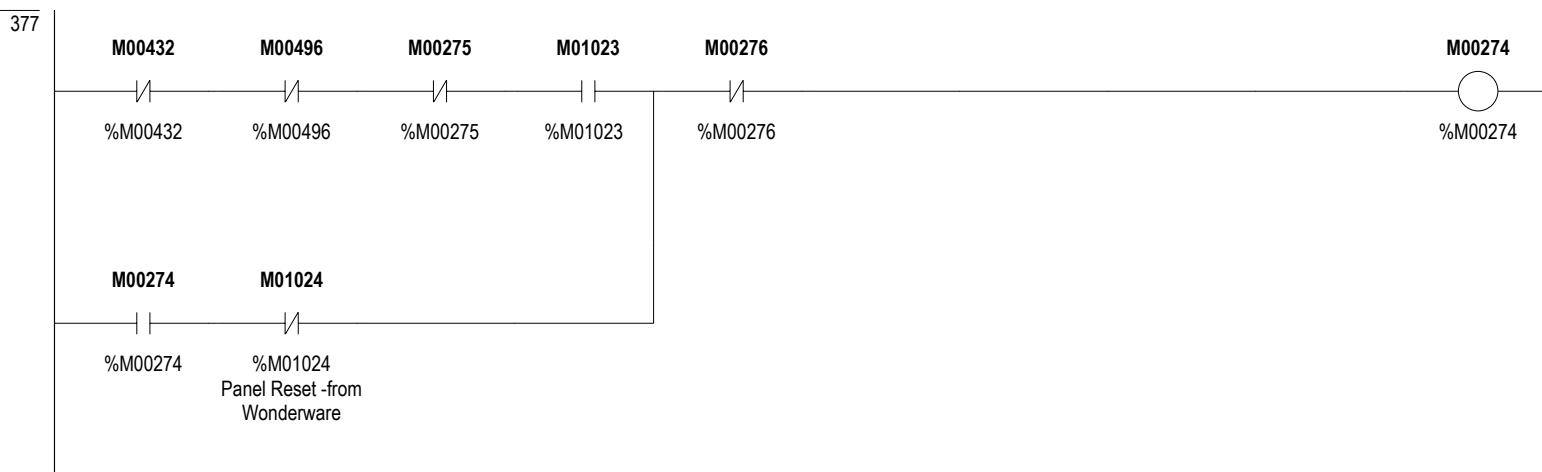
M00432 %M00432
LD Block,'FRSTOUT': NCCON 00377, 00378, 00379; COIL 00374;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00372 %M00372
LD Block,'FRSTOUT': NCCON 00379; NOCON 00375; COIL 00375;



M00496 %M00496
LD Block,'FRSTOUT': NCCON 00377, 00378, 00379; COIL 00376;



M00432 %M00432 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00374;

M00496 %M00496 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00376;

M00276 %M00276 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00373;

M00274 %M00274

LD Block,'FRSTOUT': NOCON 00377; COIL 00377;

LD Block,'SUMMARY': NCCON 00030;



M00432 %M00432 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00374;

M00496 %M00496 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00376;

M00276 %M00276 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00373;

M00275 %M00275

LD Block,'FRSTOUT': NCCON 00377; NOCON 00378; COIL 00378;

LD Block,'SUMMARY': NOCON 00051;



M00496 %M00496 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00376;

M00372 %M00372 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00375;

M00371 %M00371

LD Block,'FRSTOUT': NOCON 00379; COIL 00379;

M00432 %M00432 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00374;

380 COMMENT21



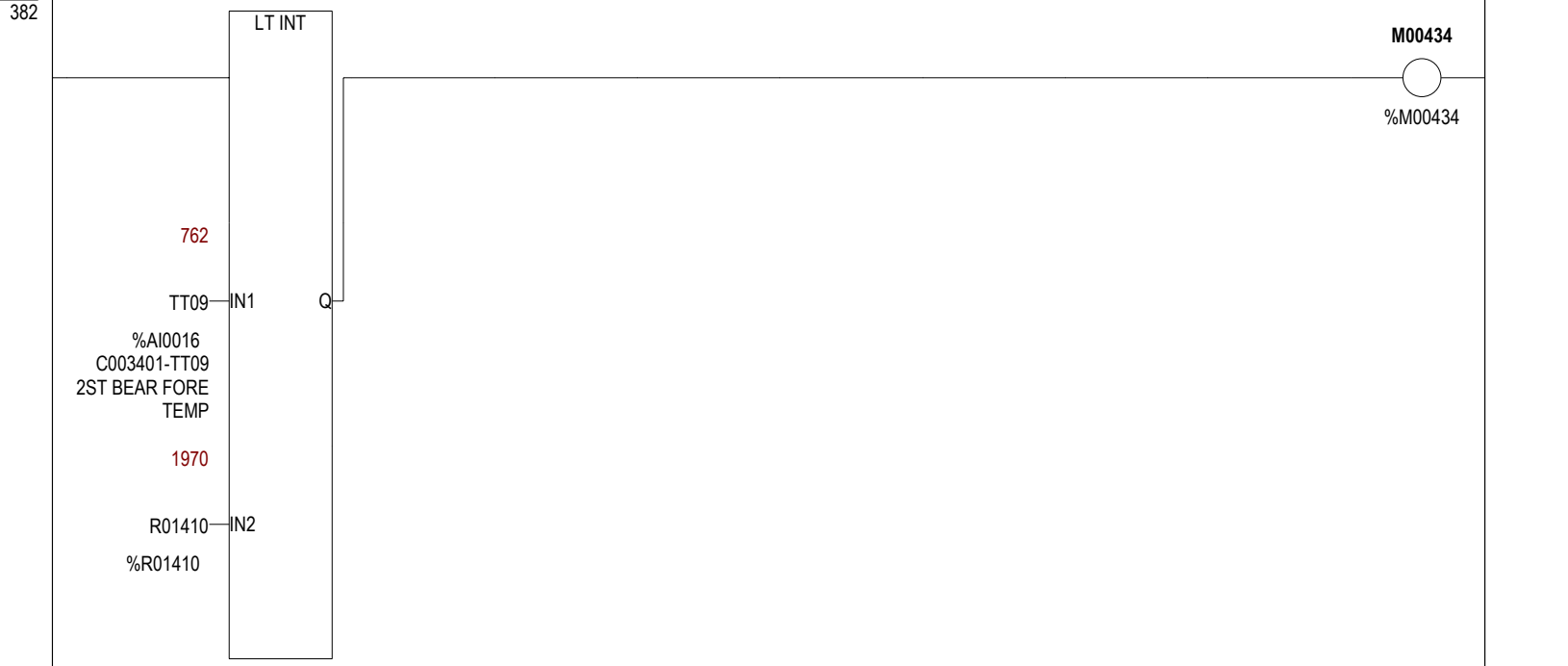
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00279 %M00279

LD Block,'FRSTOUT': NCCON 00385, 00386; NOCON 00381; COIL 00381;

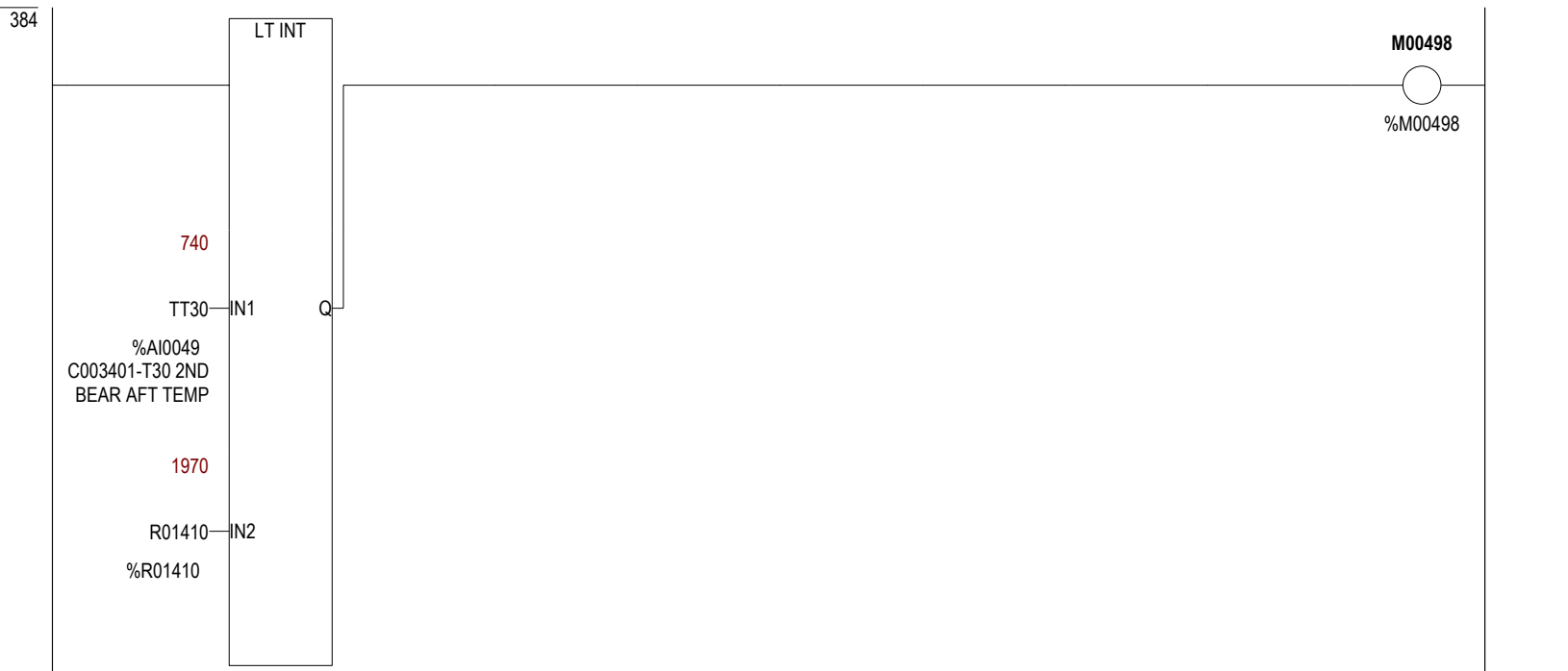
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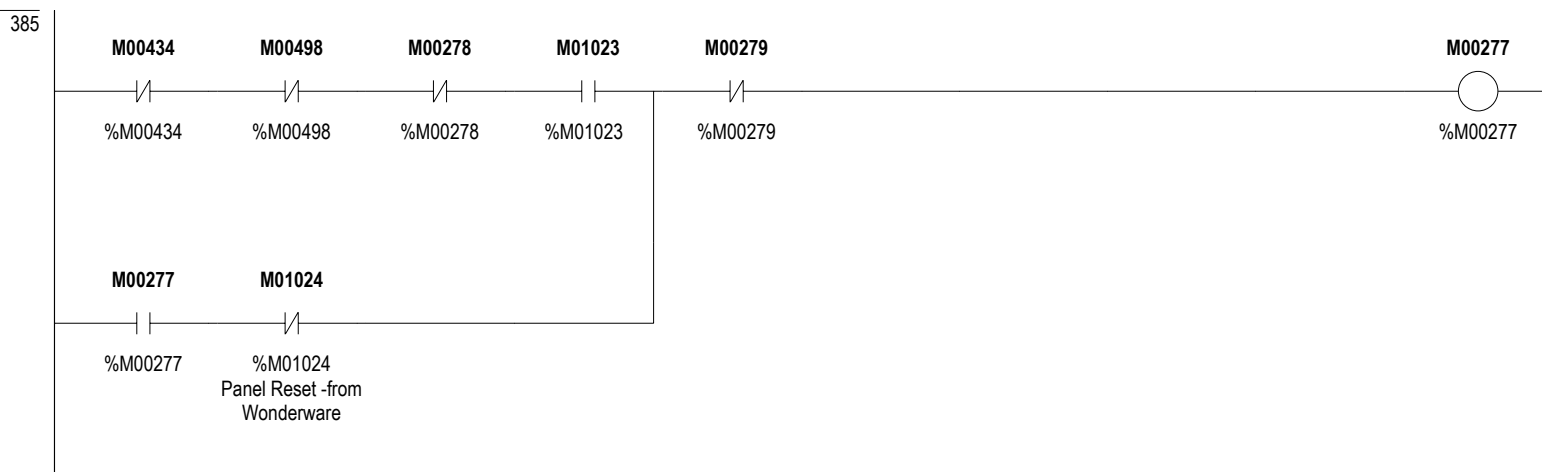
M00434 %M00434
LD Block,'FRSTOUT': NCCON 00385, 00386, 00387; COIL 00382;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00375 %M00375
LD Block,'FRSTOUT': NCCON 00387; NOCON 00383; COIL 00383;



M00498 %M00498
LD Block,'FRSTOUT': NCCON 00385, 00386, 00387; COIL 00384;



M00434 %M00434 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00382;

M00498 %M00498 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00384;

M00279 %M00279 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00381;

M00277 %M00277

LD Block,'FRSTOUT': NOCON 00385; COIL 00385;

LD Block,'SUMMARY': NCCON 00030;



M00434 %M00434 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00382;

M00498 %M00498 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00384;

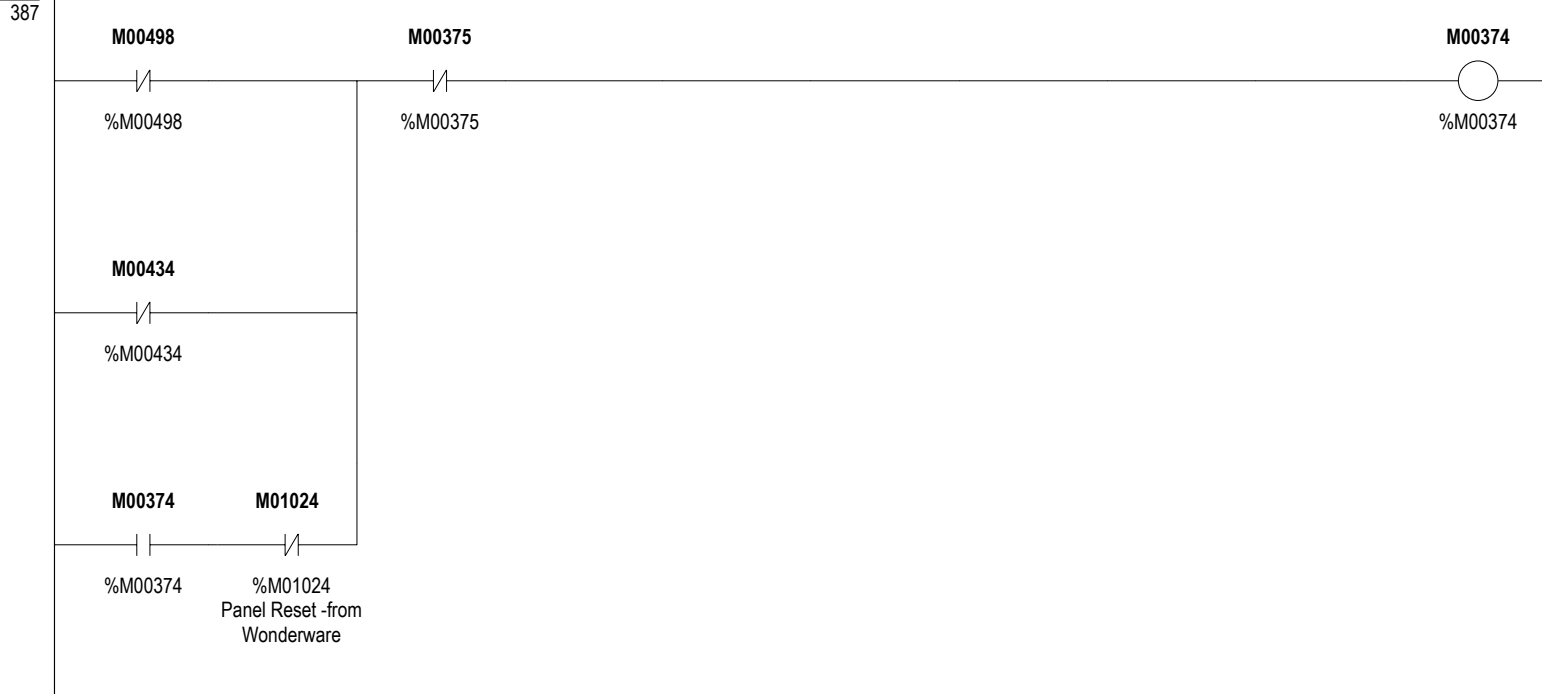
M00279 %M00279 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00381;

M00278 %M00278

LD Block,'FRSTOUT': NCCON 00385; NOCON 00386; COIL 00386;

LD Block,'SUMMARY': NOCON 00051;



M00498 %M00498 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00384;

M00375 %M00375 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00383;

M00374 %M00374

LD Block,'FRSTOUT': NOCON 00387; COIL 00387;

M00434 %M00434 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00382;

388 COMMENT22



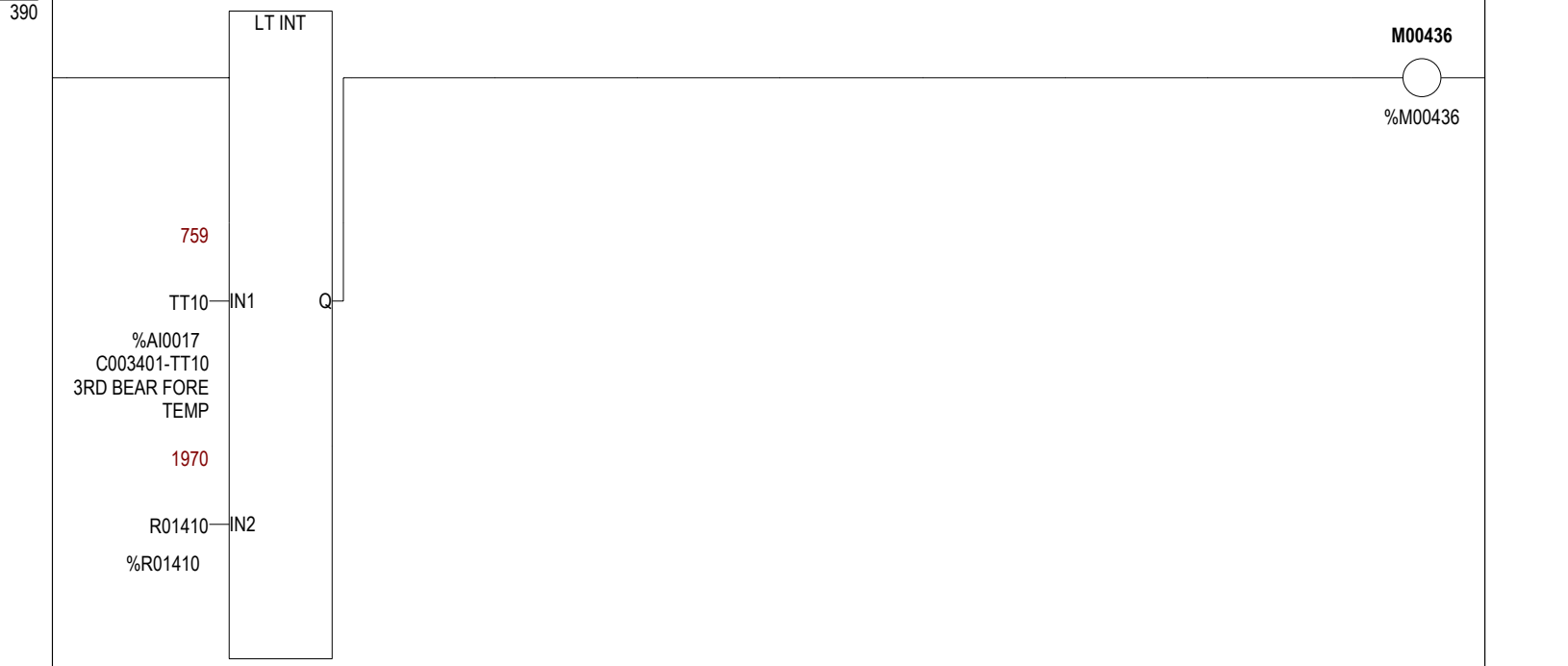
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00282 %M00282

LD Block,'FRSTOUT': NCCON 00393, 00394; NOCON 00389; COIL 00389;

LD Block,'SUMMARY': NCCON 00010;

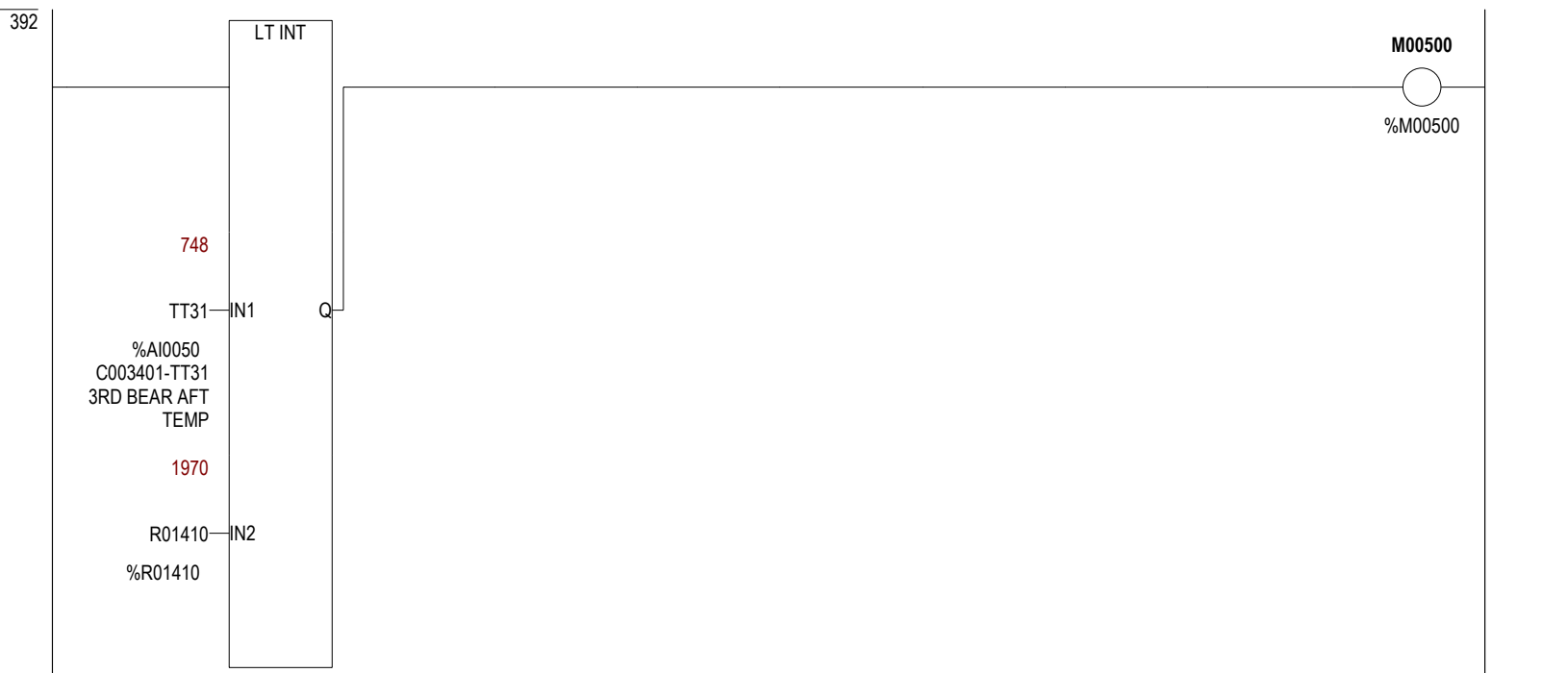


M00436 %M00436
LD Block,'FRSTOUT': NCCON 00393, 00394, 00395; COIL 00390;

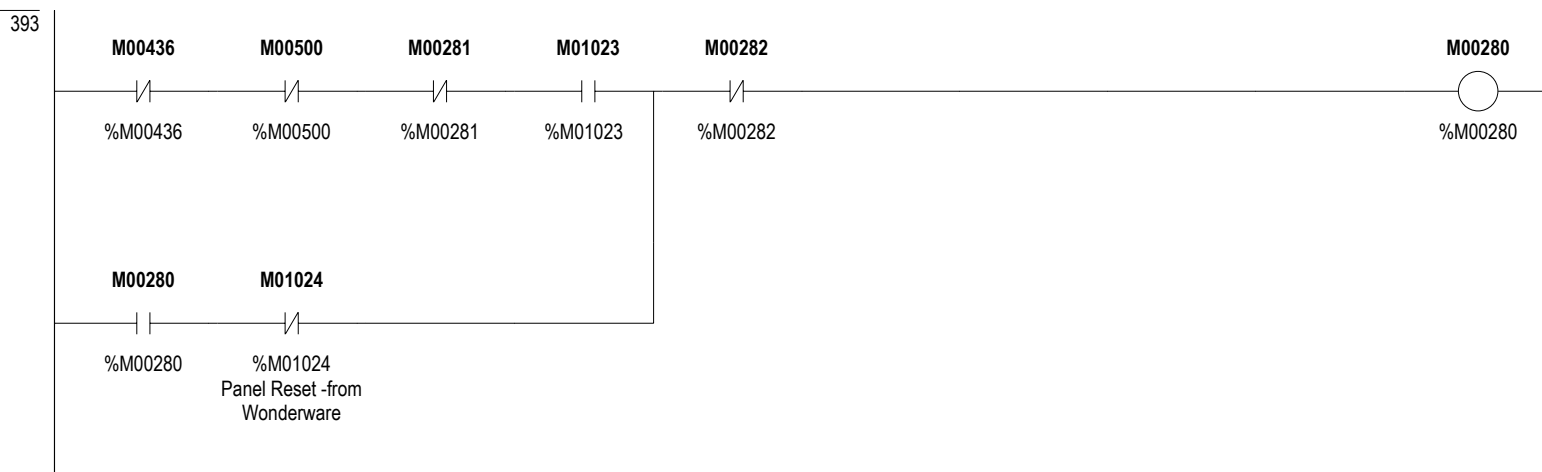


M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;

M00378 %M00378
LD Block,'FRSTOUT': NCCON 00395; NOCON 00391; COIL 00391;



M00500 %M00500
LD Block,'FRSTOUT': NCCON 00393, 00394, 00395; COIL 00392;



M00436 %M00436 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00390;

M00500 %M00500 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00392;

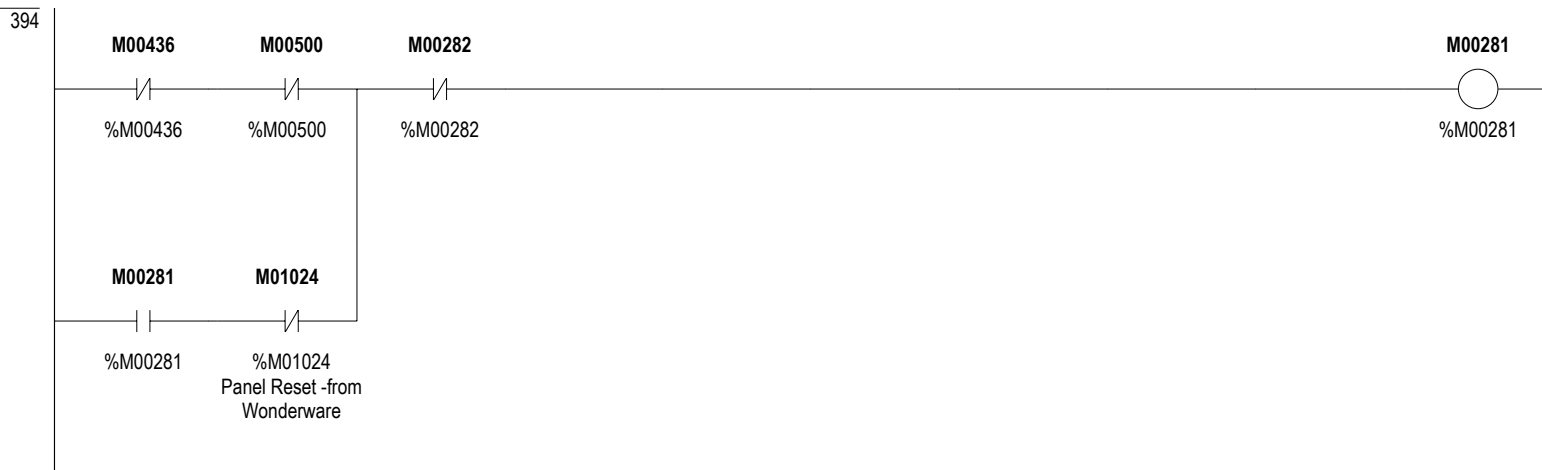
M00282 %M00282 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00389;

M00280 %M00280

LD Block,'FRSTOUT': NOCON 00393; COIL 00393;

LD Block,'SUMMARY': NCCON 00030;



M00436 %M00436 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00390;

M00500 %M00500 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00392;

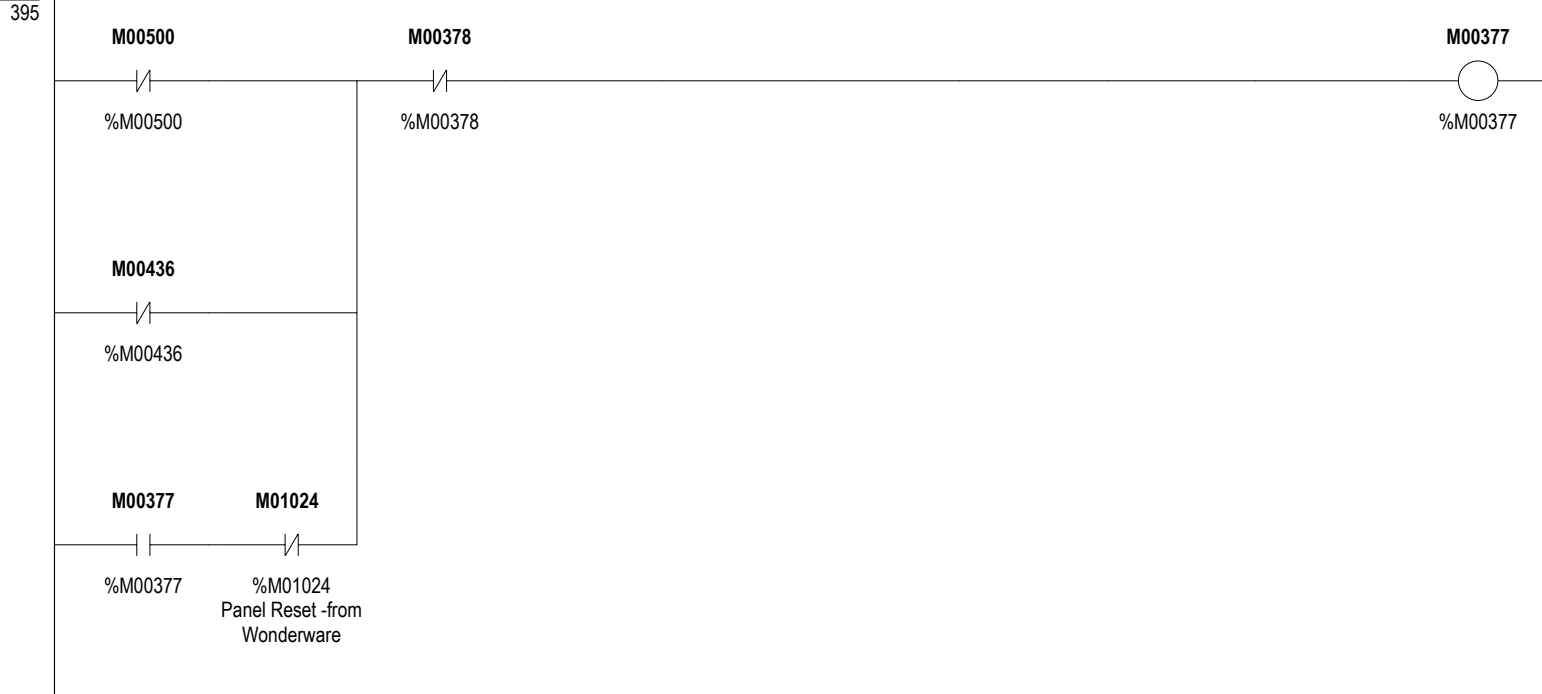
M00282 %M00282 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00389;

M00281 %M00281

LD Block,'FRSTOUT': NCCON 00393; NOCON 00394; COIL 00394;

LD Block,'SUMMARY': NOCON 00051;



M00500 %M00500 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00392;

M00378 %M00378 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00391;

M00377 %M00377

LD Block,'FRSTOUT': NOCON 00395; COIL 00395;

M00436 %M00436 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00390;

396 COMMENT23



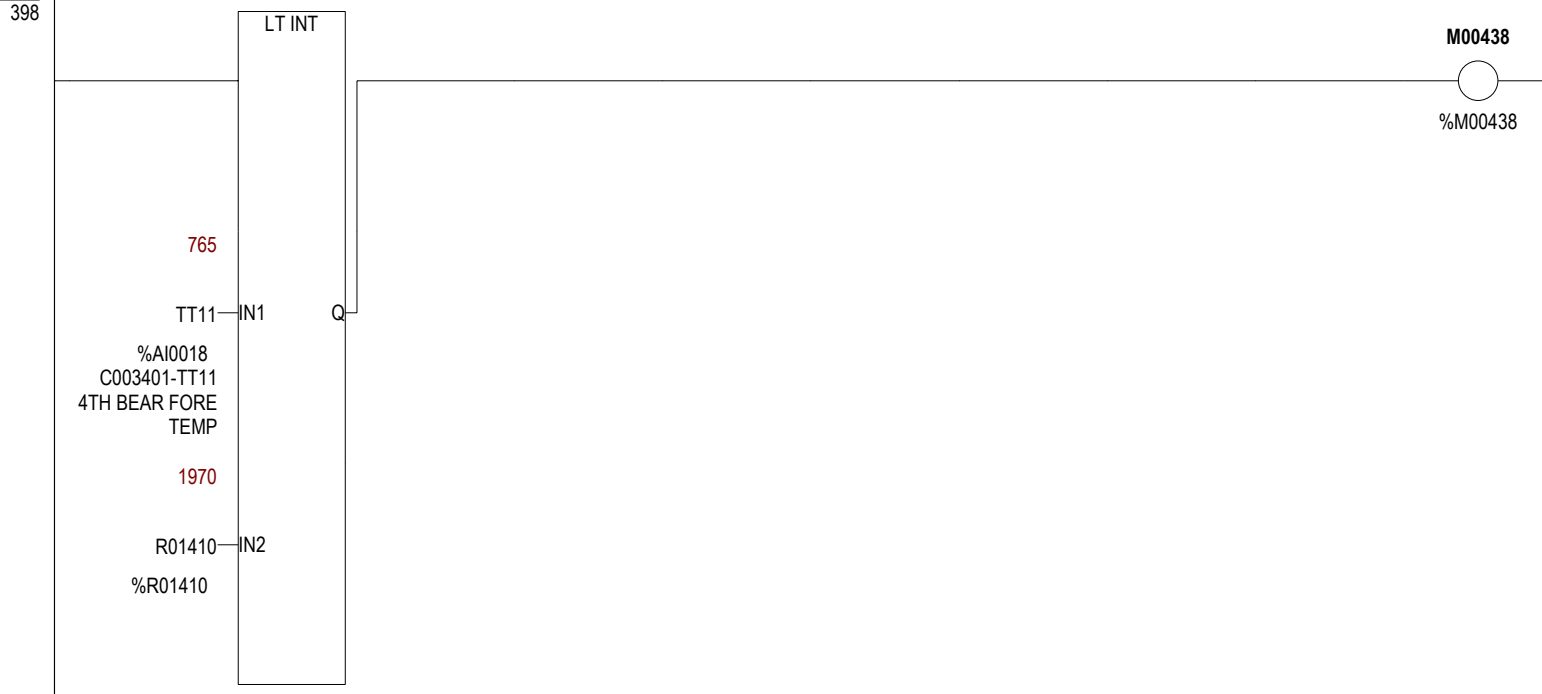
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00285 %M00285

LD Block,'FRSTOUT': NCCON 00401, 00402; NOCON 00397; COIL 00397;

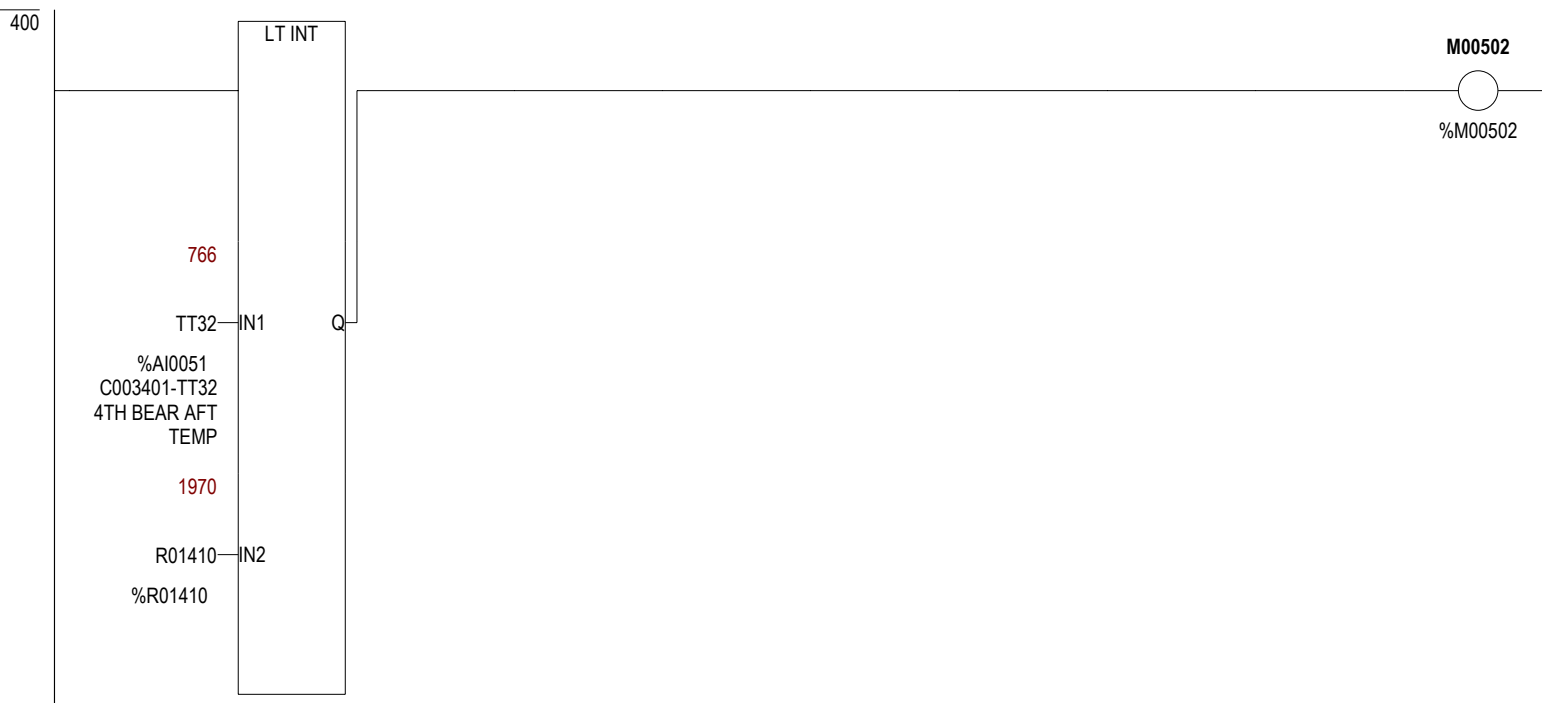
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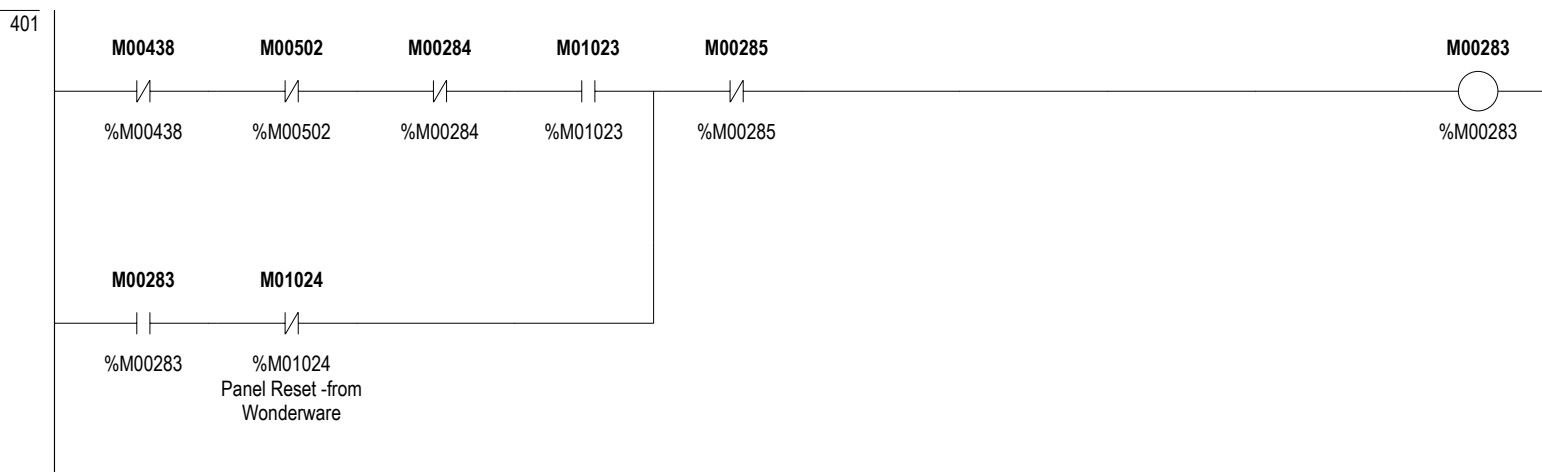
M00438 %M00438
LD Block,'FRSTOUT': NCCON 00401, 00402, 00403; COIL 00398;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00381 %M00381
LD Block,'FRSTOUT': NCCON 00403; NOCON 00399; COIL 00399;



M00502 %M00502
LD Block,'FRSTOUT': NCCON 00401, 00402, 00403; COIL 00400;



M00438 %M00438 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00398;

M00502 %M00502 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00400;

M00285 %M00285 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00397;

M00283 %M00283

LD Block,'FRSTOUT': NOCON 00401; COIL 00401;

LD Block,'SUMMARY': NCCON 00030;



M00438 %M00438 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00398;

M00502 %M00502 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00400;

M00285 %M00285 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00397;

M00284 %M00284

LD Block,'FRSTOUT': NCCON 00401; NOCON 00402; COIL 00402;

LD Block,'SUMMARY': NOCON 00051;



M00502 %M00502 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00400;

M00381 %M00381 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00399;

M00380 %M00380

LD Block,'FRSTOUT': NOCON 00403; COIL 00403;

M00438 %M00438 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00398;

404 COMMENT24



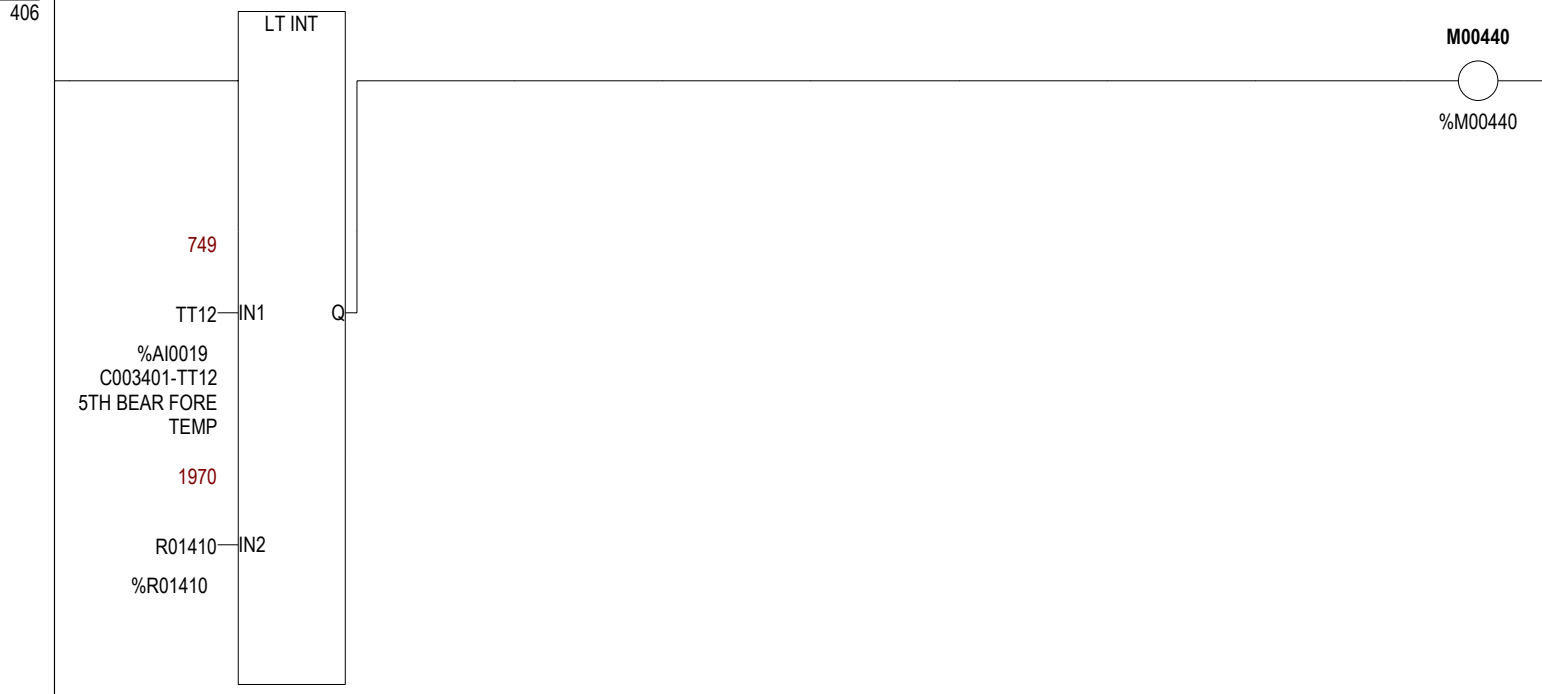
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LD Block,'FRSTOUT': COIL 00003;

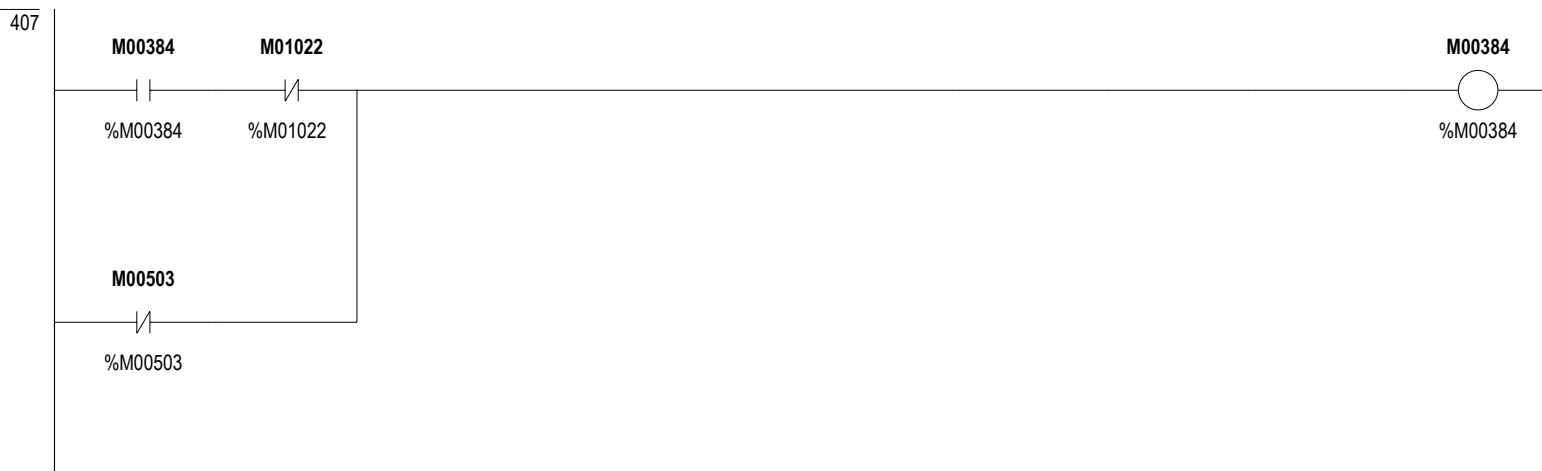
M00288 %M00288

LD Block,'FRSTOUT': NCCON 00409, 00410; NOCON 00405; COIL 00405;

LD Block,'SUMMARY': NCCON 00010;

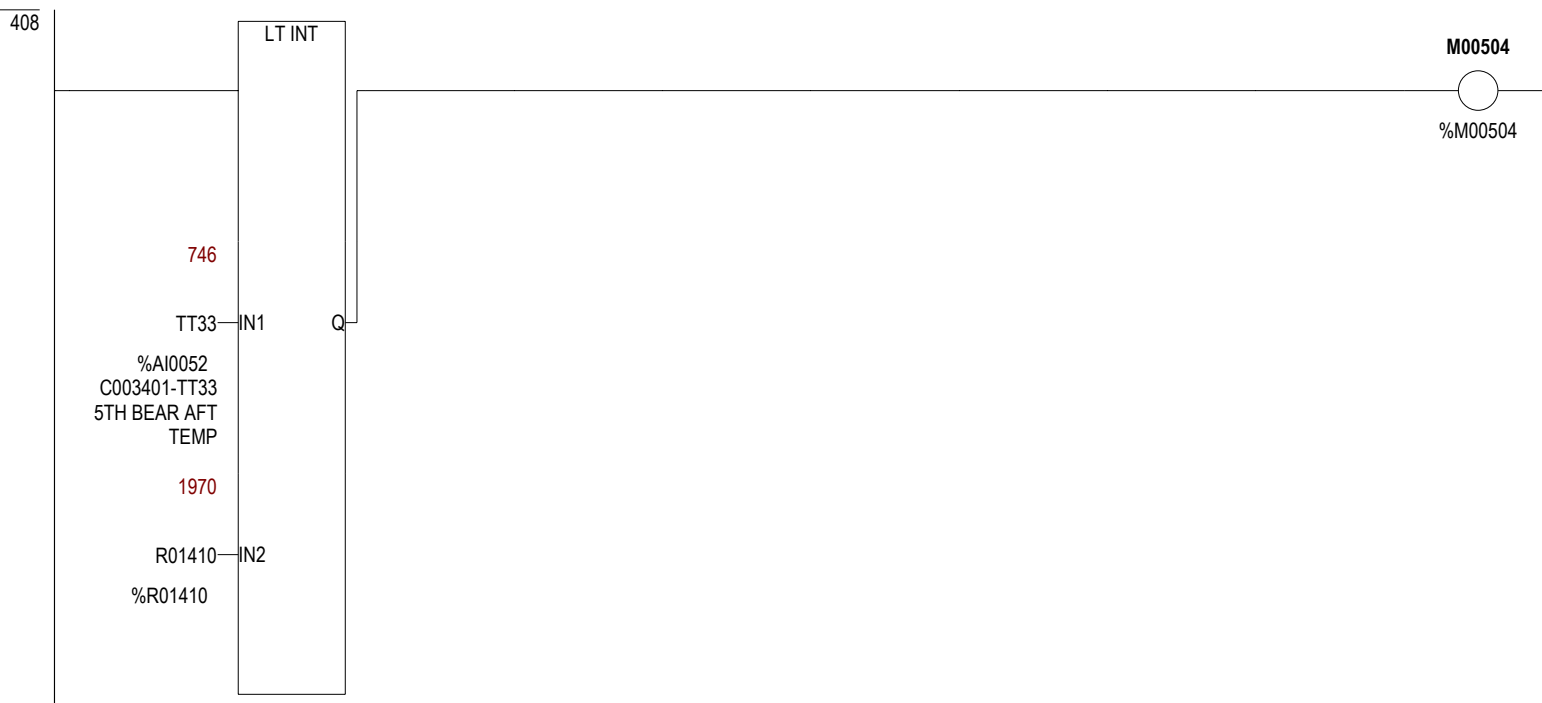


M00440 %M00440
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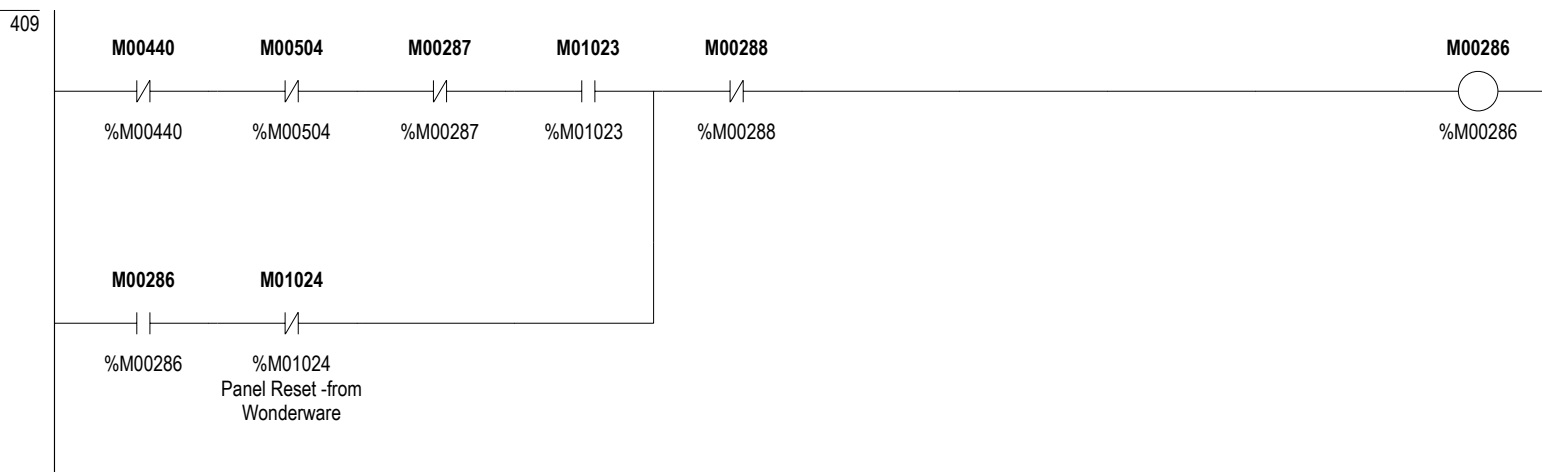


M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;

M00384 %M00384
LD Block,'FRSTOUT': NCCON 00411; NOCON 00407; COIL 00407;



M00504 %M00504
LD Block,'FRSTOUT': NCCON 00409, 00410, 00411; COIL 00408;



M00440 %M00440 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00406;

M00504 %M00504 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00408;

M00288 %M00288 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00405;

M00286 %M00286

LD Block,'FRSTOUT': NOCON 00409; COIL 00409;

LD Block,'SUMMARY': NCCON 00030;



M00440 %M00440 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00406;

M00504 %M00504 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00408;

M00288 %M00288 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00405;

M00287 %M00287

LD Block,'FRSTOUT': NCCON 00409; NOCON 00410; COIL 00410;

LD Block,'SUMMARY': NOCON 00051;



M00504 %M00504 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00408;

%M00384 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00407;

%M00383 (Controlling Rung Reference)

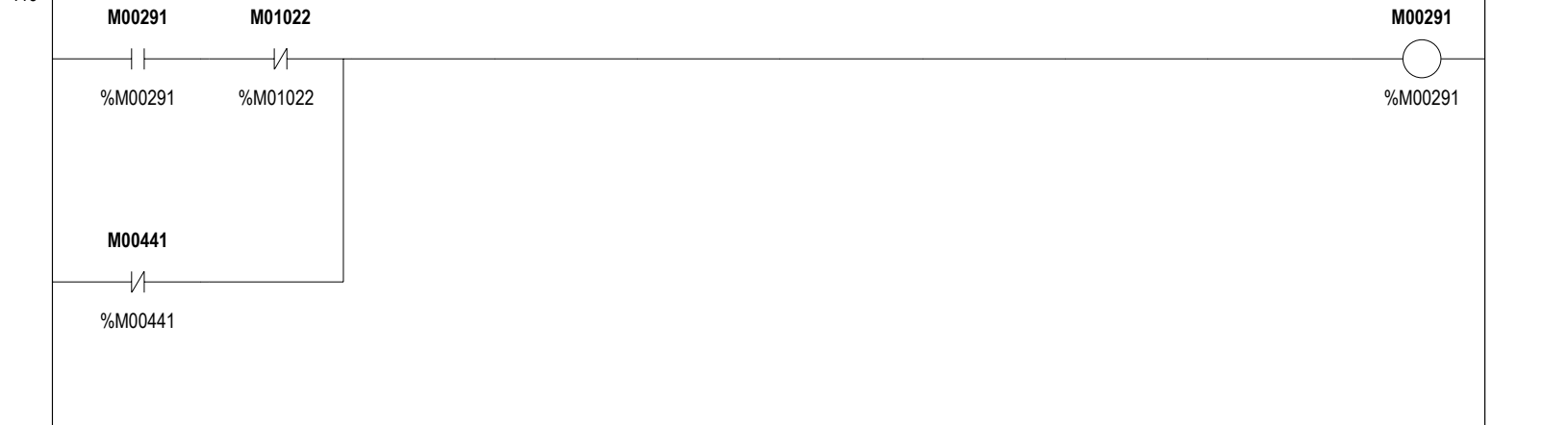
LD Block,'FRSTOUT': NOCON 00411; COIL 00411;

%M00440 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00406;

412 COMMENT25

413



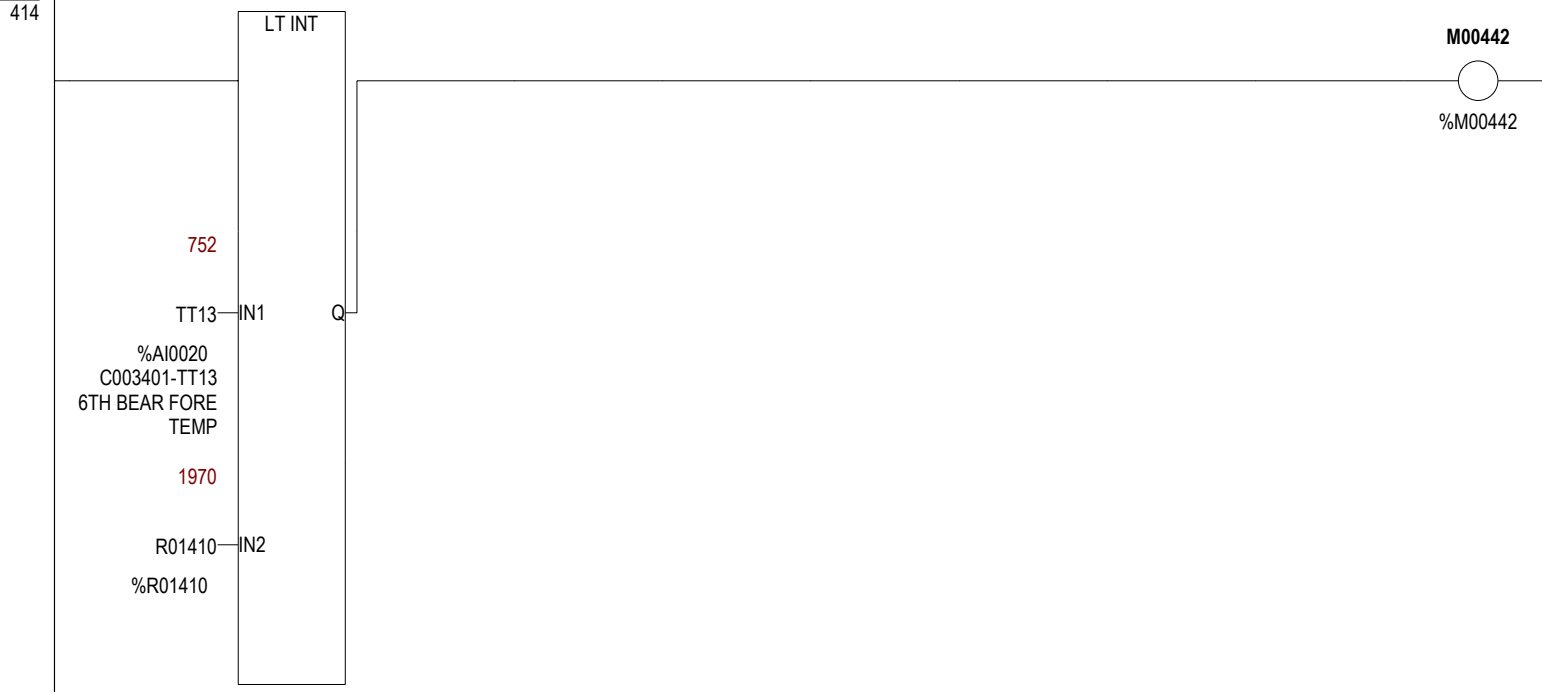
%M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

%M00291 (Controlling Rung Reference)

LD Block,'FRSTOUT': NCCON 00417, 00418; NOCON 00413; COIL 00413;

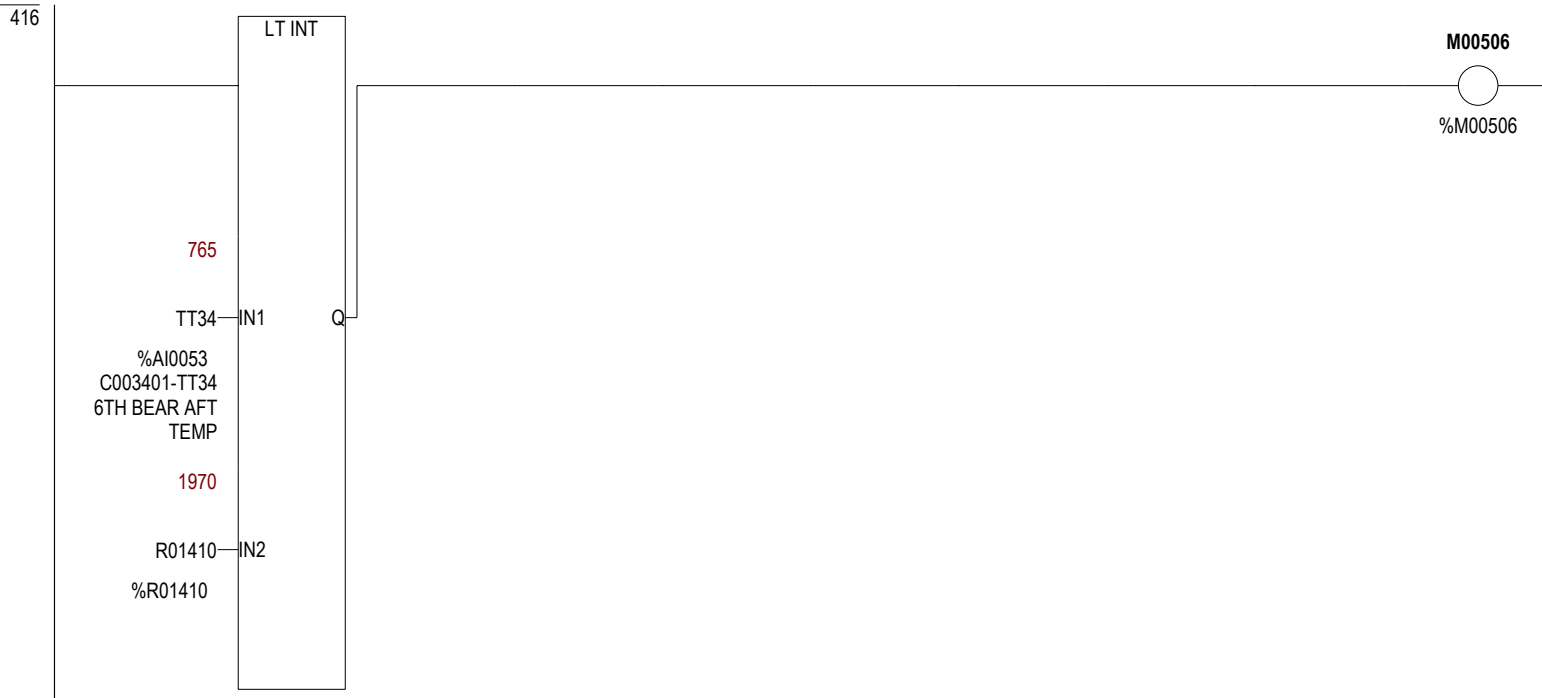
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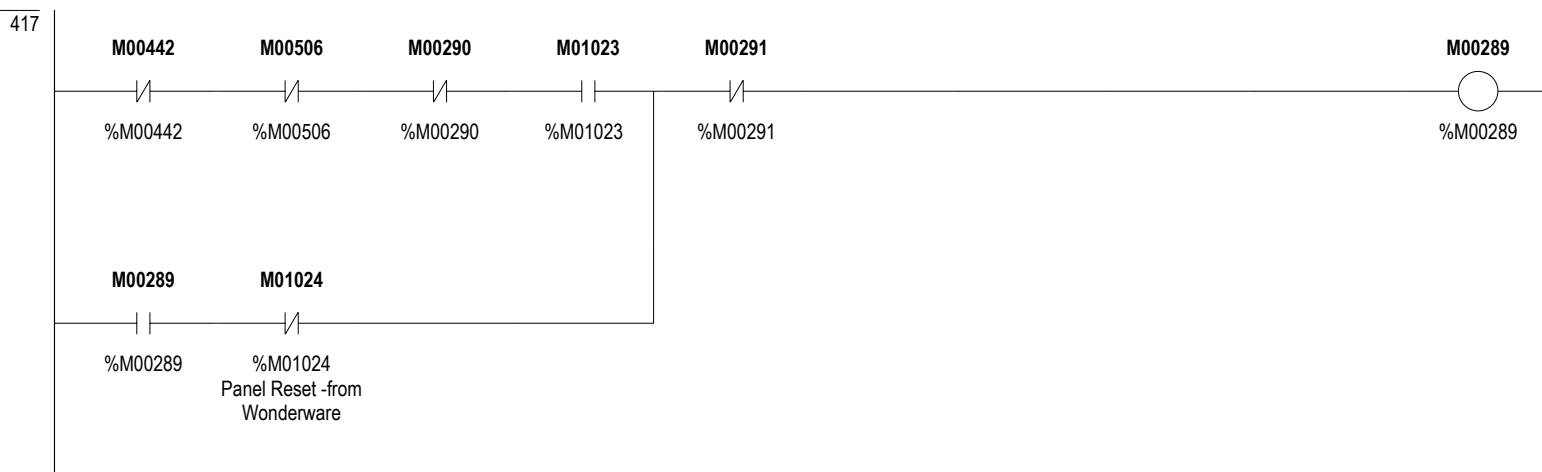
M00442 %M00442
LD Block,'FRSTOUT': NCCON 00417, 00418, 00419; COIL 00414;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00387 %M00387
LD Block,'FRSTOUT': NCCON 00419; NOCON 00415; COIL 00415;



M00506 %M00506
LD Block,'FRSTOUT': NCCON 00417, 00418, 00419; COIL 00416;



M00442 %M00442 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00414;

M00506 %M00506 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00416;

M00291 %M00291 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00413;

M00289 %M00289

LD Block,'FRSTOUT': NOCON 00417; COIL 00417;

LD Block,'SUMMARY': NCCON 00030;



M00442 %M00442 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00414;

M00506 %M00506 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00416;

M00291 %M00291 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00413;

M00290 %M00290

LD Block,'FRSTOUT': NCCON 00417; NOCON 00418; COIL 00418;

LD Block,'SUMMARY': NOCON 00051;



M00506 %M00506 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00416;

M00387 %M00387 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00415;

M00386 %M00386

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M00442 %M00442 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00414;

420 COMMENT8191



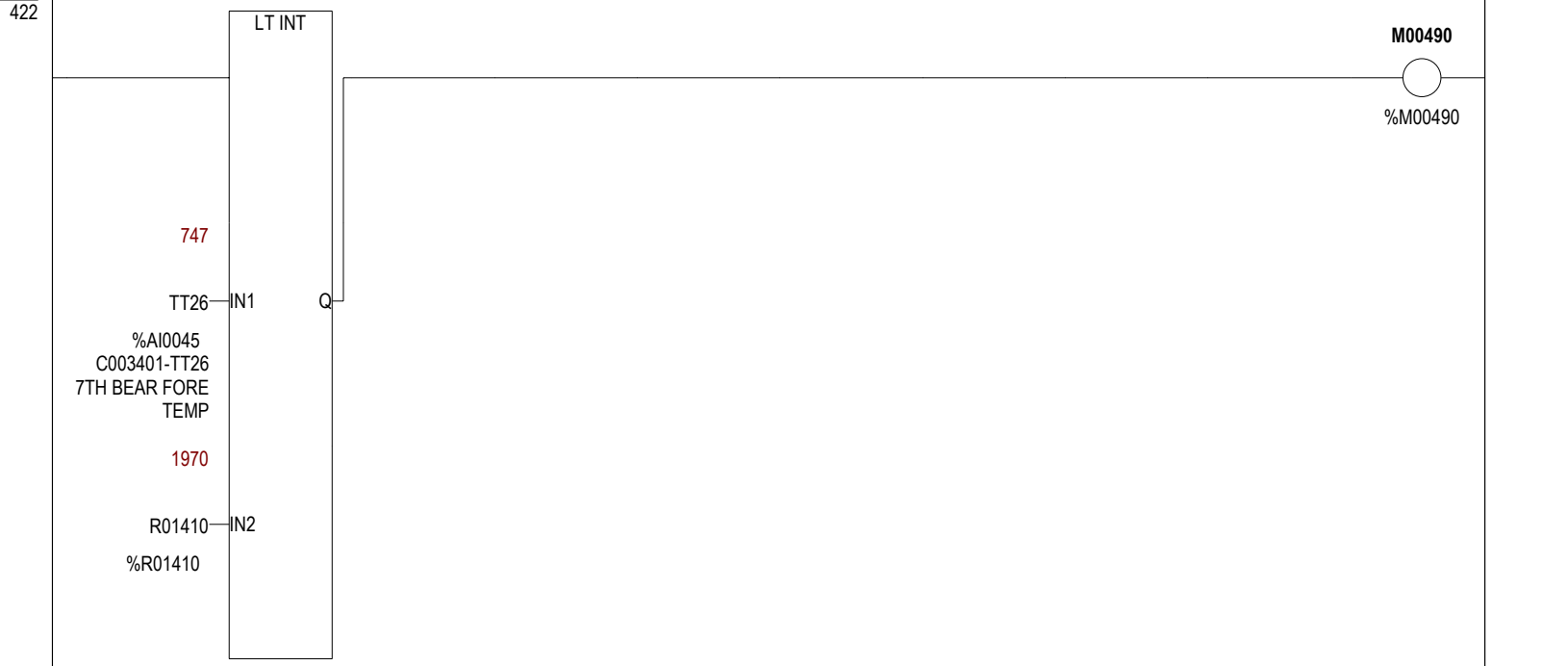
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00363 %M00363

LD Block,'FRSTOUT': NCCON 00425, 00426; NOCON 00421; COIL 00421;

LD Block,'SUMMARY': NCCON 00014;

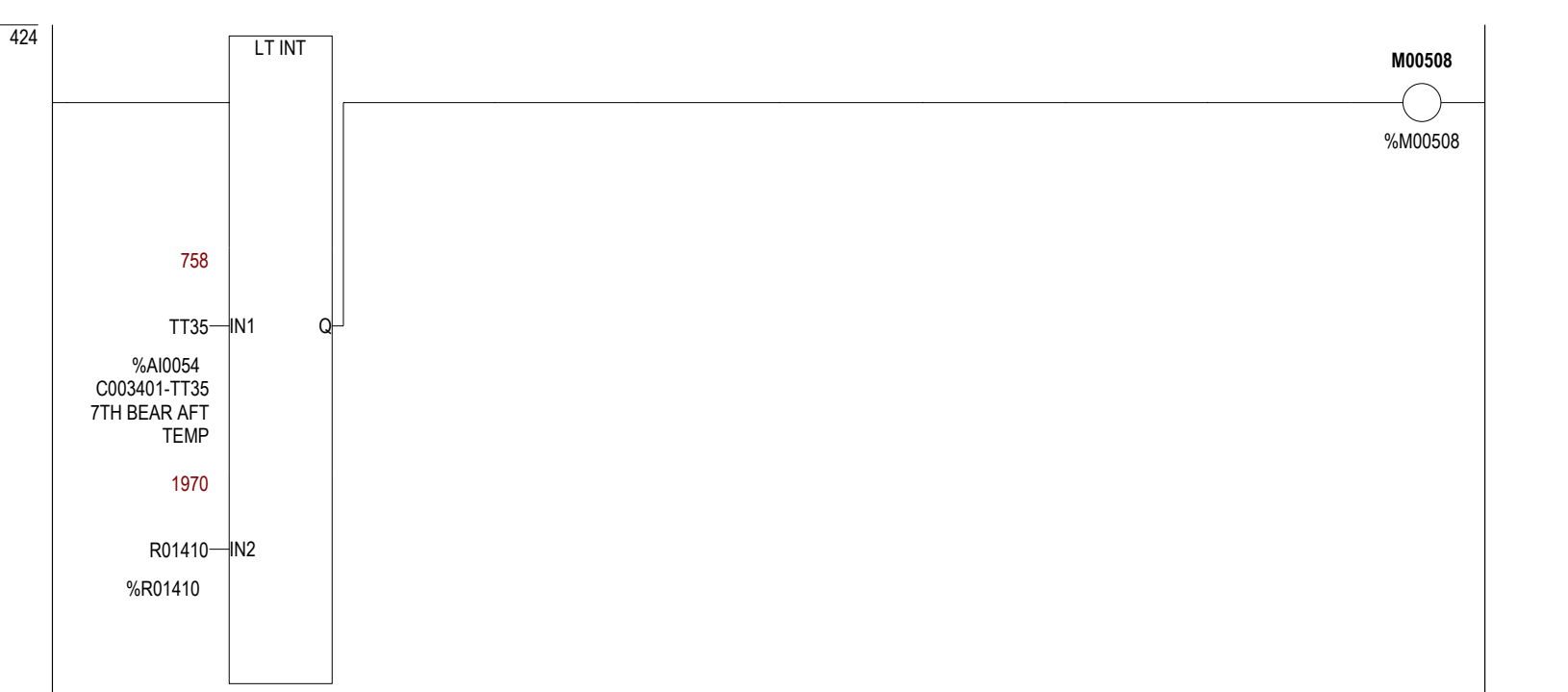


M00490 %M00490
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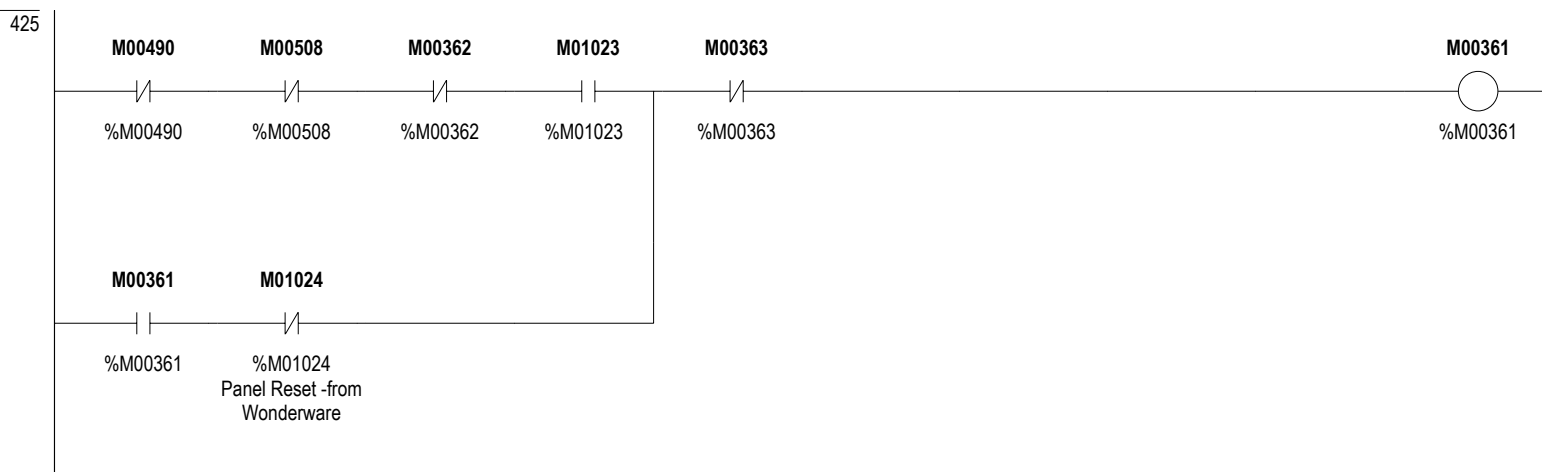


M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;

M00390 %M00390
LD Block,'FRSTOUT': NCCON 00427; NOCON 00423; COIL 00423;



M00508 %M00508
LD Block,'FRSTOUT': NCCON 00425, 00426, 00427; COIL 00424;



M00490 %M00490 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00422;

M00508 %M00508 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00424;

M00363 %M00363 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00421;

M00361 %M00361

LD Block,'FRSTOUT': NOCON 00425; COIL 00425;

LD Block,'SUMMARY': NCCON 00034;



M00490 %M00490 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00422;

M00508 %M00508 (Controlling Rung Reference)

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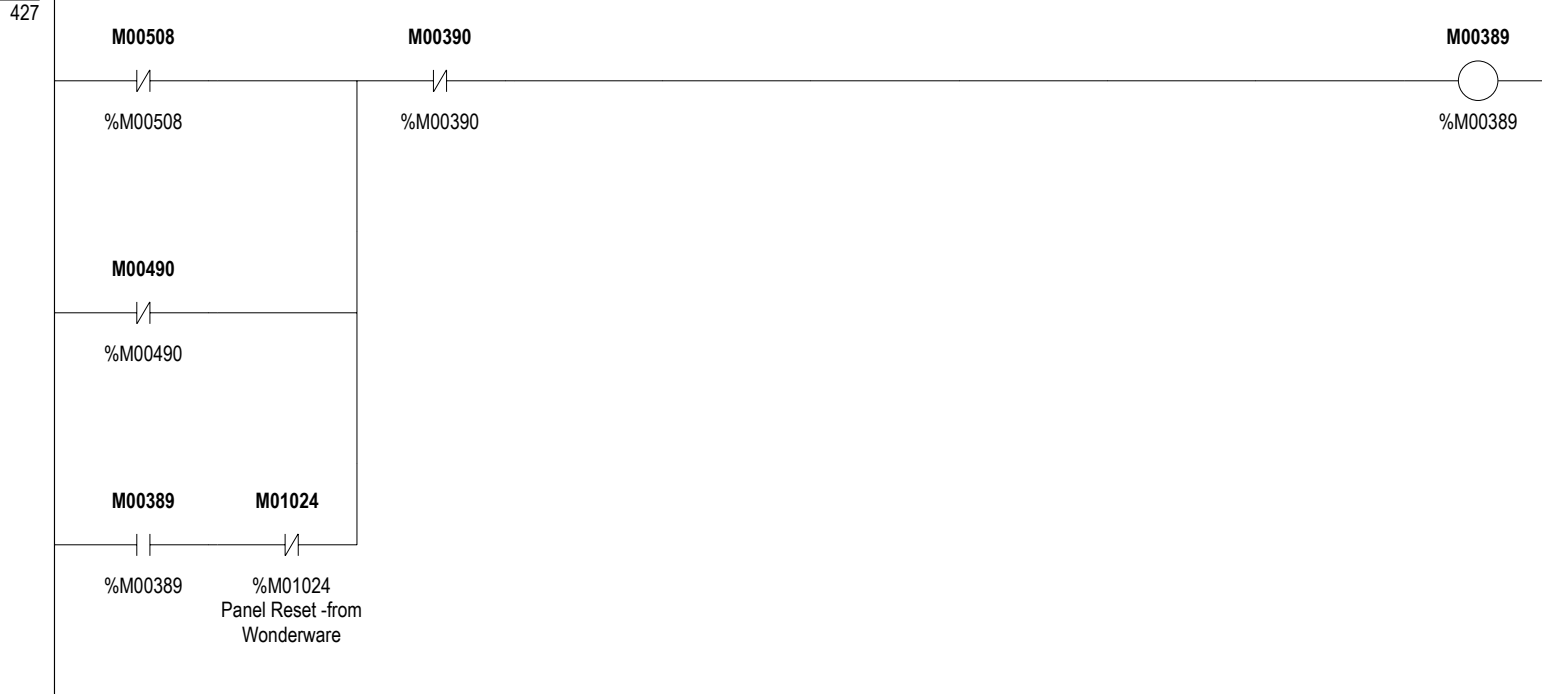
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LD Block,'FRSTOUT': COIL 00421;

M00362 %M00362

LD Block,'FRSTOUT': NCCON 00425; NOCON 00426; COIL 00426;

LD Block,'SUMMARY': NOCON 00055;



M00508 %M00508 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00424;

%M00390 %M00390 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00423;

%M00389 %M00389

LD Block,'FRSTOUT': NOCON 00427; COIL 00427;

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LD Block,'FRSTOUT': COIL 00422;

428 COMMENT276



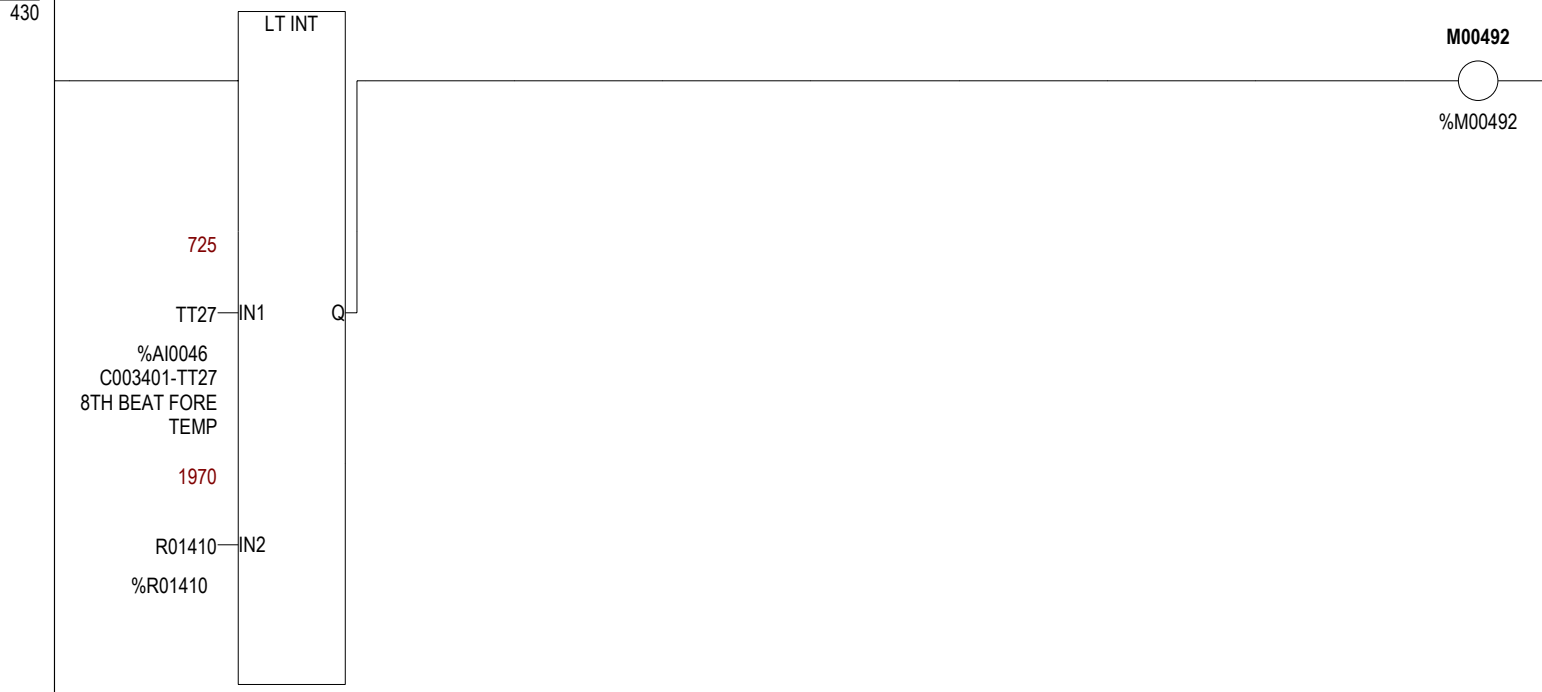
M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

%M00366 %M00366

LD Block,'FRSTOUT': NCCON 00433, 00434; NOCON 00429; COIL 00429;

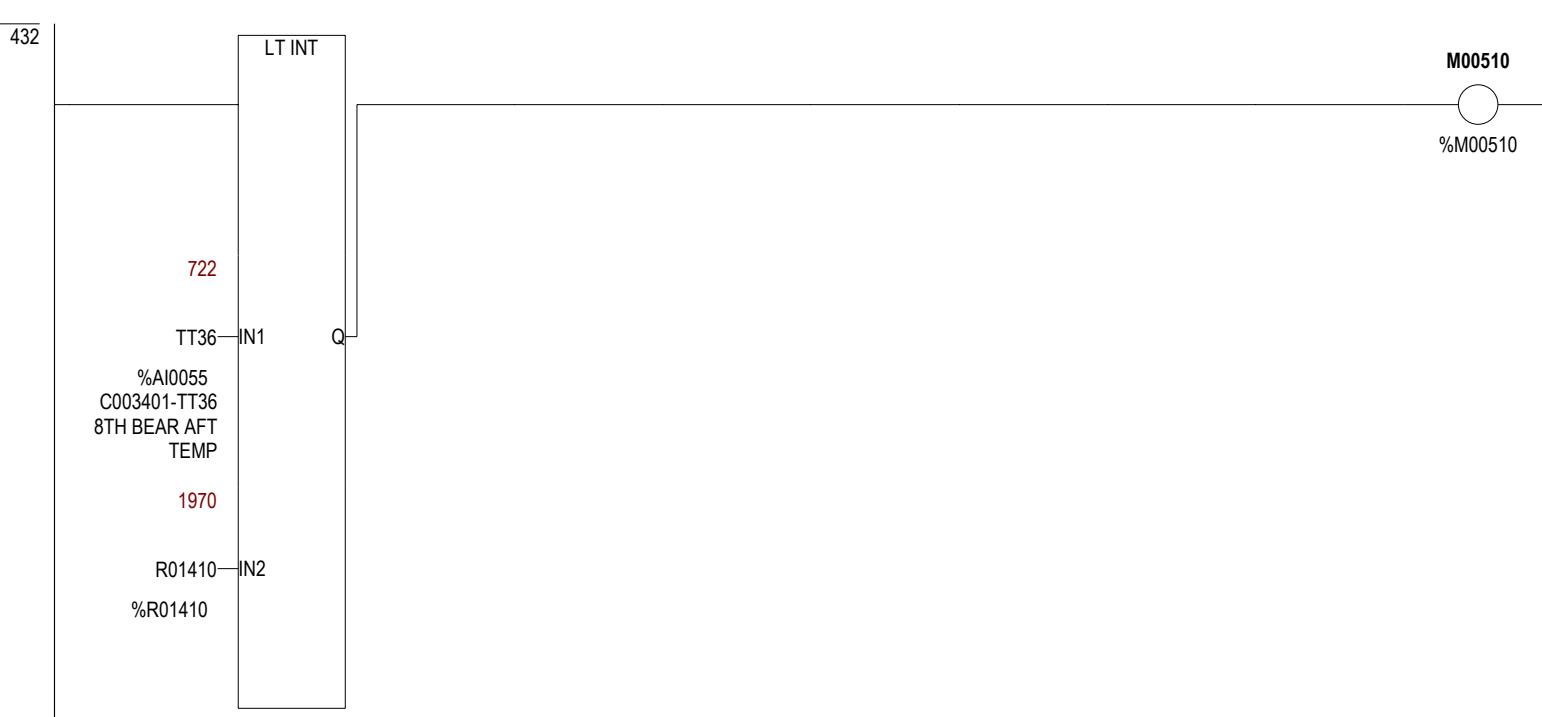
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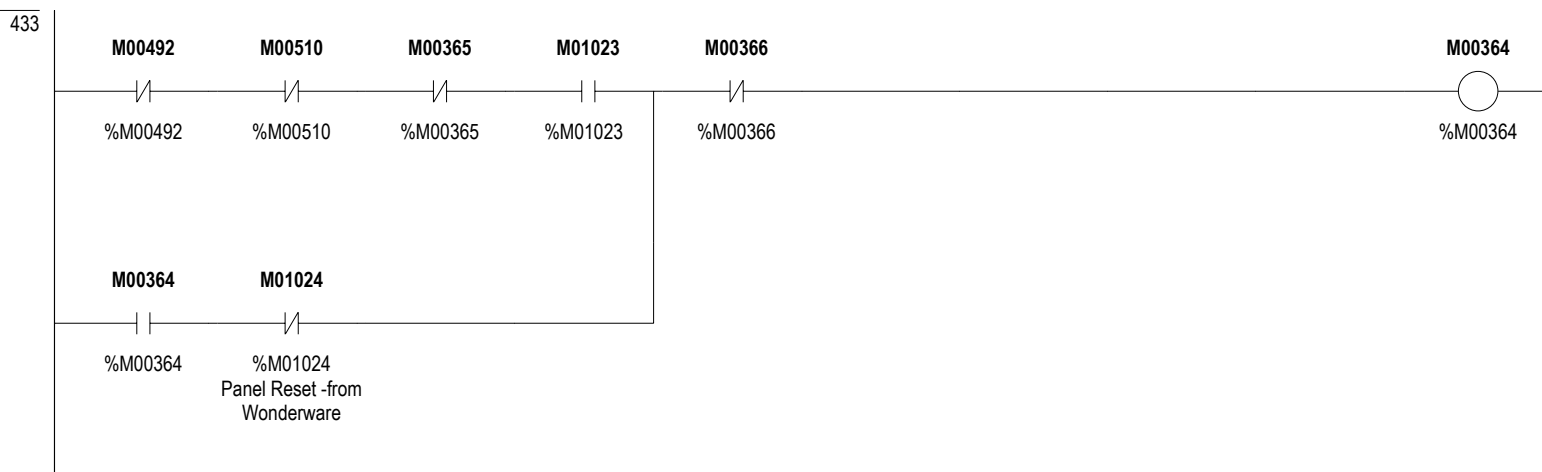
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LD Block,'FRSTOUT': NCCON 00433, 00434, 00435; COIL 00430;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00393 %M00393
LD Block,'FRSTOUT': NCCON 00435; NOCON 00431; COIL 00431;



M00510 %M00510
LD Block,'FRSTOUT': NCCON 00433, 00434, 00435; COIL 00432;



M00492 %M00492 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00430;

M00510 %M00510 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00432;

M00366 %M00366 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00429;

M00364 %M00364

LD Block,'FRSTOUT': NOCON 00433; COIL 00433;

LD Block,'SUMMARY': NCCON 00035;



M00492 %M00492 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00430;

M00510 %M00510 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00432;

M00366 %M00366 (Controlling Rung Reference)

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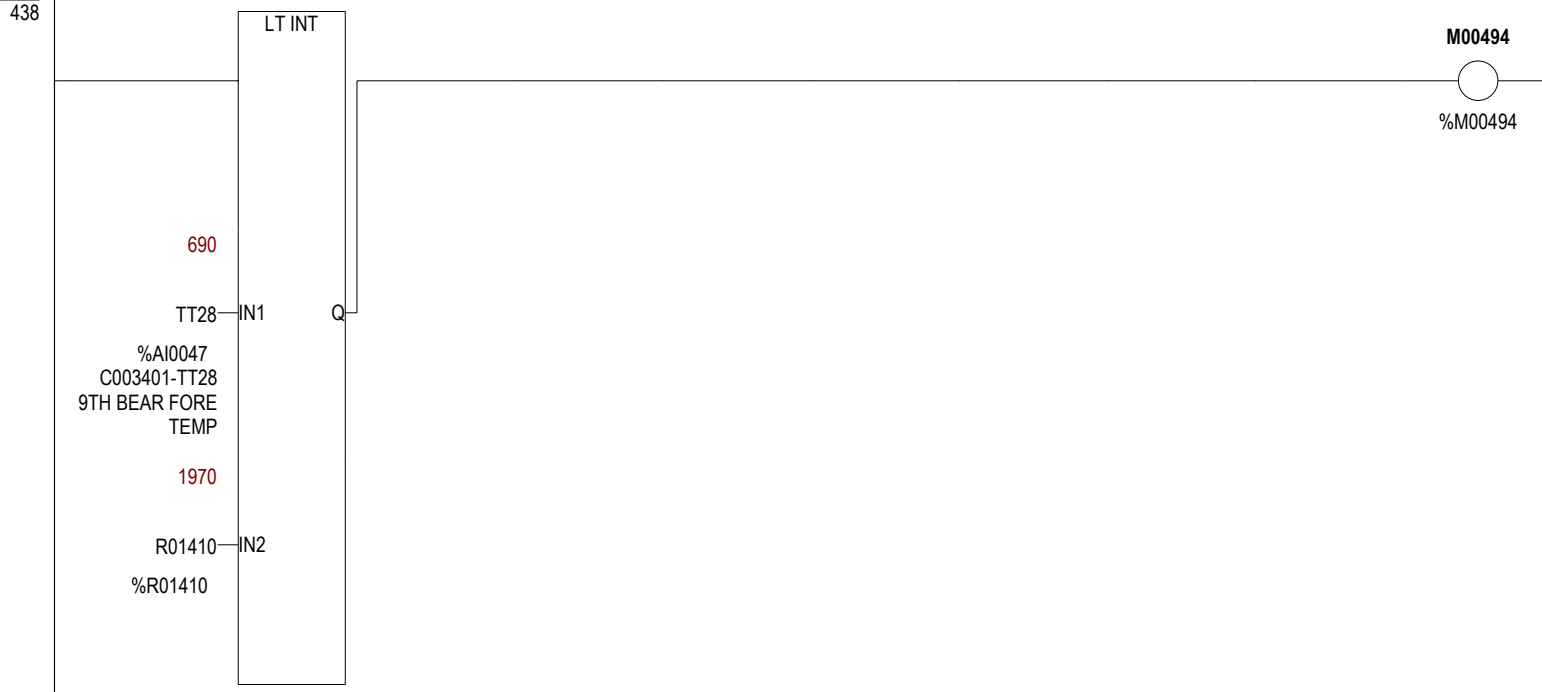
LD Block,'SUMMARY': NOCON 00056;

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LD Block,FRSTOUT: COIL 00432;
M00393  %M00393 (Controlling Rung Reference)
LD Block,FRSTOUT: COIL 00431;
M00392  %M00392
LD Block,FRSTOUT: NOCON 00435; COIL 00435;
M00492  %M00492 (Controlling Rung Reference)
LD Block,FRSTOUT: COIL 00430;

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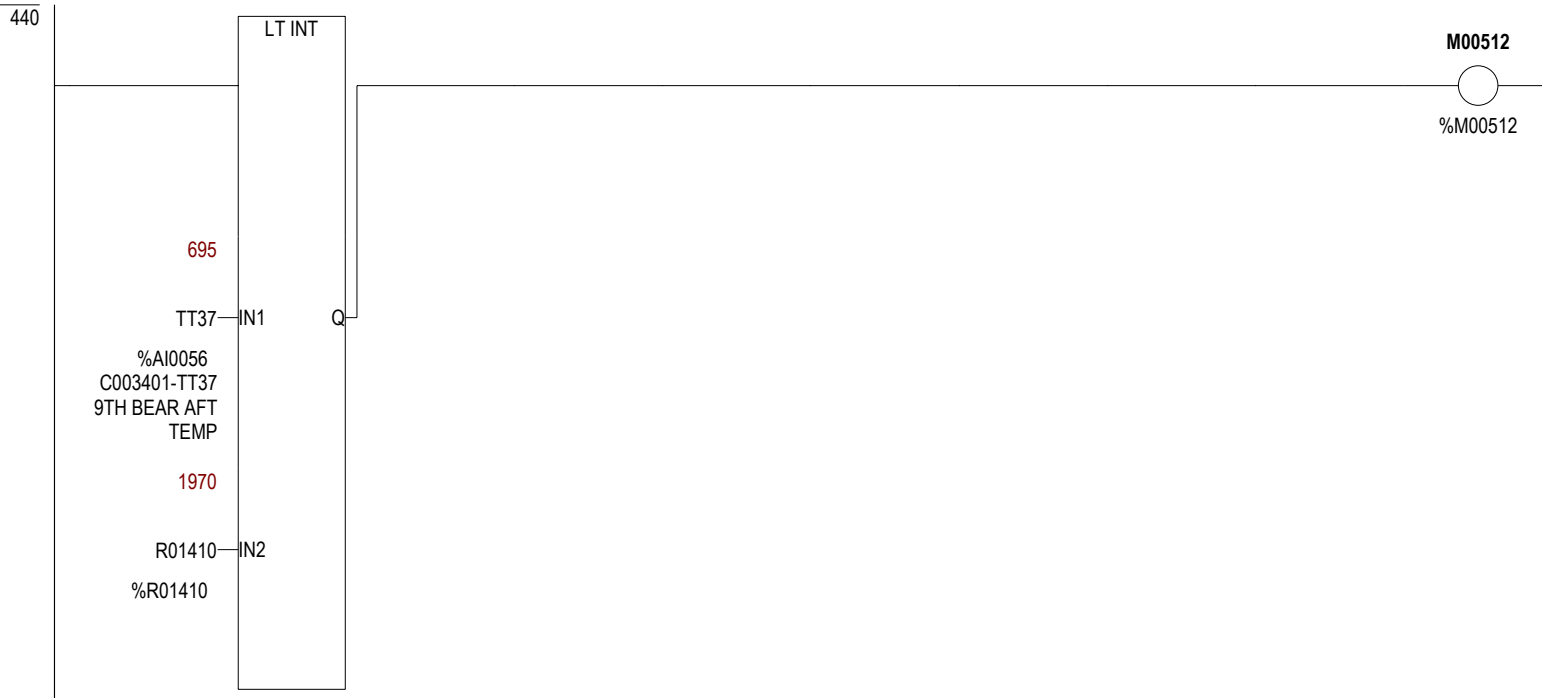
M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00369 %M00369
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LD Block,'SUMMARY': NCCON 00015;



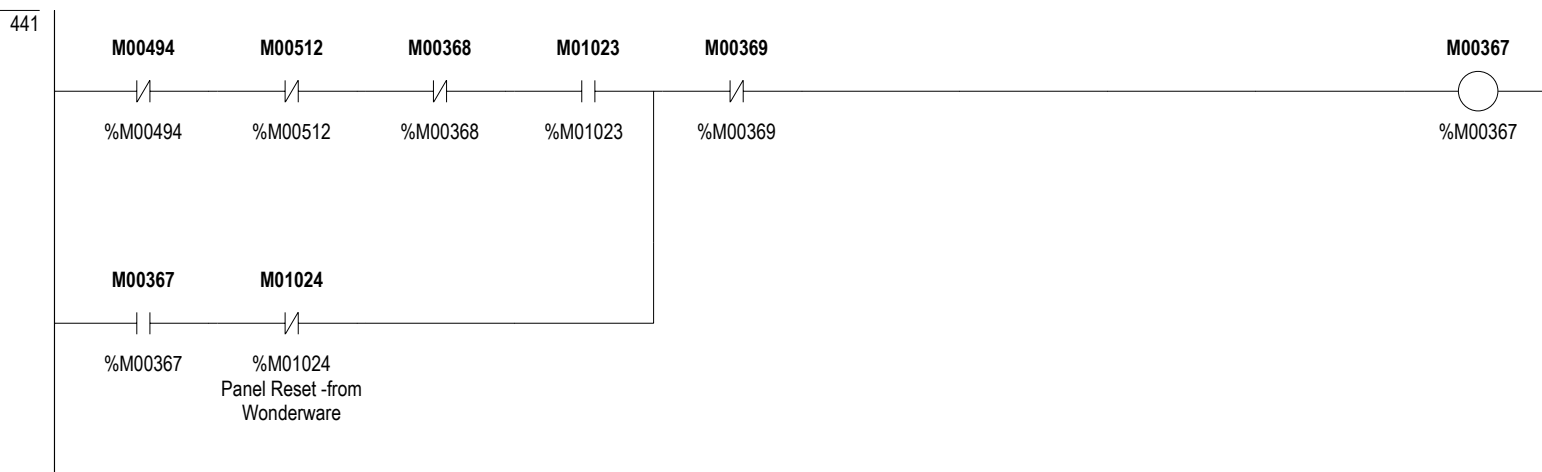
M00494 %M00494
LD Block,'FRSTOUT': NCCON 00441, 00442, 00443; COIL 00438;



M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00396 %M00396
LD Block,'FRSTOUT': NCCON 00443; NOCON 00439; COIL 00439;



M00512 %M00512
LD Block,'FRSTOUT': NCCON 00441, 00442, 00443; COIL 00440;



M00494 %M00494 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00438;

M00512 %M00512 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00440;

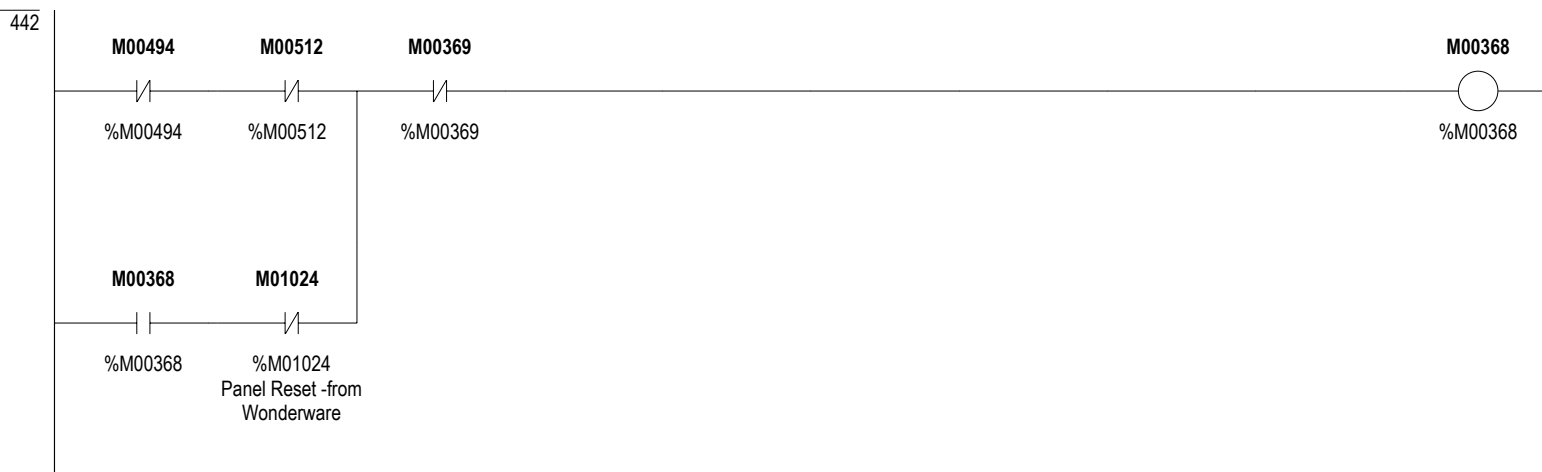
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LD Block,'FRSTOUT': COIL 00437;

M00367 %M00367

LD Block,'FRSTOUT': NOCON 00441; COIL 00441;

LD Block,'SUMMARY': NCCON 00035;



M00494 %M00494 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00438;

M00512 %M00512 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00440;

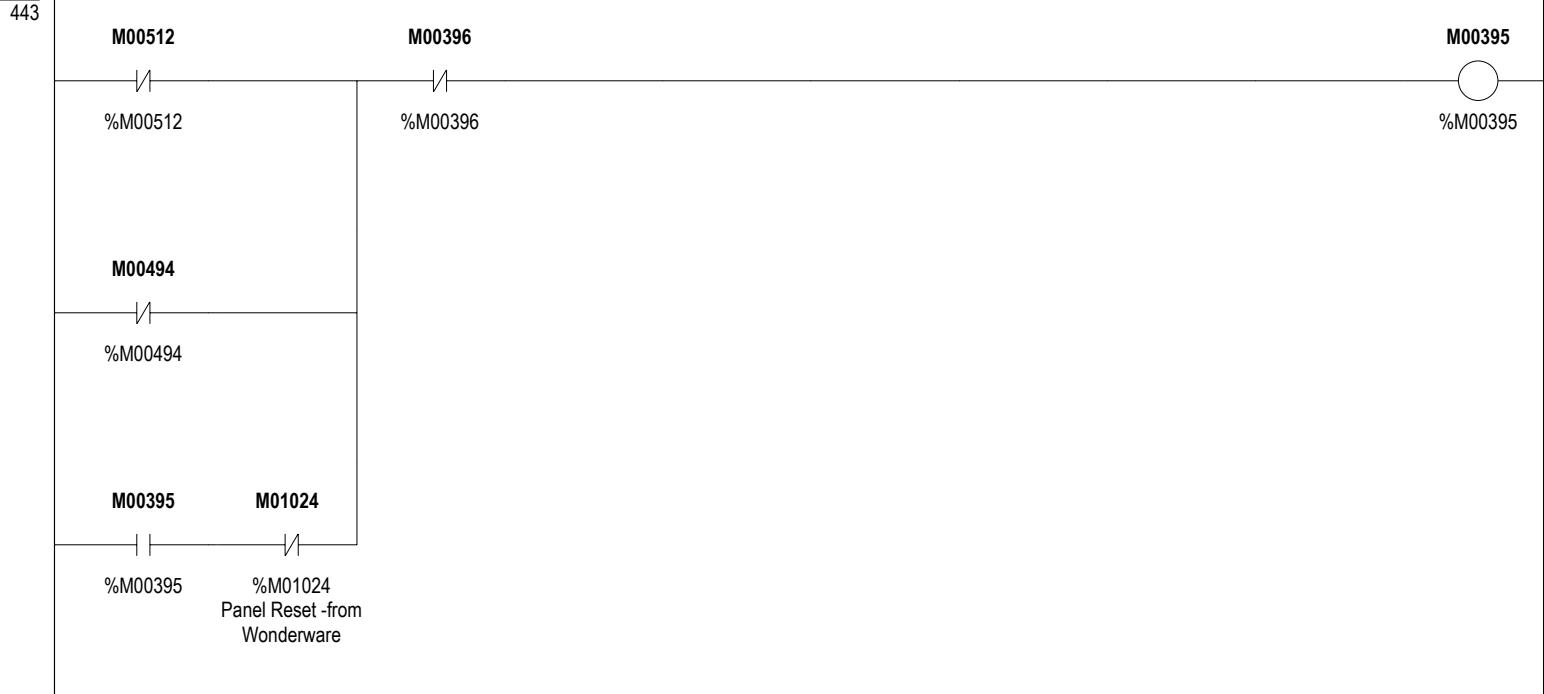
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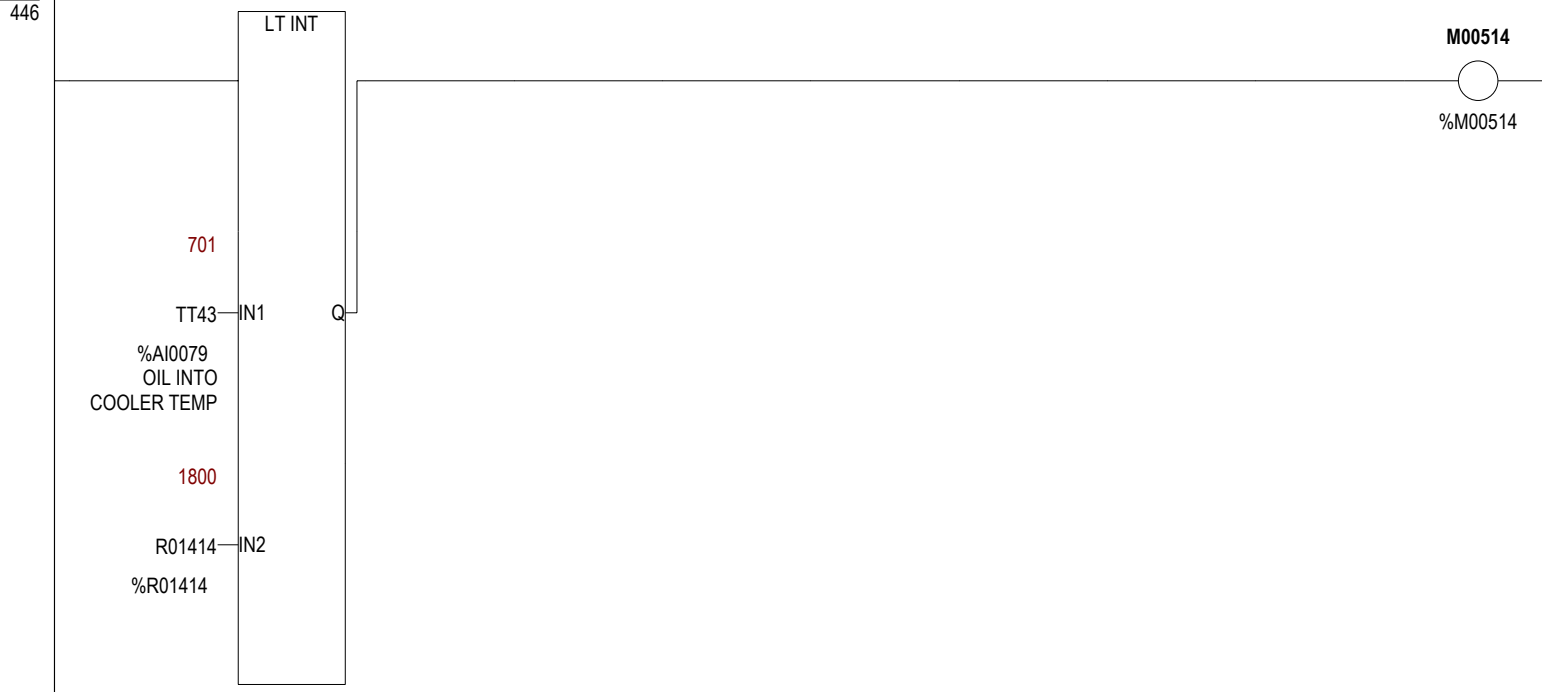
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M00368 %M00368

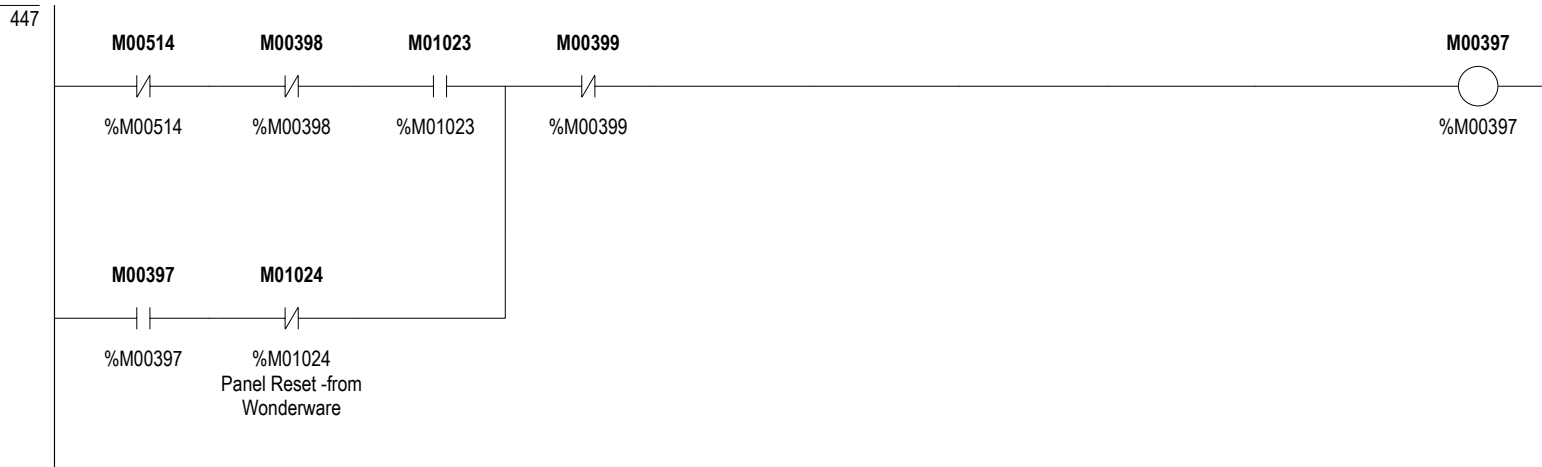
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LD Block,'SUMMARY': NOCON 00056;





M00514 %M00514
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M00514 %M00514 (Controlling Rung Reference)

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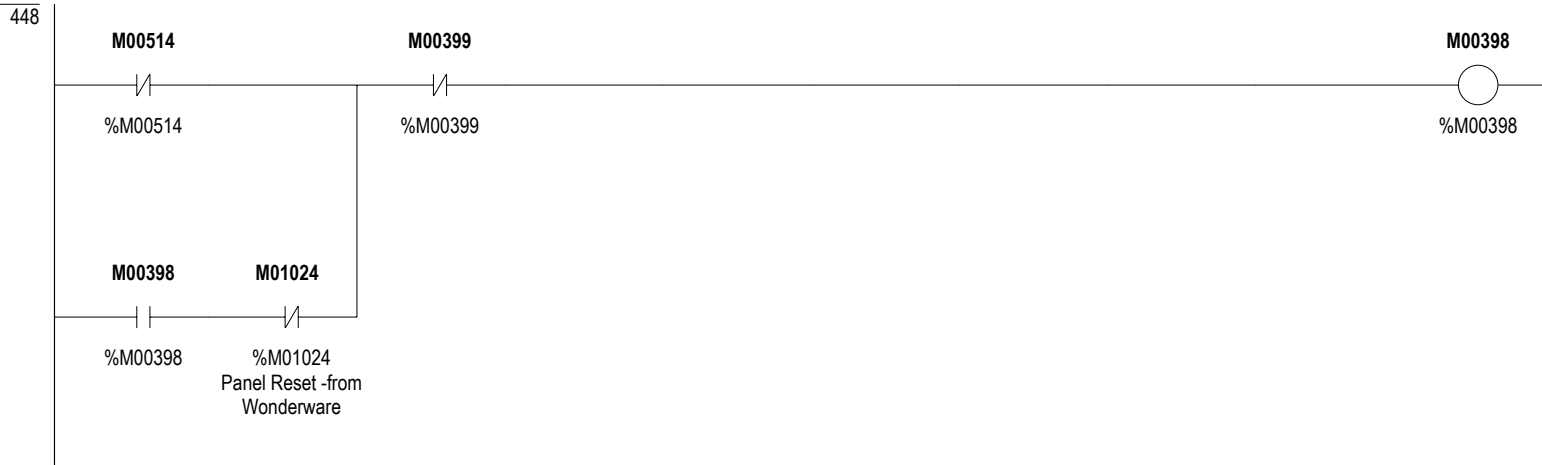
M00399 %M00399 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00445;

M00397 %M00397

LD Block,'FRSTOUT': NOCON 00447; COIL 00447;

LD Block,'SUMMARY': NCCON 00036;



M00514 %M00514 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00446;

M00399 %M00399 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00445;

M00398 %M00398

LD Block,'FRSTOUT': NCCON 00447; NOCON 00448; COIL 00448;

449
450

COMMENT19

M00528

M01022

M00528

||
%M00528

||
%M01022

○
%M00528

M00531

||
%M00531

M01022 %M01022 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00003;

M00528 %M00528

LD Block,'FRSTOUT': NCCON 00452, 00453; NOCON 00450; COIL 00450;

LD Block,'SUMMARY': NCCON 00016;

451

LT INT

M00529

○
%M00529

680

TT40-IN1

Q

%AI0028

C003401-TT40

AIR MANIF TEMP

1550

R01416-IN2

%R01416

M00529 %M00529

LD Block,'FRSTOUT': NCCON 00452, 00453; COIL 00451;

452

M00529

M00527

M01023

M00528

M00526

||
%M00529

||
%M00527

||
%M01023

||
%M00528

○
%M00526

M00526

M01024

||
%M00526

||
%M01024
Panel Reset -from
Wonderware

M00529 %M00529 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00451;

M00528 %M00528 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00450;

M00526 %M00526



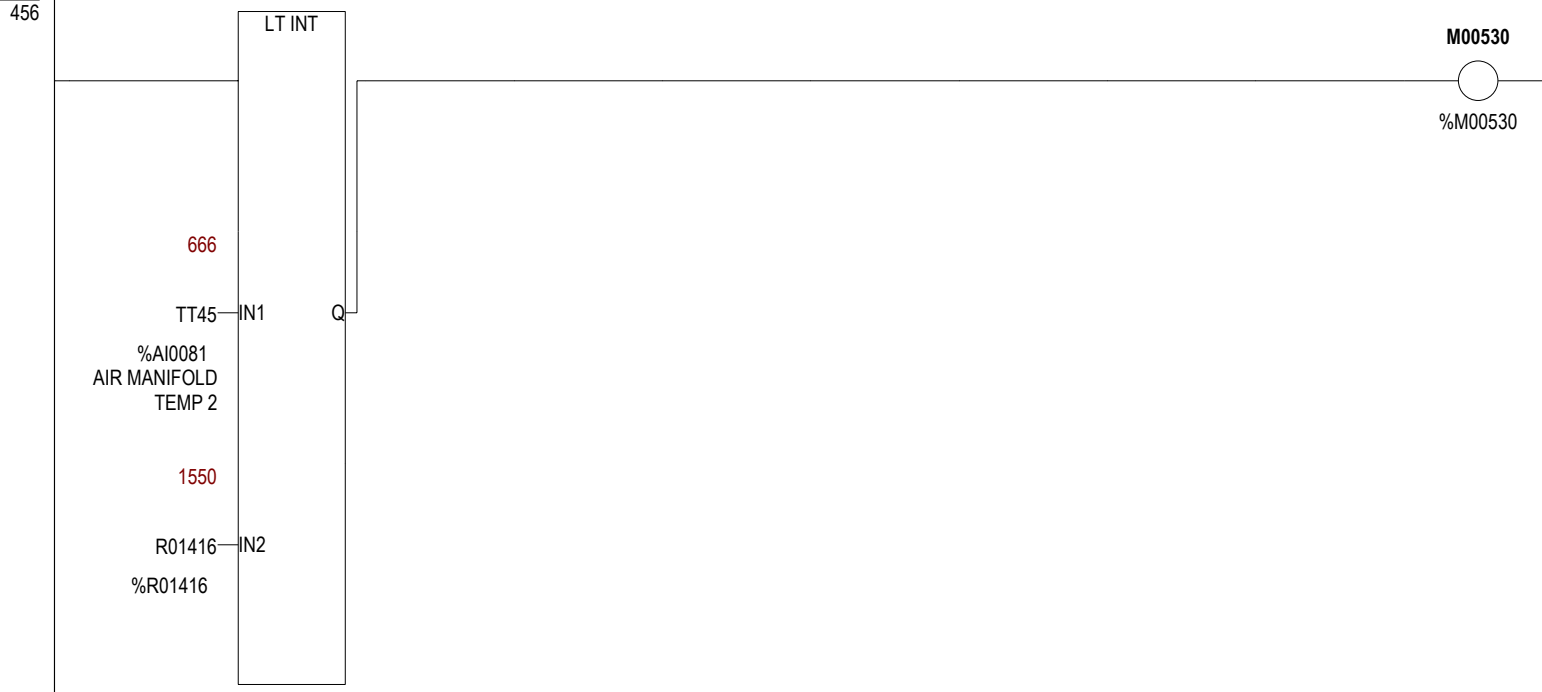
M00529 %M00529 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00451;
M00528 %M00528 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00450;
M00527 %M00527
LD Block,'FRSTOUT': NCCON 00452; NOCON 00453; COIL 00453;
LD Block,'SUMMARY': NOCON 00058;



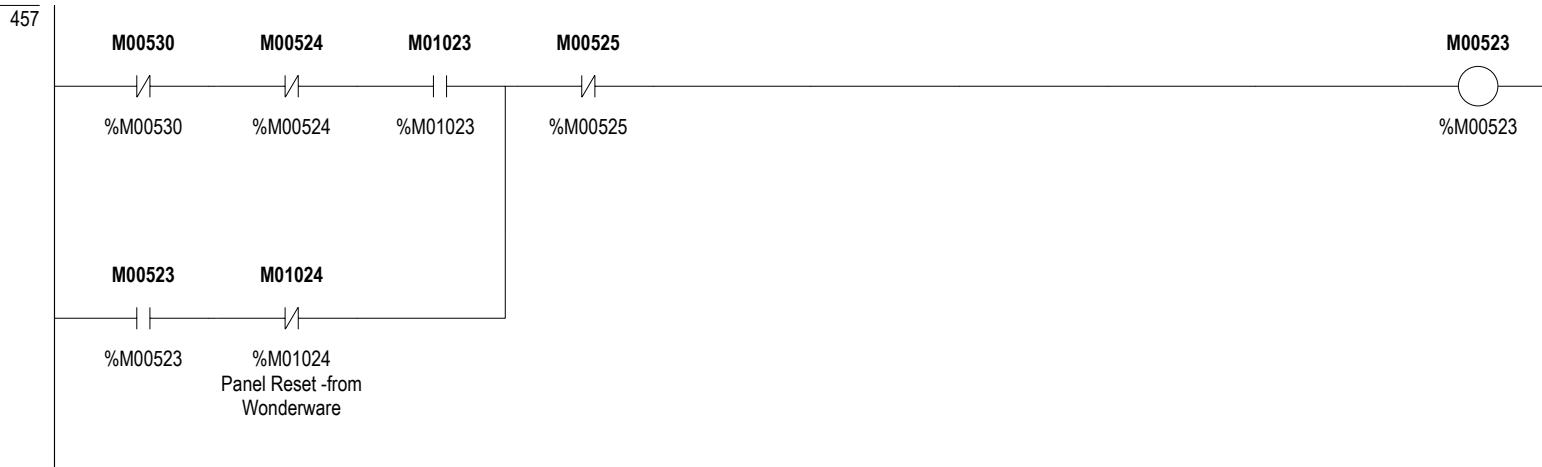
M00525

%M00525

M01022 %M01022 (Controlling Rung Reference)
LD Block,'FRSTOUT': COIL 00003;
M00525 %M00525
LD Block,'FRSTOUT': NCCON 00457, 00458; NOCON 00455; COIL 00455;
LD Block,'SUMMARY': NCCON 00016;



M00530 %M00530
LD Block,'FRSTOUT': NCCON 00457, 00458; COIL 00456;



M00530 %M00530 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00456;

M00525 %M00525 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00455;

M00523 %M00523

LD Block,'FRSTOUT': NOCON 00457; COIL 00457;

LD Block,'SUMMARY': NCCON 00036;



M00530 %M00530 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00456;

M00525 %M00525 (Controlling Rung Reference)

LD Block,'FRSTOUT': COIL 00455;

M00524 %M00524

LD Block,'FRSTOUT': NCCON 00457; NOCON 00458; COIL 00458;

